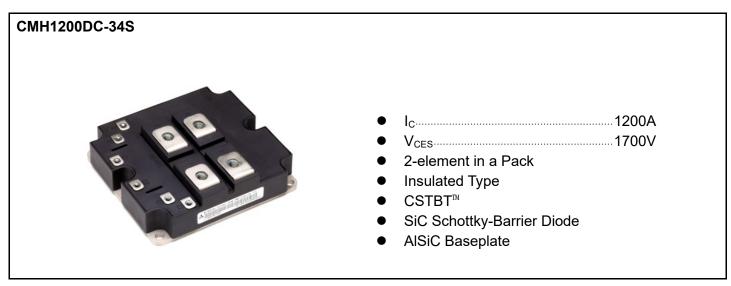


< HVIGBT MODULE >

CMH1200DC-34S

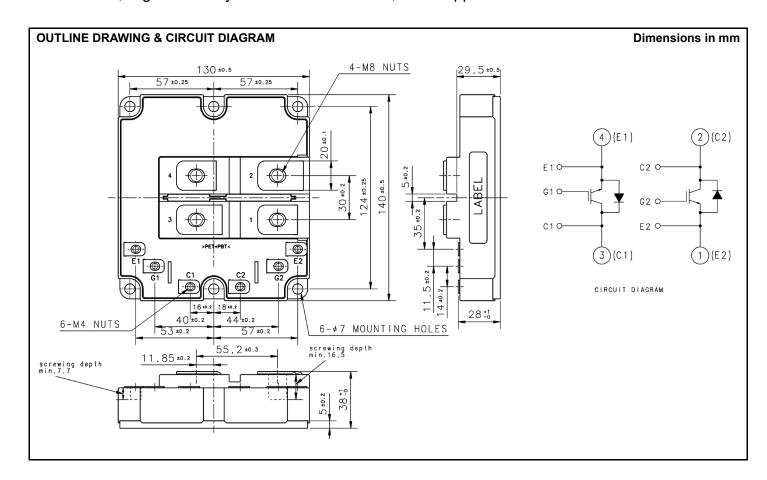
HIGH POWER SWITCHING USE INSULATED TYPE

SiC Hybrid HVIGBT (High Voltage Insulated Gate Bipolar Transistor) Module



APPLICATION

Traction drives, High Reliability Converters / Inverters, DC choppers



< HVIGBT MODULE >

CMH1200DC-34S

HIGH POWER SWITCHING USE

INSULATED TYPE

SiC Hybrid HVIGBT (High Voltage Insulated Gate Bipolar Transistor) Module

MAXIMUM RATINGS

Symbol	Item	Conditions	Ratings	Unit
V _{CES}	Collector-emitter voltage	V _{GE} = 0V	1700	V
V_{GES}	Gate-emitter voltage	$V_{CE} = 0V, T_j = 25^{\circ}C$	±20	V
Ic	Collector current	DC, T _c = 110°C	1200	Α
I _{CRM}	Collector current	Pulse (Note 1)	2400	Α
IE	Emitter current (Note 2)	DC	1200	Α
I _{ERM}	Emitter current (Note 2)	Pulse (Note 1)	2400	Α
l ² t	Surge current load integral	$T_j = 125$ °C, $V_R = 0V$, $t_p = 10$ ms	_	kA ² s
P _{tot}	Maximum power dissipation (Note 3)	T _c = 25°C, IGBT part	6750	W
V _{iso}	Isolation voltage	RMS, sinusoidal, f = 60Hz, t = 1min.	4000	V
T _{jop}	Operating junction temperature		−50 ~ +150	°C
T _{stg}	Storage temperature		−50 ~ +150	°C
t _{psc}	Short circuit pulse width	$V_{CC} = 1200 \text{ V}, V_{CE} \le V_{CES}, V_{GE} = 15 \text{V}, T_j = 150 ^{\circ}\text{C}$	10	μs

ELECTRICAL CHARACTERISTICS

Symbol	Itom	Item Conditions		Limits			Unit
Symbol	item			Min	Тур	Max	Offic
			T _j = 25°C	_	36	_	
I _{CES}	Collector cutoff current	$V_{CE} = V_{CES}, V_{GE} = 0V$	T _j = 125°C	_	150	_	mA
			$T_j = 150^{\circ}C$	_	180	_	
$V_{\text{GE(th)}}$	Gate-emitter threshold voltage	$V_{CE} = 10V, I_{C} = 120mA, T_{j} = 25^{\circ}C$		_	6.0	_	V
I _{GES}	Gate leakage current	$V_{GE} = V_{GES}$, $V_{CE} = 0V$, $T_j = 25$ °C		-0.5	_	0.5	μΑ
C _{ies}	Input capacitance	$V_{CF} = 10V, V_{GF} = 0V, f = 100kHz$		_	216	_	nF
C _{oes}	Output capacitance	$T_i = 25^{\circ}C$		_	8.0	_	nF
C _{res}	Reverse transfer capacitance	1, - 25 0			1.6	_	nF
Q_G	Total gate charge	V_{CC} = 850V, I_C = 1200A, V_{GE} = 15V		_	12.0	_	μC
		I _C = 1200 A (Note 4)	$T_j = 25^{\circ}C$	_	1.95	_	
V_{CEsat}	Collector-emitter saturation voltage	$I_C = 1200 \text{ A}$ (Note 4) $V_{GE} = 15 \text{ V}$	T _j = 125°C	_	2.25	_	V
		V _{GE} - 13 V	T _j = 150°C		2.30	_	
			T _j = 25°C	_	0.50	_	μs
$t_{d(on)}$	Turn-on delay time		T _j = 125°C	_	0.50	_	
			T _i = 150°C	_	0.50	_	
		V _{cc} = 850 V	T _i = 25°C	_	0.14	_	
t _r	Turn-on rise time	I _C = 1200 A	T _i = 125°C	_	0.15	_	μs
		V _{GE} = ±15 V	T _i = 150°C	_	0.15	_	
		R _{G(on)} = 1.3 Ω	T _i = 25°C	_	110	_	
E _{on(10%)}	Turn-on switching energy (Note 6)	L _s = 100 nH	T _i = 125°C	_	135	_	mJ
	3 3,	Inductive load T _i = 15		_	140	_	
	Turn-on switching energy (Note 5)		T _i = 25°C	_	130	_	mJ
Eon			T _i = 125°C	_	155	_	
			T _i = 150°C	_	160	_	
			T _i = 25°C	_	1.20	_	
$t_{d(off)}$	Turn-off delay time		T _i = 125°C	_	1.30	_	μs
=(=::)			T _i = 150°C	_	1.32	_	·
		V _{CC} = 850 V	T _i = 25°C	_	0.12	_	
t _f	Turn-off fall time	I _C = 1200 A	T _i = 125°C	_	0.15	_	μs
		$V_{GE} = \pm 15 \text{ V}$	T _i = 150°C	_	0.17	_	
		$R_{G(off)} = 3.3 \Omega$	T _i = 25°C	_	200	_	
E _{off(10%)}	Turn-off switching energy (Note 6)	L _s = 100 nH	T _i = 125°C	_	280	_	mJ
5(1570)		Inductive load	T _i = 150°C	_	310	_	
			T _i = 25°C	_	260	_	
E _{off}	Turn-off switching energy (Note 5)		T _i = 125°C	_	360	_	mJ
L off	Turn-on switching energy		T _i = 150°C	_	400	_	-

< HVIGBT MODULE >

CMH1200DC-34S

HIGH POWER SWITCHING USE

INSULATED TYPE

SiC Hybrid HVIGBT (High Voltage Insulated Gate Bipolar Transistor) Module

THERMAL CHARACTERISTICS

Cumbal	ol Item Conditions			Limits			Unit
Symbol			Min	Тур	Max	Offit	
V _{EC}	Emitter-collector voltage (Note 2)	$I_E = 1200A \qquad \qquad \text{(Note 4)}$ $V_{GE} = 0V$	T _j = 25°C	_	1.60	1	V
			T _j = 125°C	_	2.20		
			T _j = 150°C	_	2.30		
	Total capacitive charge (Note 2,7)	V_{CC} = 850V, I_{E} = 1200 A $R_{G(on)}$ = 1.3 Ω , L_{s} = 100 nH	T _j = 25°C	_	5.0		μC
Q_{C}			T _j = 125°C	_	8.5	_	
			T _j = 150°C	_	9.0		
$R_{th(j-c)Q}$	Thermal resistance	Junction to Case, IGBT part, 1/2 module		_		18.5	K/kW
$R_{th(j-c)D}$	Thermal resistance	Junction to Case, FWDi part, 1/2 module		_	_	36.0	K/kW
R _{th(c-s)}	Contact thermal resistance	Case to heat sink, 1/2 module λ_{grease} = 1W/m*k, $D_{(c-s)}$ = 100 μ m			16.0	10.0	12/13/8/
					16.0		K/kW

MECHANICAL CHARACTERISTICS

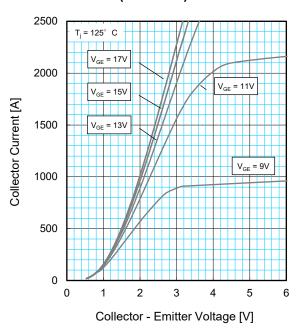
Comple ed	ltem	Conditions	Limits			1.1:4
Symbol			Min	Тур	Max	Unit
M_t		Main terminals screw	7.0		20.0	N·m
Ms	Mounting torque	Mounting screw	3.0		6.0	N·m
M _t		Auxiliary terminals screw	1.0	-	3.0	N·m
m	Mass			8.0	-	kg
CTI	Comparative tracking index		600		1	_
da	Clearance		9.5	1	1	mm
d _s	Creepage distance		15.0		-	mm
L _{P CE}	Parasitic stray inductance	1/2 module		30.0	-	nH
R _{CC'+EE'}	Internal lead resistance	T _c = 25°C, 1/2 module		0.28		mΩ

Note1. Pulse width and repetition rate should be such that junction temperature (T_j) does not exceed T_{jopmax} rating.

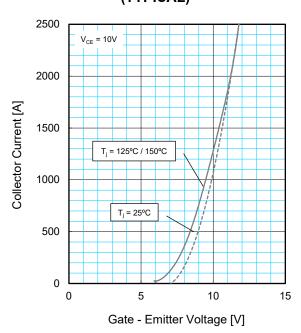
- 2. The symbols represent characteristics of the anti-parallel, emitter to collector free-wheel diode (FWDi).
- 3. Junction temperature (T_j) should not exceed T_{jmax} rating.
- Pulse width and repetition rate should be such as to cause negligible temperature rise.
- 5. Definition of all items is according to IEC 60747, unless otherwise specified.
- 6. $E_{on(10\%)}$ / $E_{off(10\%)}$ / $E_{rec(10\%)}$ are the integral of 0.1 V_{CE} x 0.1 I_C x dt.
- 7. Capacitive charge during anti-paralleled FWDi's turn-off operation.

PERFORMANCE CURVES

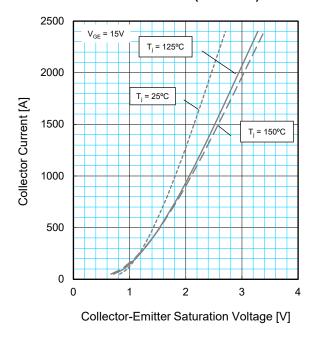
OUTPUT CHARACTERISTICS (TYPICAL)



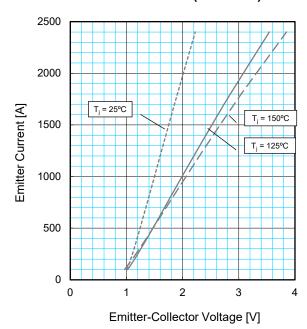
TRANSFER CHARACTERISTICS (TYPICAL)



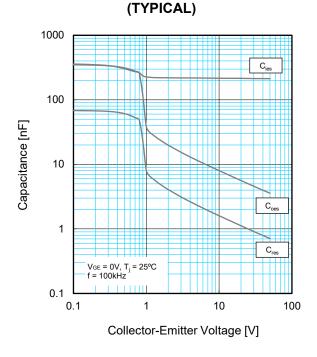
COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)



FREE-WHEEL DIODE FORWARD CHARACTERISTICS (TYPICAL)

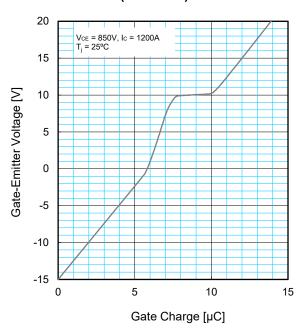


CAPACITANCE CHARACTERISTICS

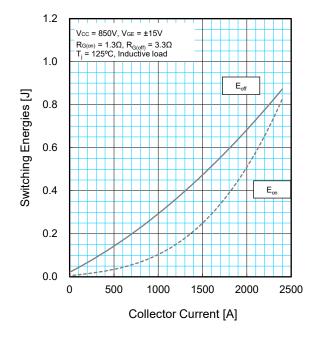


GATE CHARGE CHARACTERISTICS (TYPICAL)

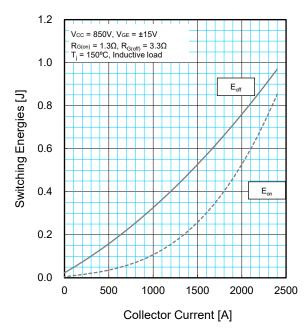
SiC Hybrid HVIGBT (High Voltage Insulated Gate Bipolar Transistor) Module



HALF-BRIDGE SWITCHING ENERGY CHARACTERISTICS (TYPICAL)

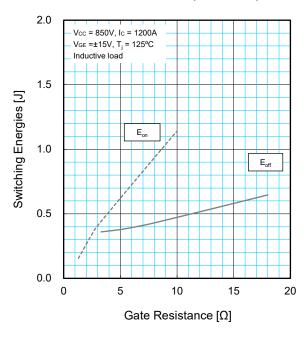


HALF-BRIDGE SWITCHING ENERGY CHARACTERISTICS (TYPICAL)

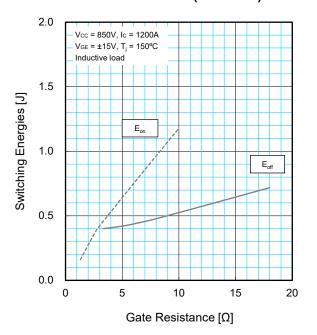


PERFORMANCE CURVES

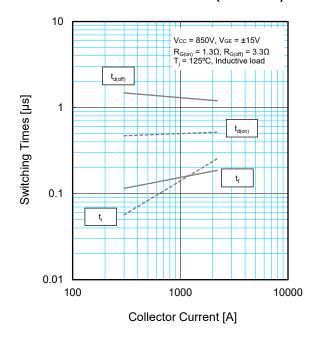
HALF-BRIDGE SWITCHING ENERGY CHARACTERISTICS (TYPICAL)



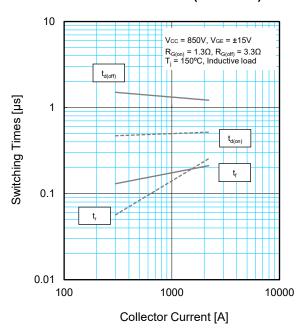
HALF-BRIDGE SWITCHING ENERGY CHARACTERISTICS (TYPICAL)



HALF-BRIDGE SWITCHING TIME CHARACTERISTICS (TYPICAL)

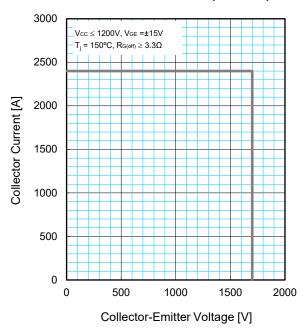


HALF-BRIDGE SWITCHING TIME CHARACTERISTICS (TYPICAL)

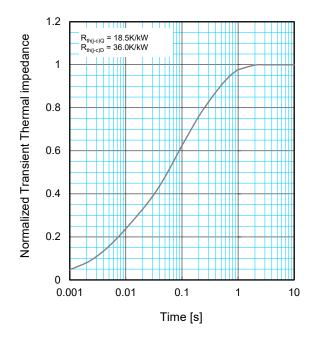


PERFORMANCE CURVES

REVERSE BIAS SAFE OPERATING AREA (RBSOA)



TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS



$Z_{th(j-c)}(t) = \sum_{i=1}^{n} R_i \left\{ 1 - exp^{\left(-\frac{t}{\tau_i}\right)} \right\}$							
	1	2	3	4			
R _i [K/kW]	0.0096	0.1893	0.4044	0.3967			
t _i [sec]	0.0001	0.0058	0.0602	0.3512			

HIGH POWER SWITCHING USE

INSULATED TYPE

SiC Hybrid HVIGBT (High Voltage Insulated Gate Bipolar Transistor) Module

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