

# **Programmable Controller**

# MELSEC iQ-R

# MELSEC iQ-R Flexible High-Speed I/O Control Module User's Manual (Startup)

-RD40PD01

# SAFETY PRECAUTIONS

(Read these precautions before using this product.)

Before using this product, please read this manual and the relevant manuals carefully and pay full attention to safety to handle the product correctly.

The precautions given in this manual are concerned with this product only. For the safety precautions of the programmable controller system, refer to the MELSEC iQ-R Module Configuration Manual.

In this manual, the safety precautions are classified into two levels: " / WARNING" and " / CAUTION".

| Indicates that incorrect handling may cause hazardous conditions, resulting in death or severe injury.                      |
|---|
|   |
| Indicates that incorrect handling may cause hazardous conditions, resulting in minor or moderate injury or property damage. |

Under some circumstances, failure to observe the precautions given under "A CAUTION" may lead to serious consequences.

Observe the precautions of both levels because they are important for personal and system safety.

Make sure that the end users read this manual and then keep the manual in a safe place for future reference.

## [Design Precautions]

- Configure safety circuits external to the programmable controller to ensure that the entire system operates safely even when a fault occurs in the external power supply or the programmable controller.
   Failure to do so may result in an accident due to an incorrect output or malfunction.
  - (1) Emergency stop circuits, protection circuits, and protective interlock circuits for conflicting operations (such as forward/reverse rotations or upper/lower limit positioning) must be configured external to the programmable controller.
  - (2) When the programmable controller detects an abnormal condition, it stops the operation and all outputs are:
    - Turned off if the overcurrent or overvoltage protection of the power supply module is activated.
    - Held or turned off according to the parameter setting if the self-diagnostic function of the CPU module detects an error such as a watchdog timer error.
  - (3) All outputs may be turned on if an error occurs in a part, such as an I/O control part, where the CPU module cannot detect any error. To ensure safety operation in such a case, provide a safety mechanism or a fail-safe circuit external to the programmable controller. For a fail-safe circuit example, refer to "General Safety Requirements" in the MELSEC iQ-R Module Configuration Manual.
  - (4) Outputs may remain on or off due to a failure of a component such as a relay and transistor in an output circuit. Configure an external circuit for monitoring output signals that could cause a serious accident.

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- In an output circuit, when a load current exceeding the rated current or an overcurrent caused by a load short-circuit flows for a long time, it may cause smoke and fire. To prevent this, configure an external safety circuit, such as a fuse.
- Configure a circuit so that the programmable controller is turned on first and then the external power supply. If the external power supply is turned on first, an accident may occur due to an incorrect output or malfunction.
- For the operating status of each station after a communication failure, refer to manuals relevant to the network. Incorrect output or malfunction due to a communication failure may result in an accident.
- When connecting an external device with a CPU module or intelligent function module to modify data of a running programmable controller, configure an interlock circuit in the program to ensure that the entire system will always operate safely. For other forms of control (such as program modification, parameter change, forced output, or operating status change) of a running programmable controller, read the relevant manuals carefully and ensure that the operation is safe before proceeding. Improper operation may damage machines or cause accidents.
- Especially, when a remote programmable controller is controlled by an external device, immediate action cannot be taken if a problem occurs in the programmable controller due to a communication failure. To prevent this, configure an interlock circuit in the program, and determine corrective actions to be taken between the external device and CPU module in case of a communication failure.
- Do not write any data to the "system area" and "write-protect area" of the buffer memory in the module. Also, do not use any "use prohibited" signals as an output signal from the CPU module to each module. Doing so may cause malfunction of the programmable controller system. For the "system area", "write-protect area", and the "use prohibited" signals, refer to the user's manual for the module used.
- If a communication cable is disconnected, the network may be unstable, resulting in a communication failure of multiple stations. Configure an interlock circuit in the program to ensure that the entire system will always operate safely even if communications fail. Failure to do so may result in an accident due to an incorrect output or malfunction.
- To maintain the safety of the programmable controller system against unauthorized access from external devices via the network, take appropriate measures. To maintain the safety against unauthorized access via the Internet, take measures such as installing a firewall.
- Outputs may remain on or off due to a failure of a transistor for external output. Configure an external circuit for monitoring output signals that could cause a serious accident.
- When changing data and operating status of the running module from an external device such as a personal computer connected, configure an interlock circuit external to the programmable controller to ensure that the entire system always operates safely.

In addition, before performing online operations, determine corrective actions to be taken between the external device and the module in case of a communication failure due to poor contact of cables.

### [Design Precautions]

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- During control of an inductive load such as a lamp, heater, or solenoid valve, a large current (approximately ten times greater than normal) may flow when the output is turned from off to on. Therefore, use a module that has a sufficient current rating.
- After the CPU module is powered on or is reset, the time taken to enter the RUN status varies depending on the system configuration, parameter settings, and/or program size. Design circuits so that the entire system will always operate safely, regardless of the time.
- Do not power off the programmable controller or reset the CPU module while the settings are being written. Doing so will make the data in the flash ROM undefined. The values need to be set in the buffer memory and written to the flash ROM again. Doing so also may cause malfunction or failure of the module.
- When changing the operating status of the CPU module from external devices (such as the remote RUN/STOP functions), select "Do Not Open by Program" for "Opening Method" of "Module Parameter". If "Open by Program" is selected, an execution of the remote STOP function causes the communication line to close. Consequently, the CPU module cannot reopen the line, and external devices cannot execute the remote RUN function.
- Do not install the control lines or communication cables together with the main circuit lines or power cables. Keep a distance of 150mm or more between them. Failure to do so may result in malfunction due to noise.

### [Installation Precautions]

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• Shut off the external power supply (all phases) used in the system before mounting or removing the module. Failure to do so may result in electric shock or cause the module to fail or malfunction.

### [Installation Precautions]

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- Use the programmable controller in an environment that meets the general specifications in the Safety Guidelines included with the base unit. Failure to do so may result in electric shock, fire, malfunction, or damage to or deterioration of the product.
- To mount a module, place the concave part(s) located at the bottom onto the guide(s) of the base unit, and push in the module until the hook(s) located at the top snaps into place. Incorrect interconnection may cause malfunction, failure, or drop of the module.
- To mount a module with no module fixing hook, place the concave part(s) located at the bottom onto the guide(s) of the base unit, push in the module, and fix it with screw(s). Incorrect interconnection may cause malfunction, failure, or drop of the module.
- When using the programmable controller in an environment of frequent vibrations, fix the module with a screw.
- Tighten the screws within the specified torque range. Undertightening can cause drop of the screw, short circuit, or malfunction. Overtightening can damage the screw and/or module, resulting in drop, short circuit, or malfunction.
- When using an extension cable, connect it to the extension cable connector of the base unit securely. Check the connection for looseness. Poor contact may cause malfunction.
- When using an SD memory card, fully insert it into the SD memory card slot. Check that it is inserted completely. Poor contact may cause malfunction.
- Securely insert an extended SRAM cassette into the cassette connector of the CPU module. After insertion, close the cassette cover and check that the cassette is inserted completely. Poor contact may cause malfunction.
- Do not directly touch any conductive parts and electronic components of the module, SD memory card, extended SRAM cassette, or connector. Doing so can cause malfunction or failure of the module.

## [Wiring Precautions]

- Shut off the external power supply (all phases) used in the system before installation and wiring. Failure to do so may result in electric shock or cause the module to fail or malfunction.
- After installation and wiring, attach the included terminal cover to the module before turning it on for operation. Failure to do so may result in electric shock.
- When connecting a differential output terminal to a differential receiver of a drive unit, connect the high-speed output common terminal to the differential receiver common terminal of the drive unit.
   Failure to do so may cause the module to fail or malfunction because of the potential difference that occurs between the high-speed output common terminal and the differential receiver common terminal.

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- Individually ground the FG and LG terminals of the programmable controller with a ground resistance of 100 ohms or less. Failure to do so may result in electric shock or malfunction.
- Use applicable solderless terminals and tighten them within the specified torque range. If any spade solderless terminal is used, it may be disconnected when the terminal screw comes loose, resulting in failure.
- Check the rated voltage and signal layout before wiring to the module, and connect the cables correctly. Connecting a power supply with a different voltage rating or incorrect wiring may cause fire or failure.
- Connectors for external devices must be crimped or pressed with the tool specified by the manufacturer, or must be correctly soldered. Incomplete connections may cause short circuit, fire, or malfunction.
- Securely connect the connector to the module. Poor contact may cause malfunction.
- Place the cables in a duct or clamp them. If not, dangling cables may swing or inadvertently be pulled, resulting in malfunction or damage to modules or cables.

In addition, the weight of the cables may put stress on modules in an environment of strong vibrations and shocks.

Do not clamp the extension cables with the jacket stripped. Doing so may change the characteristics of the cables, resulting in malfunction.

- Check the interface type and correctly connect the cable. Incorrect wiring (connecting the cable to an incorrect interface) may cause failure of the module and external device.
- Tighten the terminal screws or connector screws within the specified torque range. Undertightening can cause drop of the screw, short circuit, fire, or malfunction. Overtightening can damage the screw and/or module, resulting in drop, short circuit, fire, or malfunction.
- When disconnecting the cable from the module, do not pull the cable by the cable part. For the cable with connector, hold the connector part of the cable. For the cable connected to the terminal block, loosen the terminal screw. Pulling the cable connected to the module may result in malfunction or damage to the module or cable.
- Prevent foreign matter such as dust or wire chips from entering the module. Such foreign matter can cause a fire, failure, or malfunction.
- A protective film is attached to the top of the module to prevent foreign matter, such as wire chips, from entering the module during wiring. Do not remove the film during wiring. Remove it for heat dissipation before system operation.
- Programmable controllers must be installed in control panels. Connect the main power supply to the power supply module in the control panel through a relay terminal block. Wiring and replacement of a power supply module must be performed by qualified maintenance personnel with knowledge of protection against electric shock. For wiring, refer to the MELSEC iQ-R Module Configuration Manual.
- For Ethernet cables to be used in the system, select the ones that meet the specifications in the user's manual for the module used. If not, normal data transmission is not guaranteed.
- Do not install the control lines or communication cables together with the main circuit lines or power cables. Keep a distance of 150mm or more between them. Failure to do so may result in malfunction due to noise.
- Ground the shield cable on the encoder side (relay box) with a ground resistance of 100 ohm or less. Failure to do so may cause malfunction.

### [Startup and Maintenance Precautions]

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- Do not touch any terminal while power is on. Doing so will cause electric shock or malfunction.
- Correctly connect the battery connector. Do not charge, disassemble, heat, short-circuit, solder, or throw the battery into the fire. Also, do not expose it to liquid or strong shock. Doing so will cause the battery to produce heat, explode, ignite, or leak, resulting in injury and fire.
- Shut off the external power supply (all phases) used in the system before cleaning the module or retightening the terminal screws, connector screws, or module fixing screws. Failure to do so may result in electric shock.

### [Startup and Maintenance Precautions]

- When connecting an external device with a CPU module or intelligent function module to modify data of a running programmable controller, configure an interlock circuit in the program to ensure that the entire system will always operate safely. For other forms of control (such as program modification, parameter change, forced output, or operating status change) of a running programmable controller, read the relevant manuals carefully and ensure that the operation is safe before proceeding. Improper operation may damage machines or cause accidents.
- Especially, when a remote programmable controller is controlled by an external device, immediate action cannot be taken if a problem occurs in the programmable controller due to a communication failure. To prevent this, configure an interlock circuit in the program, and determine corrective actions to be taken between the external device and CPU module in case of a communication failure.
- Do not disassemble or modify the modules. Doing so may cause failure, malfunction, injury, or a fire.
- Use any radio communication device such as a cellular phone or PHS (Personal Handy-phone System) more than 25cm away in all directions from the programmable controller. Failure to do so may cause malfunction.
- Shut off the external power supply (all phases) used in the system before mounting or removing the module. Failure to do so may cause the module to fail or malfunction.
- Tighten the screws within the specified torque range. Undertightening can cause drop of the component or wire, short circuit, or malfunction. Overtightening can damage the screw and/or module, resulting in drop, short circuit, or malfunction.
- After the first use of the product, do not mount/remove the module to/from the base unit, and the terminal block to/from the module, and do not insert/remove the extended SRAM cassette to/from the CPU module more than 50 times (IEC 61131-2 compliant) respectively. Exceeding the limit may cause malfunction.

### [Startup and Maintenance Precautions]

- After the first use of the product, do not insert/remove the SD memory card to/from the CPU module more than 500 times. Exceeding the limit may cause malfunction.
- Do not touch the metal terminals on the back side of the SD memory card. Doing so may cause malfunction or failure.
- Do not touch the integrated circuits on the circuit board of an extended SRAM cassette. Doing so may cause malfunction or failure of the module.
- Do not drop or apply shock to the battery to be installed in the module. Doing so may damage the battery, causing the battery fluid to leak inside the battery. If the battery is dropped or any shock is applied to it, dispose of it without using.
- Startup and maintenance of a control panel must be performed by qualified maintenance personnel with knowledge of protection against electric shock. Lock the control panel so that only qualified maintenance personnel can operate it.
- Before handling the module, touch a conducting object such as a grounded metal to discharge the static electricity from the human body. Failure to do so may cause the module to fail or malfunction.
- When performing online operations of the running module from an external device such as a personal computer connected, read the relevant manuals carefully and ensure that the operation is safe before proceeding.
- Before changing any setting of the module, read the relevant manuals carefully, ensure the safety, and change the operating status of the CPU module to STOP.
   Especially when operating the module in the network system, ensure the safety thoroughly because controlled machines are likely to be moved inadvertently. Improper operation may damage machines or cause accidents.

### [Operating Precautions]

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- When changing data and operating status, and modifying program of the running programmable controller from an external device such as a personal computer connected to an intelligent function module, read relevant manuals carefully and ensure the safety before operation. Incorrect change or modification may cause system malfunction, damage to the machines, or accidents.
- Do not power off the programmable controller or reset the CPU module while the setting values in the buffer memory are being written to the flash ROM in the module. Doing so will make the data in the flash ROM undefined. The values need to be set in the buffer memory and written to the flash ROM again. Doing so can cause malfunction or failure of the module.

### [Disposal Precautions]

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- When disposing of this product, treat it as industrial waste.
- When disposing of batteries, separate them from other wastes according to the local regulations. For details on battery regulations in EU member states, refer to the MELSEC iQ-R Module Configuration Manual.

### [Transportation Precautions]

- When transporting lithium batteries, follow the transportation regulations. For details on the regulated models, refer to the MELSEC iQ-R Module Configuration Manual.
- The halogens (such as fluorine, chlorine, bromine, and iodine), which are contained in a fumigant used for disinfection and pest control of wood packaging materials, may cause failure of the product. Prevent the entry of fumigant residues into the product or consider other methods (such as heat treatment) instead of fumigation. The disinfection and pest control measures must be applied to unprocessed raw wood.

# CONDITIONS OF USE FOR THE PRODUCT

(1) Mitsubishi programmable controller ("the PRODUCT") shall be used in conditions;

i) where any problem, fault or failure occurring in the PRODUCT, if any, shall not lead to any major or serious accident; and

ii) where the backup and fail-safe function are systematically or automatically provided outside of the PRODUCT for the case of any problem, fault or failure occurring in the PRODUCT.

(2) The PRODUCT has been designed and manufactured for the purpose of being used in general industries. MITSUBISHI SHALL HAVE NO RESPONSIBILITY OR LIABILITY (INCLUDING, BUT NOT LIMITED TO ANY AND ALL RESPONSIBILITY OR LIABILITY BASED ON CONTRACT, WARRANTY, TORT, PRODUCT LIABILITY) FOR ANY INJURY OR DEATH TO PERSONS OR LOSS OR DAMAGE TO PROPERTY CAUSED BY the PRODUCT THAT ARE OPERATED OR USED IN APPLICATION NOT INTENDED OR EXCLUDED BY INSTRUCTIONS, PRECAUTIONS, OR WARNING CONTAINED IN MITSUBISHI'S USER, INSTRUCTION AND/OR SAFETY MANUALS, TECHNICAL BULLETINS AND GUIDELINES FOR the PRODUCT.

("Prohibited Application")

Prohibited Applications include, but not limited to, the use of the PRODUCT in;

- Nuclear Power Plants and any other power plants operated by Power companies, and/or any other cases in which the public could be affected if any problem or fault occurs in the PRODUCT.
- Railway companies or Public service purposes, and/or any other cases in which establishment of a special quality assurance system is required by the Purchaser or End User.
- Aircraft or Aerospace, Medical applications, Train equipment, transport equipment such as Elevator and Escalator, Incineration and Fuel devices, Vehicles, Manned transportation, Equipment for Recreation and Amusement, and Safety devices, handling of Nuclear or Hazardous Materials or Chemicals, Mining and Drilling, and/or other applications where there is a significant risk of injury to the public or property.

Notwithstanding the above restrictions, Mitsubishi may in its sole discretion, authorize use of the PRODUCT in one or more of the Prohibited Applications, provided that the usage of the PRODUCT is limited only for the specific applications agreed to by Mitsubishi and provided further that no special quality assurance or fail-safe, redundant or other safety features which exceed the general specifications of the PRODUCTs are required. For details, please contact the Mitsubishi representative in your region.

# INTRODUCTION

Thank you for purchasing the Mitsubishi Electric MELSEC iQ-R series programmable controllers.

This manual describes the specifications, procedures before operation, wiring, and programming of the relevant product listed below.

Before using this product, please read this manual and the relevant manuals carefully and develop familiarity with the functions and performance of the MELSEC iQ-R series programmable controller to handle the product correctly.

When applying the program examples provided in this manual to an actual system, ensure the applicability and confirm that it will not cause system control problems.

Please make sure that the end users read this manual.

Point P

Unless otherwise specified, this manual provides program examples in which the I/O numbers of X/Y0 to X/ Y1F are assigned to the flexible high-speed I/O control module. Assign I/O numbers when applying the program examples to an actual system. For I/O number assignment, refer to the following.

### Relevant product

RD40PD01

# COMPLIANCE WITH EMC AND LOW VOLTAGE DIRECTIVES

### Method of ensuring compliance

To ensure that Mitsubishi Electric programmable controllers maintain EMC and Low Voltage Directives when incorporated into other machinery or equipment, certain measures may be necessary. Please refer to one of the following manuals.

- D MELSEC iQ-R Module Configuration Manual
- D Safety Guidelines (This manual is included with the base unit.)

The CE mark on the side of the programmable controller indicates compliance with EMC and Low Voltage Directives.

### Additional measures

To ensure that this product maintains EMC and Low Voltage Directives, please refer to one of the following manuals.

- D MELSEC iQ-R Module Configuration Manual
- D Safety Guidelines (This manual is included with the base unit.)

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# **RELEVANT MANUALS**

| Manual name [manual number]  | Description   | Available form  |
|--|---|-----------------|
| MELSEC iQ-R Flexible High-Speed I/O Control  |   |                 |
| Module User's Manual (Startup)<br>[SH-081645ENG] (this manual)                                     | operation examples of the flexible high-speed I/O control module  | e-Manual<br>PDF |
| MELSEC iQ-R Module Configuration Manual  | Module Configuration Manual describes common information on the hardware  | Print book      |
| [SH-081262ENG]   | configuration of all modules, overview of each system configuration, and specifications of the power supply module, base unit, SD memory card, and battery. | e-Manual<br>PDF |
| MELSEC iQ-R Flexible High-Speed I/O Control  | Functions, parameter settings, I/O signals, buffer memory, and troubleshooting  | Print book      |
| Module User's Manual (Application)<br>[SH-081647ENG]   | of the flexible high-speed I/O control module, and details on the configuration tool  | e-Manual<br>PDF |
| MELSEC iQ-R Flexible High-Speed I/O Control<br>Module Function Block Reference<br>[BCN-P5999-0684] | Function blocks of the flexible high-speed I/O control module   | e-Manual<br>PDF |
| GX Works3 Operating Manual<br>[SH-081215ENG]   | System configuration, parameter settings, and online operations of GX Works3  | e-Manual<br>PDF |
| MELSEC iQ-R Inter-Module Synchronization<br>Function Reference Manual<br>[SH-081401ENG]            | Inter-module synchronization function, which controls multiple modules synchronously  | e-Manual<br>PDF |
| MELSEC iQ-R Online Module Change Manual  | Online module change, which allows a module to be changed without stopping  | Print book      |
| [SH-081501ENG]   | the system for MELSEC iQ-R series programmable controllers  | e-Manual<br>PDF |

This manual does not include detailed information on the following:

- General specifications
- · Applicable combinations of CPU modules and the other modules, and the number of mountable modules
- Installation

For details, refer to the following.

MELSEC iQ-R Module Configuration Manual

This manual does not include information on the module function blocks.

For details, refer to the Function Block Reference for the module used.

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e-Manual refers to the Mitsubishi Electric FA electronic book manuals that can be browsed using a dedicated tool.

e-Manual has the following features:

- Required information can be cross-searched in multiple manuals.
- Other manuals can be accessed from the links in the manual.
- The hardware specifications of each part can be found from the product figures.
- Pages that users often browse can be bookmarked.
- Sample programs can be copied to an engineering tool.

# TERMS

Unless otherwise specified, this manual uses the following terms.

| Term                 | Description   |
|----------------------|---|
| Buffer memory        | Memory in an intelligent function module to store data such as setting values and monitor values. For CPU modules, it refers to memory to store data such as setting values and monitor values of the Ethernet function, or data used for data communication of the multiple CPU system function.       |
| Engineering tool     | A tool used for setting up programmable controllers, programming, debugging, and maintenance  |
| Flash ROM            | A non-volatile memory to which the hardware logic can be written. Although the number of writes to a flash ROM is limited, the written hardware logic is not deleted even after the power off and automatically read at the power-on.   |
| Global label         | A label that is valid for all the program data when multiple program data are created in the project. There are two types of global label: a module specific label (module label), which is generated automatically by GX Works3, and an optional label, which can be created for any specified device. |
| Hardware logic       | A control logic that users create graphically combining inputs, outputs, logical operation circuits, and counters with the configuration tool   |
| Module label         | A label that represents one of memory areas (I/O signals and buffer memory areas) specific to each module in a given character string. For the module used, GX Works3 automatically generates this label, which can be used as a global label.  |
| Watchdog timer error | A watchdog timer is a timer to monitor whether internal processing of a module is performed normally. A watchdog timer error is an error that occurs when internal processing is not performed normally.  |

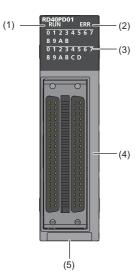
# **GENERIC TERMS AND ABBREVIATIONS**

Unless otherwise specified, this manual uses the following generic terms and abbreviations.

| Generic term/abbreviation              | Description   |
|--|---|
| Configuration tool                     | The abbreviation for the configuration tool for flexible high-speed I/O control modules |
| Flexible high-speed I/O control module | The abbreviation for the MELSEC iQ-R series flexible high-speed I/O control module      |

# **1** PART NAMES

This chapter lists the part names of the flexible high-speed I/O control module.



| No. | Name                                     | Description  |
|-----|--|--|
| (1) | RUN LED                                  | This LED indicates the operating status.         On: Normal operation         Flashing (1s-cycle): During simulation         Flashing (400ms-cycle): When a module is selected for an online module change         Off: When 5V power off, a watchdog timer error has occurred, or module replacement allowed in the process of the online module change |
| (2) | ERR LED                                  | This LED indicates the error status.<br>On: An error has occurred.<br>Off: Normal operation  |
| (3) | Indicator LED                            | This LED indicates the I/O status of external I/O terminals.   |
|     | Upper section                            | <ul> <li>This LED indicates the input status of each external input terminal.</li> <li>On: A voltage has been applied.</li> <li>Off: No voltage has been applied.</li> <li>0 to B indicate external input terminals (IN 0 to IN B).</li> </ul>   |
|     | Lower section                            | <ul> <li>This LED indicates the output status of each external output terminal.<sup>*1</sup></li> <li>On: A signal output is on.</li> <li>Off: A signal output is off.</li> <li>0 to D indicate the following external output terminals.</li> <li>0 to 7: OUT 0 to OUT 7</li> <li>8 to D: OUT 0_DIF to OUT 5_DIF</li> </ul>                              |
| (4) | Connector for external devices (40 pins) | A connector used to connect encoders and control devices. For the terminal layout, refer to the following.   |
| (5) | Production information marking           | The marking shows the production information (16 digits) of the module.  |

\*1 For differential output terminals, the LED indicates the ON/OFF status of each differential output+ signal.

# **2** SPECIFICATIONS

This chapter describes performance specifications.

# **2.1** Performance Specifications

The following table lists the performance specifications of the flexible high-speed I/O control module.

| Item                    | Specifications                          |                                   |
|-------------------------|---|-----------------------------------|
|                         | Differential                            | DC                                |
| Number of input points  | 12 points (common to 5VDC/24VDC/differ  | ential)                           |
| Number of output points | 6 points                                | 8 points (5 to 24VDC, 0.1A/point) |
| Number of interrupts    | 8 points                                |                                   |
| Input response time     | 1μs or less                             |                                   |
| Output response time    | 1µs or less                             |                                   |
| Pulse input speed       | Max. 8Mpps (2MHz) Max. 200kpps (200kHz) |                                   |
| Pulse output speed      | Max. 8Mpps (2MHz) Max. 200kpps (200kHz) |                                   |

| ltem                     |                              |  |                                       | Specifications  |  |  |
|--------------------------|------------------------------|--|---------------------------------------|---|--|--|
|                          |                              |  |                                       | Differential  | DC   |  |
| Main block <sup>*1</sup> | External input               | Logic Select                                       |                                       | Inverted, not inverted  |  |  |
|                          | block                        | Filter Time  |                                       | General-purpose<br>input:   | 0μs, 10μs, 50μs, 0.1ms, 0.2ms, 0.4ms, 0.6ms, 1ms, 5ms  |  |
|                          |                              |  |                                       | Pulse input:  | 10kpps, 100kpps, 200kpps, 500kpps, 1000kpps, 2000kpps, 4000kpps, 8000kpps  |  |
|                          |                              | Initial State                                      |                                       | Low, High   | 1  |  |
|                          | Inter-module syn             | Inter-module synchronization signal input terminal |                                       |   | Outputs the fixed scan clock synchronized with the inter-module synchronization cycle as a signal.   |  |
|                          | Y device termina             | I  |                                       | Outputs the ON/OFI<br>Y1F) as signals.  | F states of General command 0 to General command F (Y10 to   |  |
|                          | OUT terminal                 |  |                                       | Outputs the same si   | ignal as the one to be output from the external output block.  |  |
|                          | Parallel                     | Input Data Type                                    |                                       | Pure binary, Gray co  | ode, BCD   |  |
|                          | encoder block                | Data Length  |                                       |   |  |  |
|                          | SSI encoder                  | Input Data Type                                    |                                       | Pure binary, Gray co  | ode  |  |
|                          | block                        | Data Length  |                                       |   |  |  |
|                          | Multi function counter block | Terminal   |                                       | 1 bit to 32 bits<br>Input terminal, latch input terminal, event input terminal, output terminal, event output<br>terminal, cam switch output terminal, pattern generator output terminal              |  |  |
|                          |                              | Input signal event detection block                 |                                       | Combination of rise, fall, Low, and High  |  |  |
|                          |                              | Latch event detection block                        |                                       | Rise, fall  |  |  |
|                          |                              | Counter timer<br>block                             | Туре                                  | Addition, subtraction, linear counter mode, ring counter mode, addition mode, prese<br>counter function, latch counter function, internal clock function  |  |  |
|                          |                              |  | Internal clock                        | 25ns, 50ns, 0.1µs, 1µs, 10µs, 100µs, 1ms  |  |  |
|                          |                              |  | Counting range                        | 32-bit signed binary value (-2147483648 to 2147483647)<br>32-bit unsigned binary value (0 to 4294967295)<br>16-bit signed binary value (-32768 to 32767)<br>16-bit unsigned binary value (0 to 65535) |  |  |
|                          |                              | Compare block                                      | Compare Value                         | Same as the counting range  |  |  |
|                          |                              |  | Compare Mode                          | 16-bit counter: =, >, <, ≥, ≤, <><br>32-bit counter: =, >, <, ≥, ≤, <>  |  |  |
|                          |                              | Pattern<br>generator block                         | Number of<br>output pattern<br>points | 8192 points   |  |  |
|                          |                              | Cam switch<br>block                                | Refreshing<br>cycle                   | 0.1µs   |  |  |
|                          |                              |  | Number of steps                       | Up to 16 steps  |  |  |
|                          |                              | Set/reset block                                    |                                       | Uses the signal input to the Set terminal as a trigger to output the High fixed signal.<br>Uses the signal input to the Reset terminal as a trigger to output the Low fixed signal                    |  |  |
|                          | Logical operation block      | Logical operation type                             |                                       | AND, OR, XOR  |  |  |
|                          | External output              | Logic Select                                       | Logic Select                          |   | d  |  |
|                          | block                        | Delay Time   |                                       |   | $_0$ 64), 25ns $\times$ (1 to 64), 50ns $\times$ (1 to 64), 0.1µs $\times$ (1 to 64), 1µs $\times$ (1 64), 100µs $\times$ (1 to 64), 1ms $\times$ (1 to 64), inter-module le |  |
|                          |                              | Error-time Output Mode                             |                                       | OFF, ON, HOLD   |  |  |
|                          | SI device termina            | al   |                                       | Interrupt to a CPU module   |  |  |

| Item                            |                               |                                   |                   | Specifications   |  |  |
|---------------------------------|-------------------------------|-----------------------------------|-------------------|--|--|--|
|                                 |                               |                                   |                   | Differential   | DC   |  |
| Main functions<br>that can be   | Pulse count                   | Count input signal                | Phase             | 1-phase input (1 multiple/2 multiples), 2-p<br>multiples), CW/CCW  | hase input (1 multiple/2 multiples/4               |  |
| performed with the              |                               | Counting speed                    | 1 multiple        | 10kpps/100kpps/200kpps/ 500kpps/<br>1Mpps/2Mpps  | 10kpps/100kpps/200kpps                             |  |
| combination of main blocks      |                               |                                   | 2 multiples       | 10kpps/100kpps/200kpps/500kpps/<br>1Mpps/2Mpps/4Mpps   | -  |  |
|                                 |                               |                                   | 4 multiples       | 10kpps/100kpps/200kpps/500kpps/<br>1Mpps/2Mpps/4Mpps/8Mpps   | -  |  |
|                                 |                               | Counting range                    |                   | Same as the counter timer block  |  |  |
|                                 |                               | Туре                              |                   | Addition, subtraction, linear counter mode counter function, latch counter function  | e, ring counter mode, addition mode, preset        |  |
|                                 |                               | Minimum count  <br>ratio: 50%)    | oulse width (duty | 1-phase input (1 multiple/2 multiples),<br>CW/CCW  | 1-phase input (1 multiple/2 multiples),<br>CW/CCW  |  |
|                                 |                               |                                   |                   | 0.5µs  | 5µs<br>2.5µs<br>2.5µs                              |  |
|                                 |                               |                                   |                   | 2-phase input (1 multiple/2 multiples/4 multiples)   | 2-phase input (1 multiple/2 multiples/4 multiples) |  |
|                                 |                               |                                   |                   | 0.5µs<br>0.25µs<br>0.25µs<br>0.25µs<br>0.125µs   | 20μs<br>10μs<br>10μs<br>5μs                        |  |
|                                 | Coincidence                   |                                   |                   |  | d binary value, 16-bit signed binary value,        |  |
|                                 | detection                     |                                   |                   | 16-bit unsigned binary value   |  |  |
|                                 |                               | Comparison met                    | hod               | Setting value < count value, setting value = count value, setting value > count value  |  |  |
|                                 |                               | Interrupt                         |                   | Coincidence detection interrupt function   |  |  |
|                                 | Cam switch<br>Highly-accurate | Number of steps<br>pulse output   |                   | Up to 16 steps/1 block<br>The ON/OFF timing can be adjusted in in-<br>trigger input as the starting point.   | crements of 25ns at a minimum using                |  |
|                                 | PWM output                    | Output frequenc                   | vrance            | Max. 2MHz  | Max. 200kHz  |  |
|                                 | output                        | Duty ratio                        | ,                 |  |  |  |
|                                 | Ratio setting                 | Ratio setting ran                 | ge                | Any value (Can be set in increments of 25ns at a minimum.) Number of output pulses = (1 to 2147483647)/(1 to 2147483647) × Number of pulses Note that a value obtained by dividing (1 to 2147483647) by (1 to 2147483647) should not exceed 1. |  |  |
|                                 | Pulse<br>measurement          | Measurement ite                   | em                | Pulse width (ON width, OFF width, from a falling edge to the next falling edge)  | a rising edge to the next rising edge, from a      |  |
|                                 |                               | Measurement re                    | solution          | 25ns   |  |  |
| Electrical interface conversion |                               | 24VDC/5VDC/differential           |                   |  |  |  |
| Processing time                 | of the main hardw             | are logic                         |                   | Logical operation: Min. 87.5ns, Coinciden<br>262.5ns   | ice output: Min. 137.5ns, Cam switch: Min.         |  |
| Continuous logg                 | ing function                  | Number of loggi                   | ng settings       | 1 (fixed)  |  |  |
|                                 |                               | Logging target                    |                   | IN 0 to IN B   |  |  |
|                                 |                               | Logging cycle                     |                   |  |  |  |
|                                 |                               | Logging cycle                     |                   | 1μs, 10μs, 100μs, 1000μs   |  |  |
| ADCValue loggir                 | ng function                   | Logging cycle<br>Number of loggin | ng settings       | 1μs, 10μs, 100μs, 1000μs<br>2 (No.1, No.2)   |  |  |
| ADCValue loggir                 | ng function                   |                                   | ng settings       |  | t module between CH1 and CH8.                      |  |

| Item  |                  | Specifications               | Specifications  |  |  |
|---|------------------|------------------------------|---|--|--|
|   |                  | Differential                 | DC  |  |  |
| Number of writes to a flash R               | ОМ               | Up to 10000 times            |   |  |  |
| Internal current consumption                | (5VDC)           | 0.76A                        | 0.76A   |  |  |
| Applicable wire size                        | 40-pin connector |                              | 0.088mm <sup>2</sup> to 0.3mm <sup>2</sup> (28 to 22 AWG) (When the A6CON1 or A6CON4 is used)<br>0.088mm <sup>2</sup> to 0.24mm <sup>2</sup> (28 to 24 AWG) (When the A6CON2 is used) |  |  |
| External wiring connector (sold separately) |                  | A6CON1, A6CON2, A6CON        | A6CON1, A6CON2, A6CON4  |  |  |
| Number of occupied I/O points               |                  | 32 points (I/O assignment: I | 32 points (I/O assignment: Intelligent, 32 points)  |  |  |
| External dimensions Height                  |                  | 106mm                        | 106mm   |  |  |
|   | Width            | 27.8mm                       | 27.8mm  |  |  |
|   | Depth            | 110mm                        |   |  |  |
| Weight                                      |                  | 0.16kg                       | 0.16kg  |  |  |

\*1 The basic blocks supplied by the configuration tool

\*2 When Enable is set to either No.1 ADCValue logging enable/disable or No.2 ADCValue logging enable/disable.

\*3 When Enable is set to both No.1 ADCValue logging enable/disable and No.2 ADCValue logging enable/disable.

# **3** FUNCTION LIST

The following table lists the functions of the flexible high-speed I/O control module. For details on the functions, refer to the following.

| MELSEC iQ-R Flexible High-Speed I/O Control Module User's Manual (Application) |
|--|
|--|

| Item                                  | Description   |
|---------------------------------------|---|
| Hardware logic control function       | Users can create the hardware logic to perform a desired control with the configuration tool.   |
| Continuous logging function           | The function collects the status of the Output terminal of the external input blocks (IN 0 to IN B) continuously at a specified interval.   |
| ADCValue logging function             | When the flexible high-speed I/O control module and an analog input module are mounted on the same base unit, this function logs A/D conversion values of the analog input module according to output timings of a multi function counter block.  |
| Interrupt function                    | <ul> <li>This function notifies the CPU module that either of following conditions is satisfied as an interrupt request.</li> <li>A signal is input to the SI device terminal of the hardware logic.</li> <li>The logging data for 5120 points is stored in Continuous logging data storage area (Un\G15020 to Un\G25259).</li> </ul> |
| Inter-module synchronization function | The function synchronizes the control timing of multiple modules for which the inter-module synchronization function are enabled with the inter-module synchronization cycle.   |
| Error history function                | The function stores up to 16 errors that occurred in the flexible high-speed I/O control module in the buffer memory as an error history.   |
| Event history function                | The function collects the errors that occurred in the flexible high-speed I/O control module and the operations that were performed in the module in the CPU module as event information.   |
| Hardware logic backup/restoration     | The flexible high-speed I/O control module can back up and restore the hardware logic written to a flash ROM.   |
| Online module change                  | The function changes a module without stopping the system. For the procedure of the online module change, refer to the following.   |
| Firmware update function              | The function enables users to update the firmware versions of modules by using firmware update files (For the firmware update file, please consult your local Mitsubishi representative.)<br>For details on the function, refer to the following.   |

### Precautions

To perform the firmware update, stop the hardware logic control and check that the output from the flexible high-speed I/O control module has stopped. Updating the firmware during the hardware logic control may cause unintended operation, resulting in system failure.

# **4** PROCEDURES BEFORE OPERATION

This chapter describes the procedures before operation.

**1.** Installing the module

Install the flexible high-speed I/O control module with a desired configuration.

2. External wiring

Wire external devices to the flexible high-speed I/O control module.

Page 27 External Wiring

3. Adding the module

Add the flexible high-speed I/O control module to the module configuration using an engineering tool. For details, refer to the following.

GX Works3 Operating Manual

4. Creating a hardware logic

Create a hardware logic with the configuration tool. For details, refer to the following.

MELSEC iQ-R Flexible High-Speed I/O Control Module User's Manual (Application)

**5.** Programming

Create a program. For details, check the following.

# **5** SYSTEM CONFIGURATION

For system configurations using the MELSEC iQ-R series modules, CPU modules that can be used with the flexible highspeed I/O control module, and the number of mountable modules, refer to the following. MELSEC iQ-R Module Configuration Manual

# **6** INSTALLATION AND EXTERNAL WIRING

This chapter describes the installation and external wiring of the flexible high-speed I/O control module.

# 6.1 External Wiring

This section describes wiring of encoders and controllers to the flexible high-speed I/O control module.

### **External wiring precautions**

To obtain the maximum performance from the functions of the flexible high-speed I/O control module and improve the system reliability, an external wiring with high durability against noise is required.

This section describes the precautions for wiring of encoders and controllers.

### Wiring

- Different terminals are prepared depending on the voltage of the signal to be input. Connecting to a terminal with an incorrect voltage may cause a malfunction of the module or failure of the connected devices.
- In 1-phase pulse input, always connect a pulse input cable on the phase A side.
- Install a fuse for each external terminal to prevent the external devices or module from being burnt out or damaged if a load shorts in an output circuit. The following fuses have been tested by Mitsubishi Electric Corporation.

| Fuse model name | Rated current | Contact            |
|-----------------|---------------|--------------------|
| 312.750         | 0.75A         | Littelfuse         |
| 216.800         | 0.8A          | www.littelfuse.com |

#### **Connector for external devices**

- Connectors for external devices must be soldered or crimped properly. A poor soldering or crimping may result in a malfunction.
- Securely connect the connectors for external devices to the connectors of the flexible high-speed I/O control module, and securely tighten the two screws.
- When disconnecting a cable from the flexible high-speed I/O control module, do not pull the cable holding the cable part. Remove a cable supporting the connector part of the cable by hand. Pulling the cable being connected to the flexible high-speed I/O control module can cause a malfunction. In addition, a damage of the flexible high-speed I/O control module or cables can result.

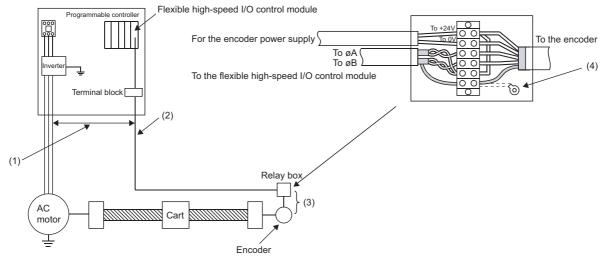
#### Measures to reduce noise

The flexible high-speed I/O control module may malfunction if pulse-like noise is input. Thus, take the following measures to reduce noise:

- · Always use a shielded twisted pair cable.
- Arrange a shielded twisted pair cable keeping a distance of 150mm or more from the power cable, I/O cables, or other cables that cause much noise. Wire the shielded twisted pair cable in the minimum distance to the extent possible.
- Ground a shield wire on the encoder side (relay box) with a ground resistance of 100 ohms or less.
- Do not wire terminals that are not to be used. Doing so may result in malfunction due to noise.

#### Wiring example for measures to reduce noise

The following figure shows a wiring example of when the measures to reduce noise are reflected to the actual system.

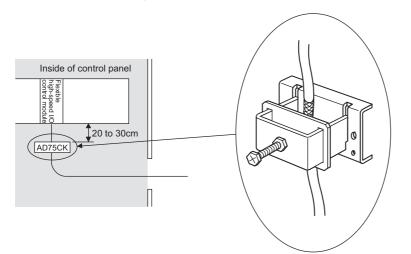


- (1) Keep a distance of 150mm or more from the I/O cables of high voltage devices including a relay and an inverter regardless of whether the devices are inside and outside the panel.
- (2) For metal pipes, do not use solenoid valves and inductive loads together in a single pipe. When the distance from high voltage cables cannot be kept because of duct wiring or other factors, use shield wires such as CVVS for the high voltage cables.
- (3) Keep the minimum distance between an encoder and a relay box. When the distance between the flexible high-speed I/O control module and an encoder is long, a voltage drop may occur. Check that the voltage while the encoder is operating and the voltage while the encoder is stopped are within the rated voltage range of the encoder using a measuring instrument such as a tester on the terminal block of the relay box. When a voltage drop is large, use a thicker wire or use the 24VDC encoder that consumes less current.
- (4) Connect the shield wires of the encoder and the shield wires of the shielded twisted pair cable inside the relay box. When the shield wires of the encoder in use are not grounded in the encoder, ground them inside the relay box as shown above.

### Compliance with the EMC and Low Voltage Directives

Take the following measures for compliance with the EMC and Low Voltage Directives.

- Always attach a ferrite core on the DC power supply cable to be connected to the flexible high-speed I/O control module and the one to be connected to a controller. Using the ESD-SR-250 ferrite core manufactured by TOKIN Corporation is recommended.
- Install a DC power and the module in the same control panel.
- Use a shielded cable for the DC power when the DC power supply cable is extended out of the control panel.
- The length of the cables to be connected to the output section and external devices must be 2m or shorter for open collector output or 10m or shorter for differential output.
- Keep the length of the cables between the input section and the external devices to 30m or less.
- Use a shielded twisted pair cable and ground the shielded part of the cable to the control panel with the AD75CK cable clamp manufactured by Mitsubishi Electric Corporation.



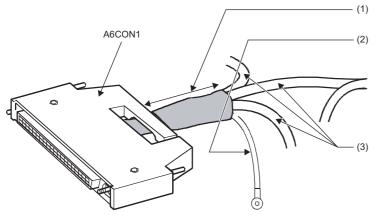
For details on the AD75CK, refer to the following.

AD75CK-type Cable Clamping Instruction Manual

• Take the following measures to reduce noise when wiring connectors for external devices.

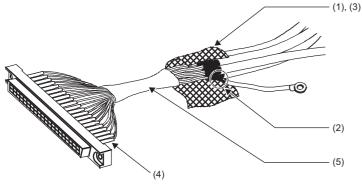
#### Wiring to use a shielded cable

The following figure shows a wiring example for measures to reduce noise using the A6CON1.



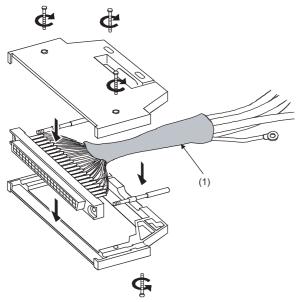
- (1) Shorten the distance between the connector and shielded cable as far as possible.
- (2) Ground a FG cable of 2mm or thicker in the minimum distance. Securely ground the cable to the control panel on the module side.
- (3) Shielded cables

#### How to process shielded cables



- (1) Remove the jacket of each shielded cable.
- (2) Take out a shield from a shielded cable and solder it on the FG wire.
- (3) Connect a shield of each shielded cable with a conductive tape.
- (4) Cover a connector pin with a heat-shrinkable insulation tube to protect signal wires. If signal wires are bared, the module may be affected by static electricity and malfunction.
- (5) Cover the signal wires with an insulating tape.

#### ■Assembling the A6CON1



(1) Cover the cables on which a conductive tape has been applied with a heat-shrinkable tube.

### Precautions

• Tighten the connector screws within the tightening torque range.

| Screw                  | Tightening torque range |
|------------------------|-------------------------|
| Connector screw (M2.6) | 0.20 to 0.29N·m         |

• Use copper wires having temperature rating of 75°C or more for the connectors.

• Use UL listed connectors if necessary for UL compliance.

#### Applicable connectors

Prepare connectors for external devices to be used with the flexible high-speed I/O control module by users. The following tables list the applicable connectors, and the reference product of a crimping tool.

#### ■40-pin connector

| Туре  | Model                | Applicable wire size                           |
|---|----------------------|--|
| Soldering type connector (Straight type)                        | A6CON1 <sup>*1</sup> | 0.088 to 0.3mm (28 to 22 AWG) (stranded wire)  |
| Crimping type connector (Straight type)                         | A6CON2               | 0.088 to 0.24mm (28 to 24 AWG) (stranded wire) |
| Soldering type connector (Dual purpose (straight/oblique) type) | A6CON4 <sup>*1</sup> | 0.088 to 0.3mm (28 to 22 AWG) (stranded wire)  |

\*1 When using 40 wires, use wires whose sheath outside diameter is 1.3mm or less. Select the wire applicable to the current value to be used.

Point P

The A6CON3 (IDC type connector (Straight type)) cannot be used.

#### ■40-pin connector crimping tool

| Туре          | Model           | Contact                         |  |
|---------------|-----------------|---------------------------------|--|
| Crimping tool | FCN-363T-T005/H | FUJITSU COMPONENT LIMITED       |  |
|               |                 | www.fujitsu.com/jp/group/fcl/en |  |

For how to wire connectors and how to use the crimping tool, contact FUJITSU COMPONENT LIMITED.

#### Wiring method, installation procedure, disconnection procedure

For the wiring method, installation procedure, and disconnection procedure, refer to the following. MELSEC iQ-R Module Configuration Manual

### Interface with external devices

The following table shows the interface of the flexible high-speed I/O control module with external devices.

### Internal circuit of the interface for external devices

The following table lists the internal circuits of the interface for external devices of the flexible high-speed I/O control module.

| I/O            | Internal circuit                   |   | Connector |                                    | Signal name                        |                                    |
|----------------|------------------------------------|---|-----------|------------------------------------|------------------------------------|------------------------------------|
| classification |                                    |   | CON1      | CON2                               | CON1                               | CON2                               |
| Input          |                                    | IN 0_DIF(B18)<br>IN 6_DIF(B18)                            | B19       | B19                                | High-speed input 0<br>24VDC        | High-speed input 6<br>24VDC        |
|                |                                    |   | A19       | A19                                | High-speed input 0<br>5VDC         | High-speed input 6<br>5VDC         |
|                |                                    | IN 6_5V(A19)<br>O<br>IN 0_24V(B19)                        | B18       | B18                                | High-speed input 0<br>differential | High-speed input 6<br>differential |
|                | 240Ω 270Ω 4.1KΩ<br>820Ω            | IN 6_24V(B19)<br>O<br>IN 0_COM(A18)<br>IN 6_COM(A18)<br>O | A18       | A18                                | High-speed input 0<br>common       | High-speed input 6<br>common       |
|                |                                    | IN 1_DIF(B16)<br>IN 7_DIF(B16)                            | B17       | B17                                | High-speed input 1<br>24VDC        | High-speed input 7<br>24VDC        |
|                |                                    |   | A17       | A17                                | High-speed input 1<br>5VDC         | High-speed input 7<br>5VDC         |
|                |                                    | IN 7_5V(A17)<br>O<br>IN 1_24V(B17)                        | B16       | B16                                | High-speed input 1<br>differential | High-speed input 7<br>differential |
|                | 240Ω 270Ω 4.1kΩ<br>820Ω            | IN 7_24V(B17)<br>o<br>IN 1_COM(A16)<br>IN 7_COM(A16)<br>o | A16       | A16                                | High-speed input 1<br>common       | High-speed input 7<br>common       |
|                |                                    | IN 2_DIF(B14)<br>IN 8_DIF(B14)                            | B15       | B15                                | High-speed input 2<br>24VDC        | High-speed input 8<br>24VDC        |
|                |                                    | IN 2_5V(A15)  | A15       | A15                                | High-speed input 2<br>5VDC         | High-speed input 8<br>5VDC         |
|                | IN 8_5V(A15)<br>O<br>IN 2_24V(B15) | B14   | B14       | High-speed input 2<br>differential | High-speed input 8<br>differential |                                    |
|                | 240Ω 270Ω 4.1kΩ<br>820Ω            |   | A14       | A14                                | High-speed input 2<br>common       | High-speed input 8<br>common       |
|                |                                    | IN 3_DIF(B12)<br>IN 9_DIF(B12)                            | B13       | B13                                | High-speed input 3<br>24VDC        | High-speed input 9<br>24VDC        |
|                |                                    | O<br>IN 3_5V(A13)<br>IN 9_5V(A13)<br>O<br>IN 3_24V(B13)   | A13       | A13                                | High-speed input 3<br>5VDC         | High-speed input 9<br>5VDC         |
|                |                                    |   | B12       | B12                                | High-speed input 3<br>differential | High-speed input 9<br>differential |
|                | 240Ω 270Ω 4.1kΩ<br>1820Ω IN        | IN 9_24V(B13)<br>o<br>IN 3_COM(A12)<br>IN 9_COM(A12)<br>o | A12       | A12                                | High-speed input 3<br>common       | High-speed input 9<br>common       |
|                |                                    | IN 4_DIF(B10)<br>IN A_DIF(B10)                            | B11       | B11                                | High-speed input 4<br>24VDC        | High-speed input A 24VDC           |
|                |                                    | 0<br>IN 4_5V(A11)   | A11       | A11                                | High-speed input 4<br>5VDC         | High-speed input A<br>5VDC         |
| Þ              | IN 4_24V(B11)                      |   | B10       | B10                                | High-speed input 4<br>differential | High-speed input A differential    |
|                | 240Ω 270Ω 4.1kΩ<br>820Ω            | IN A_24V(B11)<br>O<br>IN 4_COM(A10)<br>IN A_COM(A10)<br>O | A10       | A10                                | High-speed input 4 common          | High-speed input A common          |

| I/O            | Internal circuit  |    | tor  | Signal name                            |  |
|----------------|---|----|------|--|--|
| classification |   |    | CON2 | CON1 CON2                              |  |
| nput           | IN 5_DIF(B8)<br>IN B_DIF(B8)  | В9 | В9   | High-speed input 5<br>24VDC            | High-speed input B<br>24VDC            |
|                | o<br>IN 5_5V(A9)  | A9 | A9   | High-speed input 5<br>5VDC             | High-speed input B<br>5VDC             |
|                | IN B_5V(A9)<br>O<br>IN 5_24V(B9)  | B8 | B8   | High-speed input 5<br>differential     | High-speed input B differential        |
|                | $\begin{array}{c} \text{IN } \text{B}\_24\text{V(B9)} \\ \text{O} $ | A8 | A8   | High-speed input 5<br>common           | High-speed input B common              |
| output         | OUT 0(B7)   | B7 | B7   | High-speed output 0                    | High-speed output 4                    |
|                | IO 5V OUT 4(B7)   | A7 | A7   | High-speed output 1                    | High-speed output 5                    |
|                |   | B6 | B6   | High-speed output 2                    | High-speed output 6                    |
|                |   | A6 | A6   | High-speed output 3                    | High-speed output 7                    |
|                |   | 85 | B5   | High-speed output 0-3<br>common        | High-speed output 4-7                  |
|                | IO 5V<br>OUT 0_DIF+(B4)<br>OUT 3_DIF+(B4)   | A5 | A5   | High-speed output common <sup>*1</sup> | High-speed output common <sup>*1</sup> |
|                | OUT 0_DIF-(A4)<br>OUT 3_DIF-(A4)  | B4 | B4   | High-speed output 0<br>differential+   | High-speed output 3<br>differential+   |
|                | A AOUT_DIF_GND(A5)'   | A4 | A4   | High-speed output 0<br>differential-   | High-speed output 3<br>differential-   |
|                |   | В3 | В3   | High-speed output 1<br>differential+   | High-speed output 4<br>differential+   |
|                | OUT 4_DIF+(B3)<br>OUT 1_DIF-(A3)<br>OUT 4_DIF-(A3)<br>OUT 4_DIF-(A3)<br>OUT 4_DIF_GND(A5) <sup>11</sup>   | A3 | A3   | High-speed output 1<br>differential-   | High-speed output 4<br>differential-   |
|                |   | B2 | B2   | High-speed output 2<br>differential+   | High-speed output 5<br>differential+   |
|                | OUT 5_DIF+(B2)<br>OUT 2_DIF-(A2)<br>OUT 5_DIF-(A2)<br>OUT 5_DIF-(A2)<br>OUT 5_DIF-(A2)<br>OUT 5_DIF-(A2)<br>OUT 5_DIF-(A2)  | A2 | A2   | High-speed output 2<br>differential-   | High-speed output 5<br>differential-   |

\*1 The high-speed output common (A5) is connected with the common wire of high-speed output 0 to 5 differential.

### Input signal status in the hardware logic (High/Low)

The input signal status (High/Low) in the hardware logic is determined depending on the input signals (ON/OFF) from external devices and logic selection.

| Logic selection <sup>*1</sup> | External wiring                     | High/Low state of the external input signal IN 0 observed from the flexible high-speed I/O control module <sup>*2</sup> |
|-------------------------------|-------------------------------------|---|
| Not inverted                  | When a voltage is not applied (OFF) | Low   |
|                               | When a voltage is applied (ON)      | High  |
| Inverted                      | When a voltage is not applied (OFF) | High  |
|                               | When a voltage is applied (ON)      | Low   |

\*1 Set the logic selection with the parameters of the external input block. For details, refer to the following.

\*2 The input signal status in the hardware logic is described as High and Low. For details, refer to the following.

#### Status of an output signal to external devices (ON/OFF)

The status of an output signal to external devices (ON/OFF) is determined depending on the status of Input terminal of the external output block (High/Low) and logic selection.

| Output type  | Logic selection*1*2   |   |  |
|--------------|---|---|--|
|              | Not inverted  | Inverted  |  |
| DC           | Input terminal Low ON OFF   | Input terminal High<br>Low<br>OUT Output ON<br>OFF                          |  |
| Differential | Input terminal High<br>Low<br>OUT_DIF +Output<br>Low<br>OUT_DIF -Output High<br>Low | OUT_DIF +Output High<br>OUT_DIF -Output High<br>OUT_DIF -Output High<br>Cow |  |

- \*1 Set the logic selection with the parameters of the external output block. For details, refer to the following.
- \*2 The output signal status in the hardware logic is described as High and Low. For details, refer to the following.

## Electrical specifications of external I/O signals

## ■External input signal

The following table shows the input specifications of the flexible high-speed I/O control module.

| Signal name        | Operation | Input voltage<br>(Guaranteed value) | Operating current | Response time <sup>*1</sup> |
|--------------------|-----------|-------------------------------------|-------------------|-----------------------------|
| 24VDC input        | On        | 21.6 to 26.4V                       | 4 to 6mA          | 1μs                         |
|                    | Off       | 5V or less                          | 1.0mA or less     |                             |
| 5VDC input         | On        | 4.5 to 5.5V                         | 4 to 8mA          | 1μs                         |
|                    | Off       | 2V or less                          | 1.0mA or less     |                             |
| Differential input | —         | Equivalent to AM26C32               | —                 | 1μs                         |

\*1 When the filter time is set to  $0\mu s$ 

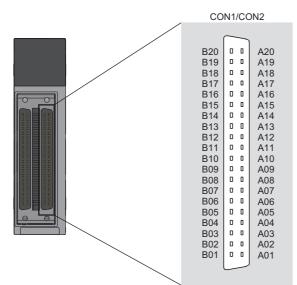
### External output signal

The following table shows the output specifications of the flexible high-speed I/O control module.

| Signal name         | Item                       |        | Description                              |
|---------------------|----------------------------|--------|--|
| DC output           | Operating load voltage     |        | 4.75 to 30VDC                            |
|                     | Maximum load current       |        | 0.1A/point                               |
|                     | Maximum voltage drop at ON |        | 0.5V                                     |
|                     | Response time              | OFF→ON | 1µs or less (Rated load, resistive load) |
|                     |                            | ON→OFF | 1µs or less (Rated load, resistive load) |
| Differential output | Operating load voltage     |        | Equivalent to AM26C31                    |
|                     | Maximum load current       |        |  |
|                     | Maximum voltage drop at ON |        |  |
|                     | Response time              |        | 1µs or less                              |

## Signal layout of a connector for external devices

The following shows the signal layout of a connector for external devices of the flexible high-speed I/O control module.



| Pin No. | CON1        |             | CON2        |             |  |
|---------|-------------|-------------|-------------|-------------|--|
|         | В           | A           | В           | Α           |  |
| 20      | NC          | NC          | NC          | NC          |  |
| 19      | IN 0_24V    | IN 0_5V     | IN 6_24V    | IN 6_5V     |  |
| 18      | IN 0_DIF    | IN 0_COM    | IN 6_DIF    | IN 6_COM    |  |
| 17      | IN 1_24V    | IN 1_5V     | IN 7_24V    | IN 7_5V     |  |
| 16      | IN 1_DIF    | IN 1_COM    | IN 7_DIF    | IN 7_COM    |  |
| 15      | IN 2_24V    | IN 2_5V     | IN 8_24V    | IN 8_5V     |  |
| 14      | IN 2_DIF    | IN 2_COM    | IN 8_DIF    | IN 8_COM    |  |
| 13      | IN 3_24V    | IN 3_5V     | IN 9_24V    | IN 9_5V     |  |
| 12      | IN 3_DIF    | IN 3_COM    | IN 9_DIF    | IN 9_COM    |  |
| 11      | IN 4_24V    | IN 4_5V     | IN A_24V    | IN A_5V     |  |
| 10      | IN 4_DIF    | IN 4_COM    | IN A_DIF    | IN A_COM    |  |
| 9       | IN 5_24V    | IN 5_5V     | IN B_24V    | IN B_5V     |  |
| 8       | IN 5_DIF    | IN 5_COM    | IN B_DIF    | IN B_COM    |  |
| 7       | OUT 0       | OUT 1       | OUT 4       | OUT 5       |  |
| 6       | OUT 2       | OUT 3       | OUT 6       | OUT 7       |  |
| 5       | OUT 0-3_COM | OUT_DIF_GND | OUT 4-7_COM | OUT_DIF_GND |  |
| 4       | OUT 0_DIF+  | OUT 0_DIF-  | OUT 3_DIF+  | OUT 3_DIF-  |  |
| 3       | OUT 1_DIF+  | OUT 1_DIF-  | OUT 4_DIF+  | OUT 4_DIF-  |  |
| 2       | OUT 2_DIF+  | OUT 2_DIF-  | OUT 5_DIF+  | OUT 5_DIF-  |  |
| 1       | NC          | NC          | NC          | NC          |  |

## ■List of input signals

The following table lists the input signals of the flexible high-speed I/O control module.

| Pin No. | CON1     |                                    | CON2     |                                    | Description   |  |
|---------|----------|------------------------------------|----------|------------------------------------|---|--|
|         | Symbol   | Signal name                        | Symbol   | Signal name                        |   |  |
| B20     | NC       | NC                                 | NC       | NC                                 | Empty pin   |  |
| A20     | NC       | NC                                 | NC       | NC                                 | -   |  |
| B19     | IN 0_24V | High-speed input 0<br>24VDC        | IN 6_24V | High-speed input 6<br>24VDC        | Inputs the + (plus) side. (common to 5VDC/<br>24VDC/differential) |  |
| A19     | IN 0_5V  | High-speed input 0<br>5VDC         | IN 6_5V  | High-speed input 6<br>5VDC         |   |  |
| B18     | IN 0_DIF |                                    |          | High-speed input 6<br>differential |   |  |
| A18     | IN 0_COM | High-speed input 0 common          | IN 6_COM | High-speed input 6 common          | Inputs the - (minus) side.  |  |
| B17     | IN 1_24V | High-speed input 1<br>24VDC        | IN 7_24V | High-speed input 7<br>24VDC        | Inputs the + (plus) side. (common to 5VDC/<br>24VDC/differential) |  |
| A17     | IN 1_5V  | High-speed input 1<br>5VDC         | IN 7_5V  | High-speed input 7<br>5VDC         |   |  |
| B16     | IN 1_DIF | High-speed input 1<br>differential | IN 7_DIF | High-speed input 7<br>differential |   |  |
| A16     | IN 1_COM | High-speed input 1 common          | IN 7_COM | High-speed input 7 common          | Inputs the - (minus) side.  |  |
| B15     | IN 2_24V | High-speed input 2<br>24VDC        | IN 8_24V | High-speed input 8<br>24VDC        | Inputs the + (plus) side. (common to 5VDC/<br>24VDC/differential) |  |
| A15     | IN 2_5V  | High-speed input 2<br>5VDC         | IN 8_5V  | High-speed input 8<br>5VDC         |   |  |
| B14     | IN 2_DIF | High-speed input 2<br>differential | IN 8_DIF | High-speed input 8<br>differential |   |  |
| A14     | IN 2_COM | High-speed input 2 common          | IN 8_COM | High-speed input 8 common          | Inputs the - (minus) side.  |  |
| B13     | IN 3_24V | High-speed input 3<br>24VDC        | IN 9_24V | High-speed input 9<br>24VDC        | Inputs the + (plus) side. (common to 5VDC/<br>24VDC/differential) |  |
| A13     | IN 3_5V  | High-speed input 3<br>5VDC         | IN 9_5V  | High-speed input 9<br>5VDC         |   |  |
| B12     | IN 3_DIF | High-speed input 3<br>differential | IN 9_DIF | High-speed input 9<br>differential |   |  |
| A12     | IN 3_COM | High-speed input 3 common          | IN 9_COM | High-speed input 9 common          | Inputs the - (minus) side.  |  |
| B11     | IN 4_24V | High-speed input 4<br>24VDC        | IN A_24V | High-speed input A<br>24VDC        | Inputs the + (plus) side. (common to 5VDC/<br>24VDC/differential) |  |
| A11     | IN 4_5V  | High-speed input 4<br>5VDC         | IN A_5V  | High-speed input A<br>5VDC         |   |  |
| B10     | IN 4_DIF | High-speed input 4 differential    | IN A_DIF | High-speed input A differential    |   |  |
| A10     | IN 4_COM | High-speed input 4 common          | IN A_COM | High-speed input A common          | Inputs the - (minus) side.  |  |
| B9      | IN 5_24V | High-speed input 5<br>24VDC        | IN B_24V | High-speed input B<br>24VDC        | Inputs the + (plus) side. (common to 5VDC/<br>24VDC/differential) |  |
| A9      | IN 5_5V  | High-speed input 5<br>5VDC         | IN B_5V  | High-speed input B<br>5VDC         |   |  |
| B8      | IN 5_DIF | High-speed input 5<br>differential | IN B_DIF | High-speed input B differential    |   |  |
| A8      | IN 5_COM | High-speed input 5 common          | IN B_COM | High-speed input B common          | Inputs the - (minus) side.  |  |

## ■List of output signals

The following table lists the output signals of the flexible high-speed I/O control module.

| Pin No. | CON1        | CON1                                 |             |                                      | Description                                      |  |  |
|---------|-------------|--------------------------------------|-------------|--------------------------------------|--|--|--|
|         | Symbol      | Signal name                          | Symbol      | Signal name                          | 1  |  |  |
| B7      | OUT 0       | High-speed output 0                  | OUT 4       | High-speed output 4                  | 5 to 24VDC output                                |  |  |
| A7      | OUT 1       | High-speed output 1                  | OUT 5       | High-speed output 5                  |  |  |  |
| B6      | OUT 2       | High-speed output 2                  | OUT 6       | High-speed output 6                  |  |  |  |
| A6      | OUT 3       | High-speed output 3                  | OUT 7       | High-speed output 7                  |  |  |  |
| B5      | OUT 0-3_COM | High-speed output 0-3 common         | OUT 4-7_COM | High-speed output 4-7 common         | High-speed output common                         |  |  |
| A5      | OUT_DIF_GND | High-speed output common             | OUT_DIF_GND | High-speed output<br>common          | High-speed output differential ground            |  |  |
| B4      | OUT 0_DIF+  | High-speed output 0<br>differential+ | OUT 3_DIF+  | High-speed output 3<br>differential+ | Outputs the differential output + (plus) side.   |  |  |
| A4      | OUT 0_DIF-  | High-speed output 0<br>differential- | OUT 3_DIF-  | High-speed output 3<br>differential- | Outputs the differential output - (minus) sides. |  |  |
| B3      | OUT 1_DIF+  | High-speed output 1<br>differential+ | OUT 4_DIF+  | High-speed output 4<br>differential+ | Outputs the differential output + (plus) side.   |  |  |
| A3      | OUT 1_DIF-  | High-speed output 1<br>differential- | OUT 4_DIF-  | High-speed output 4<br>differential- | Outputs the differential output - (minus) sides. |  |  |
| B2      | OUT 2_DIF+  | High-speed output 2<br>differential+ | OUT 5_DIF+  | High-speed output 5<br>differential+ | Outputs the differential output + (plus) side.   |  |  |
| A2      | OUT 2_DIF-  | High-speed output 2<br>differential- | OUT 5_DIF-  | High-speed output 5<br>differential- | Outputs the differential output - (minus) sides. |  |  |
| B1      | NC          | NC                                   | NC          | NC                                   | Empty pin  |  |  |
| A1      | NC          | NC                                   | NC          | NC                                   |  |  |  |

## **Connectable encoders**

The following tables list encoders that can be connected to the flexible high-speed I/O control module.

#### • Encoders with the following counting methods

| Counting method    | Condition   |
|--------------------|---|
| Incremental method | -   |
| Absolute method    | Encoders with the following specifications can be connected. <ul> <li>Parallel interface</li> <li>Resolution: 1 to 12 bits</li> <li>The absolute code is in pure binary, Gray code, or BCD.</li> </ul>                    |
|                    | <ul> <li>Encoders with the following specifications can be connected.</li> <li>SSI (Synchronous Serial Interface)</li> <li>Resolution: 1 to 32 bits</li> <li>The absolute code is in pure binary or Gray code.</li> </ul> |

#### · Encoders of the following output type

| Output type                                      | Condition  |  |
|--|--|--|
| Open collector output type                       | Check that the output voltage of the encoder meets the specifications of the |  |
| Line driver output type (equivalent to AM26LS31) | flexible high-speed I/O control module.                                      |  |
| CMOS level voltage output type                   |  |  |

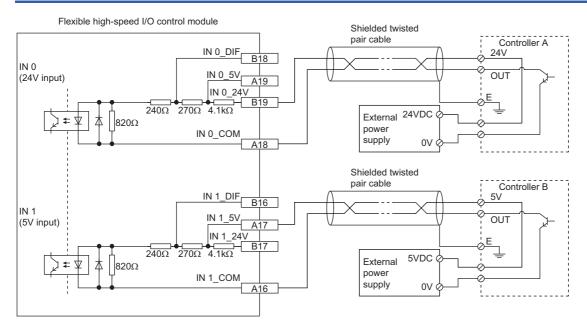
Restriction (")

TTL level voltage output type encoders cannot be used with the flexible high-speed I/O control module.

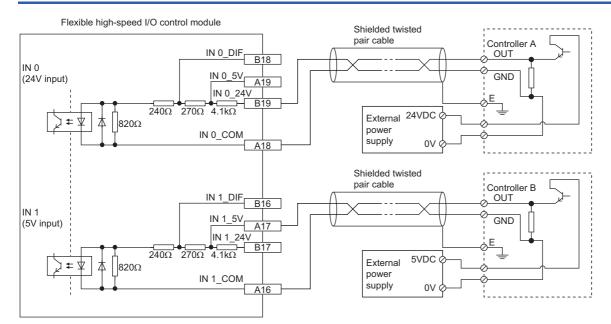
# 6.2 Examples of Wiring Between a Controller and External Input Terminals

This section shows examples of wiring between a controller and external input terminals.

## Example of external wiring with controllers (Sink load type)



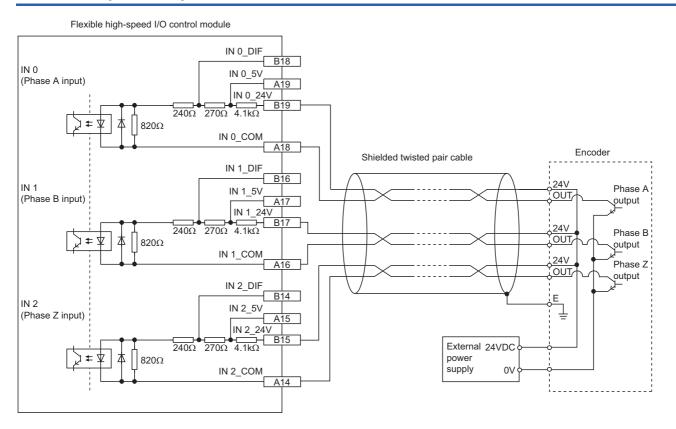
## Example of external wiring with controllers (Source load type)



# 6.3 Example of External Wiring Between the Flexible High-speed I/O Control Module and an Encoder

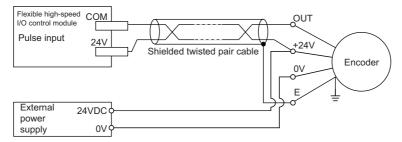
This section shows examples of external wiring between the flexible high-speed I/O control module and an encoder.

## Example of external wiring with an open collector output type encoder (24VDC)

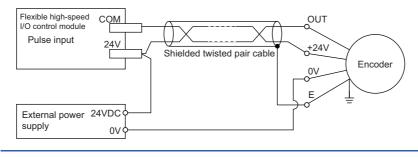




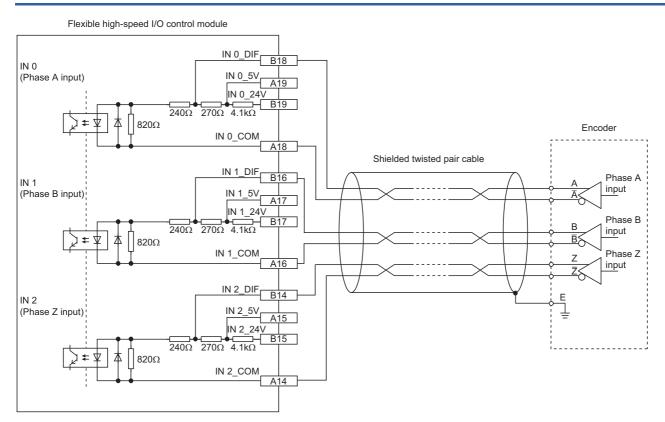
• For the wiring between the flexible high-speed I/O control module and an encoder, separate the power supply cables and signal wires as shown below.



• Do not wire the module and an encoder as shown in the figure below. Because a current flows through a shielded twisted pair cable in a single direction and the canceling effect disappears, the module is easy to be affected by electromagnetic induction.



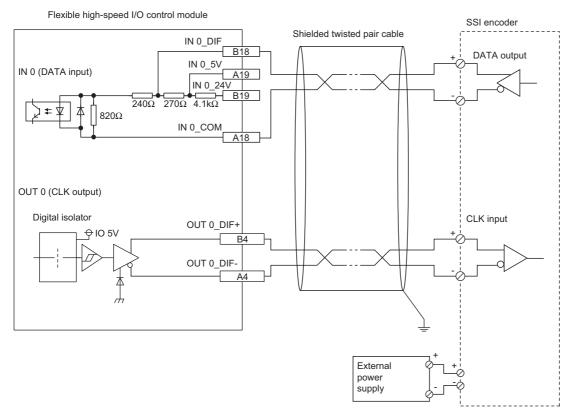
## Example of external wiring with a line driver (equivalent to AM26LS31) encoder



# Example of external wiring with the SSI encoder (serial communication)

Connect the flexible high-speed I/O control module to the SSI encoder using a shielded twisted pair cable of 0.2mm<sup>2</sup> or thicker (24 AWG or larger). Make sure to check the SSI encoder specifications.

In addition, separately prepare an external power supply for the SSI encoder.



- Connect the DATA input to any of IN 0 to IN B.
- Connect the CLK output to OUT 0\_DIF for SSI encoder 0, and to OUT 1\_DIF for SSI encoder 1.

## Relation between transmission speed and maximum cable length (reference value)

| Transmission speed | Maximum cable length |
|--------------------|----------------------|
| 100kHz             | 400m                 |
| 200kHz             | 190m                 |
| 300kHz             | 120m                 |
| 400kHz             | 80m                  |
| 500kHz             | 60m                  |
| 1MHz               | 25m                  |
| 1.5MHz             | 10m                  |
| 2MHz               | 5m                   |

The maximum cable length described in the above is a reference value. Depending on the response performance of the SSI encoder to be connected, the maximum cable length may become shorter than the above description because a response delay occurs.

Therefore, check the operation using an actual encoder to be connected, and use the module.

Point P

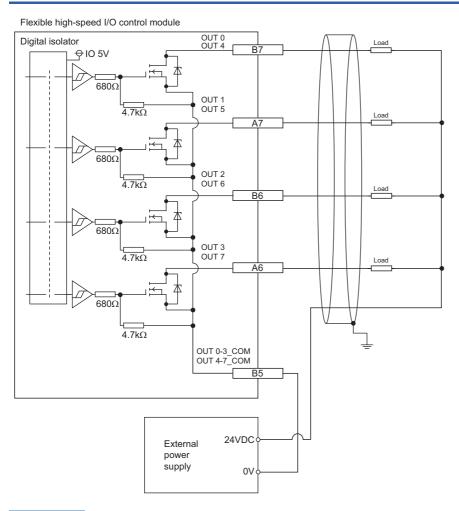
If the cable longer than the maximum length described above is used, the module becomes following states.

- An encoder value is fixed to an incorrect value and the module does not count the value correctly.
- An encoder value fluctuates and the module does not count the value correctly.
- An encoder value cannot be read and the module does not count the value correctly.

# 6.4 Example of External Wiring with External Output Terminals

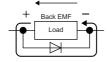
This section shows examples of external wiring with external output terminals.

## Example of external wiring with output terminals (Sink output type)

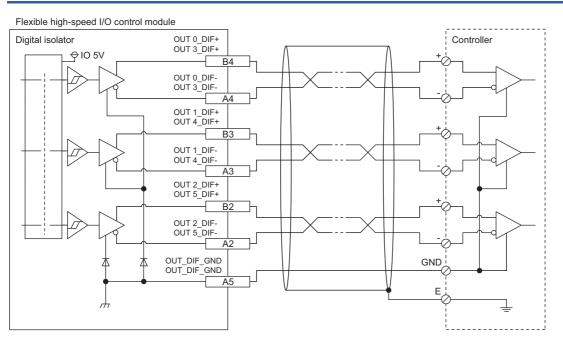


## Point *P*

When connecting an inductive load, connect a diode in parallel with the load to prevent back EMF from being generated to protect output elements.



# Example of external wiring with differential receivers (Differential output type)



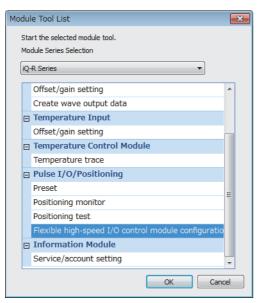
Point P

When connecting differential output terminals to differential receivers of the drive unit, connect the high-speed output common (OUT\_DIF\_GND) and the common terminals of the differential receivers of the drive unit. When the high-speed output common terminals are not connected, the potential difference will be generated between the high-speed output common (OUT\_DIF\_GND) and the common terminals of the differential receivers of the drive unit, damaging the module or causing a malfunction.

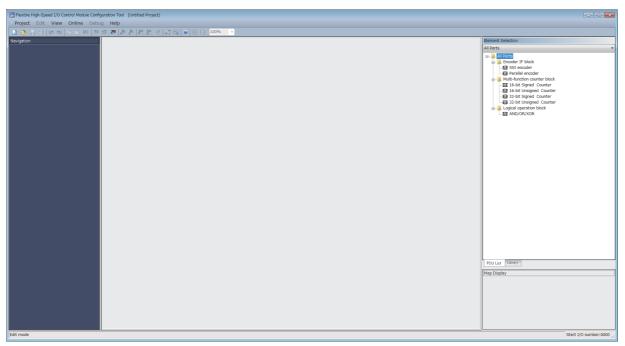
# **7** CONFIGURATION TOOL

Start the configuration tool from [Module Tool List] of GX Works3.

- "∑ [Tool] ⇔ [Module Tool List]
- 1. Select "iQ-R Series" for "Module Series Selection".
- **2.** Select [Pulse I/O/Positioning] ⇒ [Flexible high-speed I/O control module configuration tool], and click the [OK] button.



**3.** The configuration tool is started.



For details on the configuration tool, refer to the following.

MELSEC iQ-R Flexible High-Speed I/O Control Module User's Manual (Application)

# **8** OPERATION EXAMPLE

This chapter describes the programming procedure and basic programs of the flexible high-speed I/O control module.

## 8.1 Programming Procedure

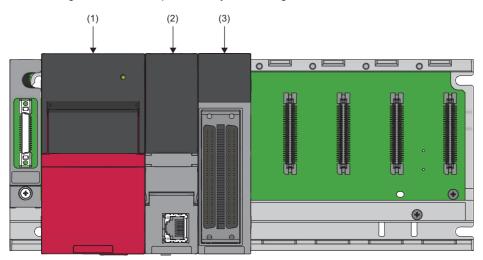
Create programs for executing the flexible high-speed I/O control module with the following procedure.

- **1.** Create a hardware logic with the configuration tool.
- Page 51 Creating a hardware logic
- 2. Create a program with GX Works3.
- Page 55 Program examples

## 8.2 Program Examples

## System configuration

The following shows an example of the system configuration.



(1) Power supply module (R61P)

(2) CPU module (R04CPU)

(3) Flexible high-speed I/O control module (RD40PD01)

## Programming conditions

The program counts pulses (2-phase 4 multiples) input to the flexible high-speed I/O control module with the multi function counter block.

## Parameter setting

Set parameters of the flexible high-speed I/O control module with GX Works3.

For details on the parameter setting, refer to the following.

MELSEC iQ-R Flexible High-Speed I/O Control Module User's Manual (Application)

Use the default values for the parameter setting in these program examples.

## Starting the configuration tool

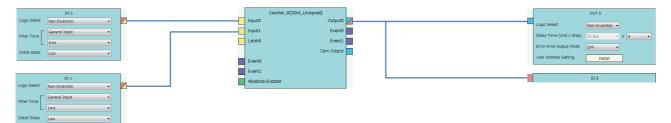
Start the configuration tool with the following procedure.

## Creating a hardware logic

Create a hardware logic with the configuration tool.

### Link and setting in the Hardware logic outline window

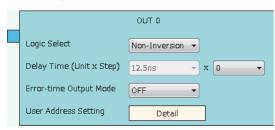
- **1.** Arrange a 32-bit unsigned multi function counter block in the Hardware logic outline window, and link it to external input blocks.
- To perform external outputs according to the counting result, link the multi function counter block to an external output block.
- To start an interrupt program according to the counting result, link the multi function counter block to an SI device terminal. (
  Page 57 Interrupt program example)



2. Set the external input blocks according to the specifications of external input signals.

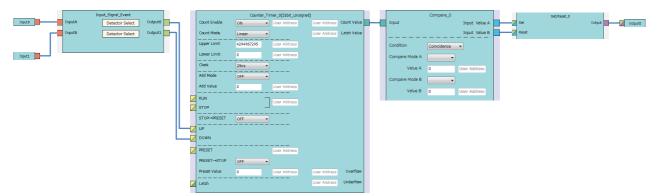
|               | IN O                    |  |
|---------------|-------------------------|--|
| Logic Select  | Non-Inversion 👻         |  |
| Filter Time   | 2-Phase Multiple of 4 🔹 |  |
| L             | 100kpps 🔻               |  |
| Initial State | Low •                   |  |
|               |                         |  |
|               |                         |  |
|               | IN 1                    |  |
| Logic Select  | IN 1 Non-Inversion      |  |
| Logic Select  |                         |  |
| Г             | Non-Inversion           |  |

**3.** To use the external output block, set it according to the specifications of external output signals and desired output timings.

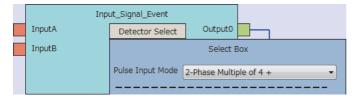


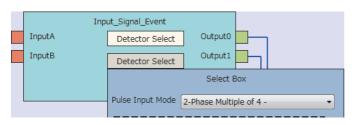
## Link and setting in the Multi function counter block detail window

1. Link blocks in the Multi function counter block detail window as shown below.

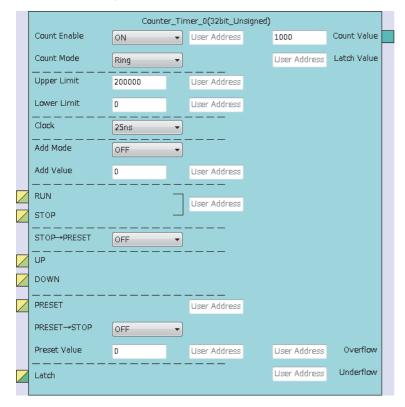


2. Set the setting values of the input signal event detection block as shown below.

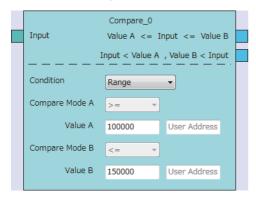




**3.** Set the setting values of the counter timer block as shown below.



4. Set the setting values of the comparison block as shown below.



### **Writing data to the module**

Write a project to the CPU module with GX Works3.

Write the hardware logic to the flexible high-speed I/O control module with the configuration tool.

## Label setting

Functions that support creation of programs are prepared for GX Works3.

The following table lists the module labels and global labels used in the program examples.

Do not change the settings of the module labels. For details on the global labels, refer to the following.

L MELSEC iQ-R Programming Manual (Program Design)

| Classification      | Label name  |                            |                                     | Description   |                           |                         |             | Device              |  |
|---------------------|---|----------------------------|-------------------------------------|---|---------------------------|-------------------------|-------------|---------------------|--|
| Module label        | RD40PD01_1.bModuleREADY   |                            | Module                              | Module READY  |                           |                         |             | X0                  |  |
|                     | RD40PD01_1.bOperatingConditionSettingsBatchResetComple teFlag   |                            |                                     | Operating condition settings batch-reset<br>complete flag |                           |                         |             | Х3                  |  |
|                     | RD40PD01_1.bHardwareLogicCont   | rolFlag                    | Hardwar                             | e lo  | gic control flag          |                         |             | X4                  |  |
|                     | RD40PD01_1.bErrorFlag   |                            | Error fla                           | g   |                           |                         |             | XF                  |  |
|                     | RD40PD01_1.bOperatingConditionSettingsBatchResetComma         Operating condition settings batch-reset           nd         command |                            |                                     | et  | Y3                        |                         |             |                     |  |
|                     | RD40PD01_1.bHardwareLogicCont   | rolStartRequest            | Hardwar                             | e lo  | gic control start request |                         |             | Y4                  |  |
|                     | RD40PD01_1.bHardwareLogicCont   | rolStopRequest             | Hardware logic control stop request |   |                           |                         |             | Y5                  |  |
|                     | RD40PD01_1.bErrorClearRequest   |                            | Error clear request                 |   |                           |                         |             | YF                  |  |
|                     | RD40PD01_1.uLatestErrorCode_D   |                            |                                     | Latest error code   |                           |                         |             | Un\G100             |  |
|                     | RD40PD01_1.udCumulativeNumberOfWriteAccessesToFlashR<br>OM_D  |                            |                                     | Cumulative number of write accesses to a flash ROM        |                           |                         |             | Un\G102,<br>Un\G103 |  |
|                     | RD40PD01_1.uClearSettingOfError   | History_D                  | Clear se                            | tting   | g of error history        |                         |             | Un\G8002            |  |
| Label to be defined | Define the global labels as follows.  |                            |                                     |   |                           |                         |             |                     |  |
|                     | Label Name  | Data Type                  |                                     |   | Class                     |                         | Assign (Der | vice/Label)∆        |  |
|                     | LatestErrorCode   | Word [Unsigned]/Bit String | [16-bit]                            |   | VAR_GLOBAL •              | • [                     | D1 00       |                     |  |
|                     | CountValue  | Double Word [Signed]       |                                     |   |                           | VAR GLOBAL VAR OT 01010 |             | D1010               |  |
|                     | SettingStartReq   | Bit                        |                                     |   | VAR_GLOBAL                | •                       | M1 00       |                     |  |
|                     | HardwareLogicControlStartReq  | Bit                        |                                     |   | VAR_GLOBAL VAR_M110       |                         | M110        |                     |  |
|                     | HardwareLogicControlStopReq   | Bit                        |                                     |   | VAR_GLOBAL                | •                       | M111        |                     |  |
|                     | CountValueReadReq   | Bit                        |                                     |   | VAR_GLOBAL                | •                       | M112        |                     |  |
|                     | HardwareLogicResetReq   | Bit                        |                                     |   | -                         |                         | M113        |                     |  |
|                     | ErrorResetReq   | Bit                        |                                     |   | VAR_GLOBAL                | •                       | M1 20       |                     |  |

### Program examples

### ■Program example 1

A program example to configure the clear setting of error history when a module is started



(0) Store Clear the history. (1) in 'Clear setting of error history' (Un\G8002).

#### ■Program example 2

A program example to start the hardware logic control



(7) Turn on 'Hardware logic control start request' (Y4).

(12) Turn off 'Hardware logic control start request' (Y4).

### Program example 3

A program example to stop the hardware logic control



- (15) Turn on 'Hardware logic control stop request' (Y5).
- (19) Turn off 'Hardware logic control stop request' (Y5).

#### ■Program example 4

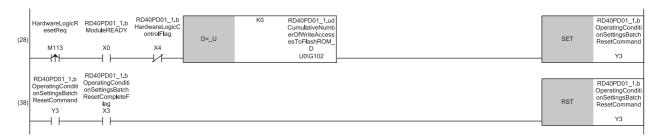
A program example to read the count value



#### (22) Read the count value.

### ■Program example 5

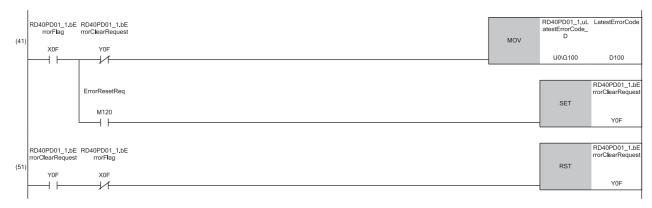
A program example to reset the operating condition settings in a batch



- (28) Turn on 'Operating condition settings batch-reset command' (Y3).
- (38) Turn off 'Operating condition settings batch-reset command' (Y3).

## ■Program example 6

A program example to read the latest error code and reset the error



- (41) Read 'Latest error code' (Un\G100) and turn on 'Error clear request' (YF).
- (51) Turn off 'Error clear request' (YF).

## Interrupt program example

This program outputs an interrupt request to the CPU module by inputting a signal to an SI device terminal.

## ■"Interrupt setting" of GX Works3

Set "Interrupt setting" from "Module Parameter" of GX Works3.

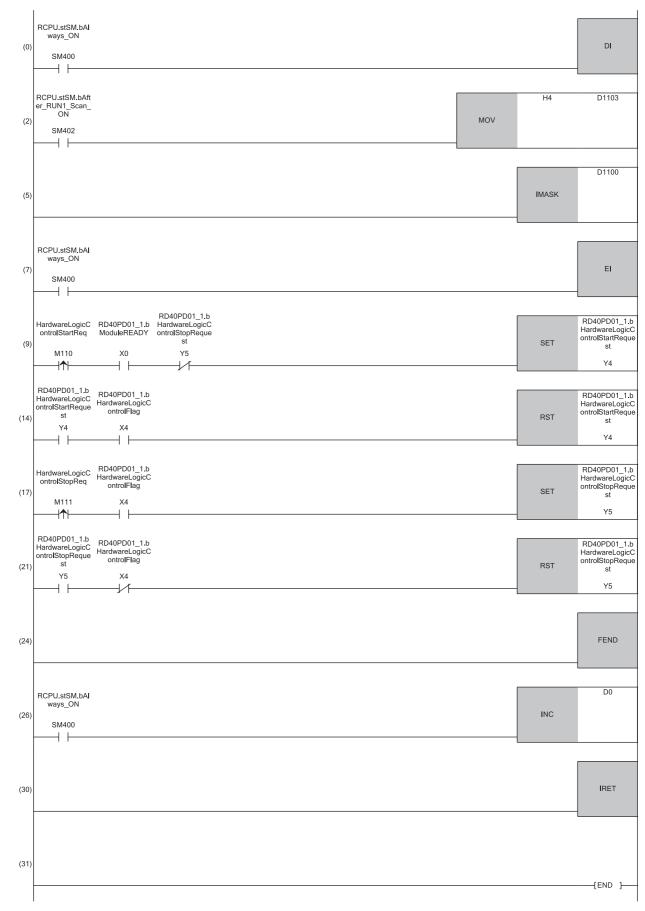
| $\bigcirc$ | [Navigation window] | ⇔ [Parameter] ⇔ | [Module Information] ⇒ | [RD40PD01] ⇒ | [Module Parameter] | ⇒ [Interrupt setting] |
|------------|---------------------|-----------------|------------------------|--------------|--------------------|-----------------------|
|------------|---------------------|-----------------|------------------------|--------------|--------------------|-----------------------|

| 💼 0000:RD40PD01 Module Para | ameter   |                                    |                      |
|-----------------------------|--|------------------------------------|----------------------|
| Setting Item List           | Setting Item   |                                    |                      |
| Input the Setting Item to S |  |                                    |                      |
|                             | No.  | Condition target setting           | Interruption pointer |
|                             | 1  | SI device terminal 0(SI0)          | 150                  |
| ⊕                           | 2  | SI device terminal 1(SI1)          |                      |
|                             | 3  | SI device terminal 2(SI2)          |                      |
|                             | 4  | SI device terminal 3(SI3)          |                      |
|                             | 5  | SI device terminal 4(SI4)          |                      |
|                             | 6  | SI device terminal 5(SI5)          |                      |
|                             | 7  | SI device terminal 6(SI6)          |                      |
|                             | 8  | SI device terminal 7(SI7)          |                      |
|                             | 9  | Continuous logging data storage    |                      |
|                             |  |                                    |                      |
|                             | Explanation  |                                    |                      |
|                             | Set the starting interrupt pointer number when interrupt factor is<br>Please set the interrupt pointer to the interrupt factor that confi<br>•The SI device terminal that wired in hardware logic<br>•Continuous logging data storage interrupt enabled<br><example> When using the interrupt pointer I50, set to "I50".</example> | s detected.<br>gured to following. |                      |
| a ( ) ( )                   |  |                                    | ·                    |
| Item List Find Result       | Check Restore the Default Settings   |                                    |                      |

### ■User devices

| Device         | Description   |
|----------------|---|
| D0             | Used for interrupt program                          |
| D1100 to D1115 | Interrupt enable flag storage for IMASK instruction |

### ■Program example



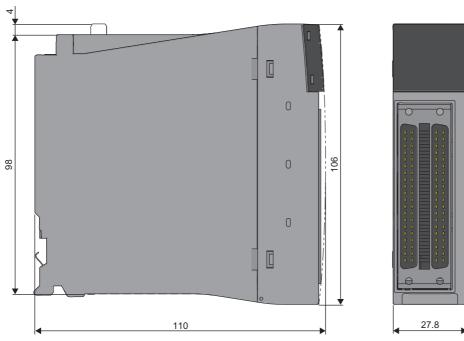
150

- (0) Prohibit the execution of an interrupt program.
- (2) Turn on the bit corresponding to I50.
- (5) Enable the interrupt of I50.
- (7) Enable the execution of the interrupt program.
- Arrange the main routine program between the EI instruction and FEND instruction.
- (26) Execute the interrupt program (add 1 to D0).

# APPENDIX

This chapter shows the external dimensions of the flexible high-speed I/O control module.

## Appendix 1 External Dimensions



(Unit: mm)

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| RUN LED   |

## REVISIONS

| * The manual number is given on the bottom left of the back cover. |                    |   |
|--|--------------------|---|
| Revision date  | *Manual number     | Description   |
| October 2016   | SH(NA)-081645ENG-A | First edition   |
| April 2017   | SH(NA)-081645ENG-B | ■Added or modified parts<br>SAFETY PRECAUTIONS, INTRODUCTION, Chapter 3                         |
| July 2018  | SH(NA)-081645ENG-C | ■Added or modified parts<br>RELEVANT MANUALS, Chapter 5, Section 6.3                            |
| April 2019   | SH(NA)-081645ENG-D | ■Added or modified parts<br>SAFETY PRECAUTIONS, Section 2.1, Chapter 3, Section 6.1             |
| May 2020   | SH(NA)-081645ENG-E | ■Added or modified parts<br>RELEVANT MANUALS, TERMS, GENERIC TERMS AND ABBREVIATIONS, Chapter 3 |

## \*The manual number is given on the bottom left of the back cover.

### Japanese manual number: SH-081644-E

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 SH(NA)-081645ENG-E(2005)MEE

 MODEL:
 RD40PD01-U-IN-E

 MODEL CODE:
 13JX55

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