TECHNICAL BULLETIN

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[Title] Instructions on starting PLC operation after storage   **[Date of Issue]** Dec, '02
[Relevant Models] Basic Model QCPU, High Performance Model QCPU and Process CPU

Thank you for your continuous support to Mitsubishi programmable logic controllers, MELSEC-Q series.

This bulletin is to explain operating instructions when the Basic Model QCPU, High Performance Model QCPU, and Process CPU are to be used after being stored for some period of time.

1. When PLC has been stored without a battery

When the PLC has been stored with the battery removed, the memories in the CPU module or memory card may be corrupted.
Format the corresponding memory using GX Developer before restarting the PLC. After formatting, write previously backed-up data to the corresponding memory.

The following table shows the relation between battery and memory types.

◯: Backed up by battery ✕: Not backed up by battery

Memory	Battery	Q6BAT or Q7BAT of QCPU module*	Battery embedded in memory card
CPU module	Program memory	◯	✕
	Standard RAM	◯	✕
	Standard ROM	- (Battery backup not required)	
Memory card*	SRAM card	✕	◯
	Flash card	- (Battery backup not required)	
	ATA card	- (Battery backup not required)	

* Not applicable for the Basic Model QCPU

For the battery-backed memories shown in the above table, formatting with GX Developer is required before starting the PLC.

Please refer to the GX Developer manual for information about formatting.

POINT
When storing the PLC, make sure to back up each memory.

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2. When battery has gone flat during storage of PLC

When the battery has gone flat during storage of the PLC, the memories in the CPU module or memory card may be corrupted.
Format the corresponding memory using GX Developer before restarting the PLC . After formatting, write previously backed-up data to the corresponding memory.

The following table shows the relation between battery and memory types:

○: Backed up by battery ✕: Not backed up by battery

Memory	Battery	Q6BAT or Q7BAT of QCPU module*	Battery embedded in memory card
CPU module	Program memory	○	✕
	Standard RAM	○	✕
	Standard ROM	- (Battery backup not required)	
Memory card*	SRAM card	✕	○
	Flash card	- (Battery backup not required)	
	ATA card	- (Battery backup not required)	

* Not applicable for the Basic Model QCPU

For the battery-backed memories shown in the above table, formatting with GX Developer is required before starting the PLC.

Please refer to the GX Developer manual for information about formatting.

POINT
When storing the PLC, make sure to back up each memory.

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