TECHNICAL BULLETIN ..... [ 1/80]
[Issue No.] FA-A-0142-C
[Title] Production discontinuation of MELSEC-AnS/QnAS (small type) series and MELSEC-I/OLINK
[Date of Issue] October, 2012 (Ver. C: March 2015)
[Relevant Models] AnSCPU, AnUSCPU, QnASCPU, and others
Thank you for your continued support of Mitsubishi programmable controllers, MELSEC-AnS/QnAS series and MELSEC-I/OLINK. MELSEC-AnS/QnAS (small type) series and MELSEC-I/OLINK have been used for about 20 years since they were released in 1990. However, we have decided to discontinue them. This technical bulletin is to provide the information regarding this production discontinuation.
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## 1. Models to be discontinued

Production will be discontinued for the AnS/QnAS (small type) series CPU modules (AnSCPU, AnUSCPU, QnASCPU), power supply module (partial), base unit, I/O module, special function module, network module and relevant products of the AnS/QnAS (small type) series, the products relevant to a made-to-order production based on the small-sized A series products, and the I/OLINK (master, remote I/O).
For the details of programmable controllers to be discontinued, refer to Section 10. Regarding the details of the motion controller A171SHCPUN, A172SHCPUN, A173UHCPU, A173UHCPU-S1 and other relevant models to be discontinued, refer to "Sales and Service" (No.12-14:Issued in October, 2012) for the motion controller.

## 2. Production discontinuation

- Transition to made-to-order
- Order deadline
- Final production

April 1st, 2011 (Refer to "Technical Bulletin" (FA-A-0094 ).)
Through August, 2014
Through September, 2014

## 3. Reasons for discontinuing production

Conventional main electronic components of the relevant programmable controllers, i.e., semiconductor components (micro computer, memory, ASIC, etc.) are now absolutely difficult to obtain because they are produced based on the stricter process rules and the contributions to environmental conservation, such as lead-free, compliance to RoHS directives are required. We have been producing AnS/QnAS series and I/OLINK products by securing the stock of these obsolete components. However, the stock is about to run out, and we have extreme difficulty to maintain the production system and product quality.

## 4. Repair acceptance

- Repair acceptance

Through September, 2021 (for 7 years after production discontinuation)

|  | 2013 | 14 | 15 | 16 | 17 | 18 | 19 | 2020 | 21 | 22 | 23 | 24 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Discontinuation Schedule | Repair Acceptance (7 years) |  |  |  |  |  |  |  |  |  |  |  |
|  | $\underset{2014 / 9}{\wedge}$ |  |  |  |  |  |  | $\triangle$ |  |  |  |  |
|  |  |  |  |  |  |  |  | 2021/9 <br> Finish Repair Acceptance |  |  |  |  |

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## 5. Continued production

We will continue production of the modules listed below.
As for power supply modules, we will continue production of the two models, A1S61PN and A1S63P.
It is recommended you to purchase spare parts or replace with the above two models, if using other power supply modules.
For batteries, we will continue production of three models, A6BAT, A8BAT, and A10BAT.

| Product | Model |
| :---: | :---: |
| Power supply module | A1S61PN |
|  | A1S63P |
| Battery | A6BAT |
|  | A8BAT |
|  | A10BAT |
| Memory card | Q1MEM-64S |
|  | Q1MEM-128S |
|  | Q1MEM-256S |
|  | Q1MEM-512S |
|  | Q1MEM-1MS |
|  | Q1MEM-2MS |
|  | Q1MEM-64SE |
|  | Q1MEM-128SE |
|  | Q1MEM-256SE |
|  | Q1MEM-512SE |
|  | Q1MEM-1MSE |
| Positioning module | A1SD75-C01H |
|  | A1SD75-C01HA |
| MELSECNET/10 network module | A1SJ71LP21 |
|  | A1SJ71BR11 |
|  | A1SJ71QLP21 |
|  | A1SJ71QBR11 |
| CC-Link module | A1SJ61BT11 |
|  | A1SJ61QBT11 |
| MELSECNET/MINI-CC-Link wiring conversion adapter | A6ADP-1MC16D |
|  | A6ADP-1MC16T |
|  | A6ADP-2MC16D |
| A-A1S module conversion adapter | A1ADP-XY |
|  | A1ADP-SP |

## 6. Recommendable proposals

We recommend the following solutions for AnS/QnAS (small type) series and MELSEC-I/OLINK (master, remote I/O) production discontinuation.
(1) Purchase of spare parts for necessary models before order deadline described in Section 2.
(2) Replacement with L series or AnyWire

If modules and functions that cannot be replaced by the $L$ series or AnyWire are used in the existing system, replacement with the $Q$ series or CC-Link/LT is recommended.
For the alternative models, please refer to Section 12.
In the alternative model list, we have introduced models that have small restrictions when an AnS/QnAS (small type) series module or I/OLINK module is replaced. Models with reduced specifications can be selected depending on your system, so please check your existing system specification and select the models.
AnyWire products are not available in some countries.
Please consult your local Mitsubishi representative for details.
(3) Replacement with $Q$ series

It is recommended to replace a module with a $Q$ series module if the following conditions are met;

- Existing communication cables are used or the system is gradually replaced when a system including a MELSECNET(II) data link system is replaced.
- Only a CPU module is replaced and an existing AnS/QnAS series module is continued to be used.
- The existing wiring of terminal block type modules is used by utilizing a conversion adapter.


## (4) Replacement with CC-Link/LT

It is recommended to replace a module with a CC-Link/LT module if the following conditions are met;

- The sequence program of existing I/OLINK is not modified.
- The communication cables of existing I/OLINK or external power supply are not used.


## Reference Material

When considering transition to $L$ series, please refer to the following materials:

- Transition from MELSEC-AnS/QnAS (Small Type) Series to L Series Handbook (Fundamentals)
- Transition from MELSEC-AnS/QnAS (Small Type) Series to L Series Handbook (Intelligent Function Modules)
- Transition from MELSEC-AnS/QnAS (Small Type) Series to L Series Handbook (Network Modules)
- Transition from MELSEC-AnS/QnAS (Small Type) Series to L Series Handbook (Communications)

L(NA)08258ENG
L(NA)08259ENG
L(NA)08260ENG L(NA)08261ENG

When considering transition to $\mathbf{Q}$ series, please refer to the following materials:

- Programmable Controllers MELSEC-A/QnA Series Transition Examples
- Transition from MELSEC-AnS/QnAS (Small Type) Series to Q series Handbook (Fundamentals)
- Transition from MELSEC-AnS/QnAS (Small Type) Series to Q series Handbook (Intelligent Function Modules)
- Transition from MELSEC-A/QnA (Large Type) Series to Q series Handbook (Network Modules)
- Transition from MELSEC-A/QnA (Large Type) Series to Q series Handbook (Communications)

When considering transition to CC-Link/LT, please refer to the following material:
-Transition from MELSEC-I/OLINK to CC-Link/LT Handbook
L(NA)08062ENG

For useful renewal tools not described in this technical bulletin, please refer to the following material:

- Mitsubishi Programmable Controllers MELSEC-AnS/QnAS (Small Type) Series Transition Guide

L(NA)08236E

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## 7. Gradual transition from $A n S / Q n A S$ series to $Q$ series

By utilizing the QA1SDB type extension base unit, we propose gradual transition by using existing AnS/QnAS (small type) series assets. (Basic model CPUs, Process CPUs, and Redundant CPUs cannot be mounted to a QA1SDB type extension base unit.)
By using your modules on your A (small type) base in your existing system and mounting them to the QA1SDB type extension base unit, you can use your existing A (small type) series modules, and transfer to a system configuration that is controlled by a new $Q$ series CPU. Also, from the next step by gradually changing to the $Q$ series, you can realize a $Q$ series configuration.

For modules that can be mounted to a QA1SDB type extension base unit, please refer to the QCPU User's Manual (Hardware Design, Maintenance and Inspection) (SH-080483ENG).


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## 8. Machine/line modification and spare parts

In conjunction with the AnS/QnAS (small type) series product discontinuation, we propose the following for machine/line modification and spare parts for failure.

### 8.1 Correspondence to module failures

We propose possession of a sufficient amount of spare parts for maintenance and exchange for module failures. We will continue production of the power supply modules of A1S61PN and A1S63P after October, 2014.

### 8.2 Correspondence to machine/line modification

Please select the module that has the function for the machinery/line modification necessary among the CC-Link products. Please mount a CC-Link system master/local station module to an empty slot of a $Q$ series base unit and add modules using the CC-Link system.
"When there are no open slots on the existing system, or there are no spare I/O points left", please remove one/or some existing modules from the existing system to mount a CC-Link system master/local station module. Please add modules of CC-Link remote stations to use the functions of the modules that were removed.


Remote I/O station Remote device station

Intelligent Remote device statio


CC-Link
device station


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### 8.3 Spare parts storage

(1) The general specifications of programmable controllers are as follows. Please do not store spare parts under a high temperature or high humidity condition, even within the range guaranteed by the specifications.

| Storage ambient temperature | -20 to $75^{\circ} \mathrm{C}$ |
| :--- | :--- |
| Storage ambient humidity | 10 to $90 \%$, no condensation |

(2) Store in a place avoiding direct sunlight.
(3) Store under a condition with no dust or corrosive gas.
(4) The battery capacity of an A6BAT battery or a lithium-coin battery (commercially available) for memory card will be decreased by its self-discharging even when it is not used. Replace it with new one in 5 years as a guideline.
(5) Secure the spare parts for a power supply module, power supply built-in type CPU module, or analog module that uses the aluminum electrolytic capacitor, which is listed in the following table, because the basic function will be influenced by life deterioration.
If products are left un-energized for a long time, the characteristics of the aluminum electrolytic capacitor will be deteriorated; therefore, take following measures.

| Product | Model |
| :--- | :--- |
| CPU module (Power supply built-in type) | A1SJHCPU |
| Power supply module | A1S61PN, A1S63P |
| Analog module | A1S62DA, A1S63ADA, A1S66ADA, A1S68DAV, A1S68DAI |

[Countermeasures for preventing aluminum electrolytic capacitor characteristics deterioration]
For the power supply module or power supply built-in type CPU module which uses the aluminum electrolytic capacitor and has the rated voltage of 100VAC/DC or more, characteristics will be deteriorated when it is left un-energized for a long time. Therefore, rotate products at regular inspection (once in one or two years). Or, activate the product once in two or three years, increasing voltage from 0 V to the rated voltage over 10 minutes and maintaining the voltage for a few hours.

## [Reference]

When an aluminum electrolytic capacitor is left un-energized, it will be deteriorated at approximately $1 / 4$ speed of the case when it is energized, even at normal temperature. For example, when an aluminum electrolytic capacitor is stored for 10 years at normal temperature, its life will be shortened by approximately 2.5 years.
The deterioration will be further accelerated under high temperature and humidity; therefore, avoid such an environment when storing the spare parts.

## 9. Precautions for replacing with L series, AnyWire, Q series, and CC-Link/LT

The precautions for replacing with alternative models are described below.
(1) CPU module

Select a new CPU module considering the required program capacity, I/O points and device points.
(2) Power supply module

Select a new power supply module by considering the amount of current consumed by each module to be mounted.
(a) Pay enough attention when selecting the capacity of the power supply module if using an extension base unit that does not need power.
(b) In the $\mathbf{Q}$ series, when the capacity of the power supply module is selected, it is necessary to take consideration of the base unit current consumed as well, so please pay attention.
(3) Base unit

Select a new base unit based on the number of slots and power supply module to be used.
Pay full attention to the followings.
(a) The dimension of holes for fixing a base unit to a control panel must be modified because they vary depending on the series.
(b) In the $L$ series, install a base unit to a DIN rail to fix it to a control panel.
(c) Please note that the number of extension blocks to be configured, the number of applicable modules, and the maximum number of the entire system configuration vary depending on the CPU module.
(4) I/O module

Select the model that satisfies the following specifications such as the number of I/O points and I/O current/voltage. Pay full attention to the followings.
(a) Modify the wiring by referring to the manual because the terminal block/connector shape, signal layout, and common type vary depending on the series.
(5) Special function module (Intelligent function module)

Select the model that satisfies the performance specifications.
Pay full attention to the followings.
(a) Modify the $\mathrm{X} / \mathrm{Y}$ device Nos. if the new model is different from the old one in the number of occupied I/O points.
(b) Modify the wiring by referring to the manual because the terminal block/connector shape and signal layout vary depending on the series.

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## (6) Network module

Pay full attention to the followings.
(a) Cables may have to be modified. Check the specifications described in the manual.
(b) If there is no alternative network module, it is recommended to replace the system with other network systems.
(c) For the CC-Link and Ethernet network, use the network parameters to modify the initial settings configured using a sequence program.
(7) Programming

When the PLC type is changed in GX Developer (also included with GX Works2), the programs and parameters are automatically converted for the new CPU. Note the followings.
(a) Prepare GX Developer (or GX Works2) with the version supporting the CPU to be used and a cable for connecting GX Developer (or GX Works2) and a programmable controller. For applicable cables, refer to the GX Developer (or GX Works2) manual.
(b) Some network parameters have been deleted because they cannot be converted. Set the parameters after conversion.
In the $Q$ series, parameters can be modified by using "MELSECNET(II)->MELSECNET/10(H) parameter conversion tool" of
"A/QnA->Q conversion support tool".

| Network parameter | AnS to L | QnAS to L | AnS to Q | QnAS to Q |
| :--- | :--- | :--- | :--- | :--- |
| MELSECNET(II) | MELSECNET(II) parameters have been deleted. | MELSECNET/10 parameters are converted for the <br> MELSECNET/10 mode. |  |  |
| MELSECNET/10(H) | MELSECNET/10 parameters have been deleted. | The parameters of the 5th and <br> subsequent modules are not <br> available when five or more <br> modules are set in the CC- <br> Link module configuration <br> window. |  |  |
| MELSECNET/MINI | MELSECNET/MINI parameters have been deleted. | The parameters of the 5th and <br> subsequent modules are not <br> available when five or more <br> modules are set in the CC- <br> Link module configuration <br> window. | - | "Use the KeepAlive" is set on <br> "Ethernet operations". |
| CC-Link | - | Ethernet parameters have <br> been deleted. | - |  |

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(c) The instructions or devices that cannot be used without modification are converted into SM1255 or SD1255 (*1). After PLC type change, search SM1255 or SD1255 and modify them.

By using "A/QnA->Q program conversion support tool" of "A/QnA->Q conversion support tool", a program with instructions or devices converted into SM1255 or SD1255 can be easily modified.
*1 The instructions or devices are converted into SM999 or SD999 in a Basic model QCPU.
(d) If the new CPU module does not have a sufficient program capacity, some parts of the program are deleted during conversion. (The END instruction is added.)

After PLC type change, check if any parts of program are missing or not.
(e) Buffer memory contents and addresses of special function modules (intelligent function modules) and network modules vary depending on the series. Modify the program that writes to/reads from buffer memory, if necessary.

By using "A/QnA->Q program conversion support tool" of "A/QnA->Q conversion support tool", the buffer memory can be converted into compatible buffer memory.

## [Reference]

Q and L series support GX Works2. Use of GX Works2 simplifies a program for an intelligent function module and network module.
(f) Roles of accumulator (A), index register (V, Z) and file register (R) vary depending on the series. Therefore, modify programs if necessary.
(g) Microcomputer programs cannot be created.
(h) Each of a main program, sub-program and SFC program is converted into one program file. When a sub-program or SFC program is used, be sure to enable the multiple programs in the PLC parameter (program settings) after conversion. Then modify the part that starts up programs.

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## 10.Models to be discontinued

| Product | Model |
| :---: | :---: |
| Base unit | A1S32B |
|  | A1S32B-E |
|  | A1S33B |
|  | A1S33B-E |
|  | A1S35B |
|  | A1S35B-E |
|  | A1S38B |
|  | A1S38B-E |
|  | A1S38HB |
|  | A1S38HBEU |
|  | A1S52B |
|  | A1S52B-S1 |
|  | A1S55B |
|  | A1S55B-S1 |
|  | A1S58B |
|  | A1S58B-S1 |
|  | A1S65B |
|  | A1S65B-S1 |
|  | A1S68B |
|  | A1S68B-S1 |
| Power supply module | A1S62PN |
| Extension cable | A1SC01B |
|  | A1SC03B |
|  | A1SC07B |
|  | A1SC12B |
|  | A1SC30B |
|  | A1SC60B |
| CPU module | A1SCPUC24-R2 |
|  | A1SHCPU |
|  | A1SJHCPU |
|  | A1SJHCPU-S8 |
|  | A2ASCPU |
|  | A2ASCPU-S1 |
|  | A2ASCPU-S30 |
|  | A2SHCPU |
|  | A2SHCPU-S1 |
|  | A2USCPU |
|  | A2USHCPU-S1 |
|  | Q2ASCPU |
|  | Q2ASCPU-S1 |
|  | Q2ASHCPU |
|  | Q2ASHCPU-S1 |
| Memory cassette | A1SNMCA-2KE |
|  | A1SNMCA-8KE |
|  | A1SNMCA-8KP |
|  | A2SNMCA-30KE |

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| Product | Model |
| :---: | :---: |
| Battery | A8BAT-SET |
| Input module | A1S42X |
|  | A1SX10 |
|  | A1SX10EU |
|  | A1SX20 |
|  | A1SX20EU |
|  | A1SX30 |
|  | A1SX40 |
|  | A1SX40-S1 |
|  | A1SX40-S2 |
|  | A1SX41 |
|  | A1SX41-S1 |
|  | A1SX41-S2 |
|  | A1SX42 |
|  | A1SX42-S1 |
|  | A1SX42-S2 |
|  | A1SX71 |
|  | A1SX80 |
|  | A1SX80-S1 |
|  | A1SX80-S2 |
|  | A1SX81 |
|  | A1SX81-S2 |
|  | A1SX82-S1 |
| Output module | A1S42Y |
|  | A1SY10 |
|  | A1SY10EU |
|  | A1SY14EU |
|  | A1SY18A |
|  | A1SY18AEU |
|  | A1SY22 |
|  | A1SY28A |
|  | A1SY40P |
|  | A1SY41P |
|  | A1SY42P |
|  | A1SY50 |
|  | A1SY60 |
|  | A1SY60E |
|  | A1SY68A |
|  | A1SY71 |
|  | A1SY80 |
|  | A1SY81 |
|  | A1SY82 |
| I/O combined module | A1SH42 |
|  | A1SH42P |
|  | A1SH42P-S1 |
|  | A1SH42-S1 |
|  | A1SX48Y18 |
|  | A1SX48Y58 |
| Dummy module | A1SG62 |

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| Product | Model |
| :---: | :---: |
| Blank cover for I/O slot | A1SG60 |
| Interrupt module | A1SI61 |
| Analog input module | A1S64AD |
|  | A1S68AD |
| Analog output module | A1S68DAI |
|  | A1S68DAV |
|  | A1S62DA |
| Analog I/O module | A1S63ADA |
|  | A1S66ADA |
| Temperature input module | A1S62RD3N |
|  | A1S62RD4N |
|  | A1S68TD |
| Temperature control module | A1S64TCTRT |
|  | A1S64TCTRTBW |
| High-speed counter module | A1SD61 |
|  | A1SD62 |
|  | A1SD62D |
|  | A1SD62D-S1 |
|  | A1SD62E |
| Positioning module | A1SD75M1 |
|  | A1SD75M2 |
|  | A1SD75M3 |
|  | A1SD75P1-S3 |
|  | A1SD75P2-S3 |
|  | A1SD75P3-S3 |
| Position detection module | A1S62LS |
| Ethernet interface module | A1SJ71E71N3-T |
|  | A1SJ71QE71N3-T |
| Computer link module | A1SJ71UC24-PRF |
|  | A1SJ71UC24-R2 |
|  | A1SJ71UC24-R4 |
| Serial communication module | A1SJ71QC24N1 |
|  | A1SJ71QC24N1-R2 |
| MELSECNET(II)/B data link module | A1SJ71AP21 |
|  | A1SJ71AR21 |
|  | A1SJ71AT21B |
| MELSECNET/10 network module | A1SJ71LP21GE |
|  | A1SJ71LR21 |
|  | A1SJ71QLP21S |
|  | A1SJ71QLP21GE |
|  | A1SJ71QLR21 |
| MELSECNET/10 remote I/O station module | A1SJ72QBR15 |
|  | A1SJ72QLP25 |
|  | A1SJ72QLR25 |
| JEMANET(OPCN-1) interface module | A1SJ71J92-S3 |
| AS-i master module | A1SJ71AS92 |
| PC fault detection module | A1SS91 |

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| Product | Model |
| :--- | :--- |
| Conversion adapter for AnS | A1S-TA32 |
|  | A1S-TA32-3 |
|  | A1S-TA32-7 |
| ROM writer module | A1S-TB32 |
| Modem interface module | A6WA-28P |
| MELSEC-I/OLINK master module | Q6TEL |
| MELSEC-I/OLINK remote I/O module | A1SJ51T64 |
|  | AJ55TB2-16R |
|  | AJ55TB2-16T |
|  | AJ55TB2-4R |
|  | AJ55TB2-4T |
|  | AJ55TB2-8R |
|  | AJ55TB2-8T |
|  | AJ55TB3-16D |
|  | AJ55TB32-16DR |
| MODBUS interface module | AJ55TB32-16DT |
|  | AJ55TB32-4DR |
|  | AJ55TB32-4DT |
|  | AJ55TB32-8DR |
|  | AJ55TB32-8DT |
|  | AJ55TB3-4D |
|  | AJ55TB3-8D |

11. Models to be discontinued (announced before September, 2012)

| Product | Model | Production discontinuation |
| :---: | :---: | :---: |
| CPU module | A1SCPU | Through September, 1998 |
|  | A1SJCPU | Through March, 1996 |
|  | A1SJCPU-E | Through December, 1999 |
|  | A1SJCPU-S3 | Through September, 1998 |
|  | A1SJCPU-S3-E | Through March, 2003 |
|  | A2SCPU | Through September, 1998 |
|  | A2SCPU-S1 | Through August, 1998 |
|  | A2USCPU-S1 | Through December, 1999 |
| Power supply module | A1S61P | Through December, 1997 |
|  | A1S61PEU | Through December, 1997 |
|  | A1S61PUL | Through December, 1999 |
|  | A1S62P | Through December, 1997 |
|  | A1S62PEU | Through December, 1997 |
|  | A1S62PUL | Through December, 1999 |
| Memory cassette | A1SMCA-2KE | Through December, 1999 |
|  | A1SMCA-8KE | Through December, 1999 |
|  | A1SMCA-8KP | Through December, 1999 |
|  | A2SMCA-14KE | Through December, 1999 |
|  | A2SMCA-14KP | Through August, 2002 |
|  | A2SMCA-30KE | Through December, 1999 |
|  | A2SMCA-30KP | Through December, 1999 |
| Memory card | Q1MEM-256SF | Through August, 2002 |
|  | Q1MEM-512SF | Through August, 2002 |
|  | Q1MEM-1MSF | Through August, 2002 |
|  | Q1MEM-2MSF | Through August, 2002 |
| Memory card interface module | A1SD59J-S2 | Through April, 2010 |
|  | A1SD59J-MIF | Through April, 2011 |
| Extension cable | A1SC05NB | Through September, 2008 |
|  | A1SC07NB | Through September, 2008 |
|  | A1SC30NB | Through September, 2008 |
|  | A1SC50NB | Through September, 2008 |
| Output module | A1SY28EU | Through March, 2003 |
|  | A1SY42 | Through August, 2004 |
|  | A1SY81EP | Through December, 1999 |
| I/O combined module | A1SJ-56DT | Through April, 2010 |
|  | A1SJ-56DR | Through April, 2010 |
| Terminal block cover | A1STEC-S | Through October, 2010 |
| Analog timer module | A1ST60 | Through October, 2010 |
| Pulse catch module | A1SP60 | Through April, 2011 |
| Positioning module | A1SD70 | Through September 2013 (planned) |
|  | A1SD71-S2 | Through October, 2004 |
|  | A1SD71-S7 | Through October, 2004 |

## [Issue No.] FA-A-0142-C

| Product | Model | Production discontinuation |
| :---: | :---: | :---: |
| Temperature control module | A1S62TCRTBW-S2 | Through May, 2007 |
|  | A1S62TCRT-S2 | Through May, 2007 |
|  | A1S62TCTTBW-S2 | Through May, 2007 |
|  | A1S62TCTT-S2 | Through May, 2007 |
|  | A1S64TCRTBW-S1 | Through May, 2007 |
|  | A1S64TCRT-S1 | Through May, 2007 |
|  | A1S64TCTT | Through December, 1999 |
|  | A1S64TCTTBW-S1 | Through May, 2007 |
|  | A1S64TCTT-S1 | Through May, 2007 |
| Ethernet interface module | A1SJ71QE71-B2 | Through February, 2002 |
|  | A1SJ71QE71-B5 | Through February, 2002 |
|  | A1SJ71QE71N-B2 | Through January, 2011 |
|  | A1SJ71QE71N-B5 | Through January, 2011 |
|  | A1SJ71QE71N-B5T | Through July, 2003 |
|  | A1SJ71E71-B2 | Through September, 1996 |
|  | A1SJ71E71-B5 | Through September, 1996 |
|  | A1SJ71E71-B2-S3 | Through December, 2001 |
|  | A1SJ71E71-B5-S3 | Through December, 2001 |
|  | A1SJ71E71N-B2 | Through January, 2011 |
|  | A1SJ71E71N-B5 | Through January, 2011 |
|  | A1SJ71E71N-B5T | Through July, 2003 |
|  | A1SJ71E71N-T | Through July, 2005 |
| Serial communication module | A1SJ71QC24 | Through December, 2004 |
|  | A1SJ71QC24-R2 | Through December, 2004 |
|  | A1SJ71QC24N | Through June, 2006 |
|  | A1SJ71QC24N-R2 | Through June, 2006 |
| Modem interface module | A1SJ71CMO-S3 | Through January, 2010 |
| Intelligent communication module | A1SD51S | Through June, 2010 |
| MELSECNET(II)/B data link module | A1SJ71AP21-S3 | Through September, 2008 |
|  | A1SJ71T21B | Through December, 1999 |
|  | A1SJ72T25B | Through September, 2008 |
| MELSECNET/MINI-S3 master module | A1SJ71PT32-S3 | Through September, 2008 |
|  | A1SJ71T32-S3 | Through March, 2003 |
| ME-NET interface module | A1SJ71ME81 | Through February, 2004 |
| S-LINK system master module | A1SJ71SL92 | Through March, 2000 |
|  | A1SJ71SL92N | Through July, 2010 |
| JEMANET(OPCN-1) interface module | A1SJ72J95 | Through October, 2008 |
| B/NET interface module | A1SJ71B62-S3 | Through November, 2011 |
| ID interface module | A1SJ71ID1-R4 | Through September, 2001 |
|  | A1SJ71ID2-R4 | Through September, 2001 |
|  | A1S32ID1 | Through September, 2001 |
|  | A1S32ID2 | Through September, 2001 |
|  | A1S35ID1 | Through January, 2011 |
|  | A1S35ID2 | Through January, 2011 |
| ROM writer module | A2SWA-28AP | Through December, 1999 |
|  | A2SWA-28P | Through August, 2002 |
| Cap | A1SCAP | Through December, 1999 |
|  | A1SCCA | Through December, 1999 |
| Paging interface module | A1SD21-S1 | Through May, 2002 |

## [lssue No.] FA-A-0142-C

| Product | Model | Production discontinuation |
| :--- | :--- | :--- |
| PROFIBUS interface module | A1SJ71PB96F | Through February, 2011 |

## 12. List of alternative models

### 12.1 CPU module

### 12.1.1 Replacement with $L$ series

| AnS/QnAS series model |  | L series alternative model |  |
| :--- | :--- | :--- | :--- | :--- |
| Product | Model | Model | Remarks (restrictions) |

[^0]
## [Issue No.] FA-A-0142-C

| AnS/QnAS series model |  | L series alternative model |  |
| :---: | :---: | :---: | :---: |
| Product | Model | Model | Remarks (restrictions) |
| CPU module | A2SHCPU | $\begin{aligned} & \text { L02CPU/ } \\ & \text { L02CPU-P } \end{aligned}$ | 1) I/O control: Selectable (refresh or direct mode) $\rightarrow$ Refresh mode only <br> 2) Processing speed (LD instruction): During refresh $0.25 \mu \mathrm{~s} \rightarrow 0.04 \mu \mathrm{~s}$ <br> 3) PC MIX value: $0.5 \rightarrow 14$ <br> 4) Number of I/O points: $512 \rightarrow 1024$ <br> 5) Number of I/O device points: $2048 \rightarrow 8192$ <br> 6) Program capacity: 14 K steps $\rightarrow 20 \mathrm{~K}$ steps <br> 7) Number of file register points: $8 \mathrm{~K} \rightarrow 64 \mathrm{~K}$ <br> 8) Built-in function: None $\rightarrow$ Built-in I/O function and Ethernet function <br> 9) Number of extension base units: 1 base unit (max. 16 slots) $\rightarrow 2$ blocks (max. 30 modules) <br> 10) Applicable memory: Built-in RAM or $E^{2} P R O M$ cassette (sold separately) <br> $\rightarrow$ Program memory, standard RAM, standard ROM, or memory card (sold separately) <br> 11) Microcomputer program: Available $\rightarrow$ Not available <br> 12) Configuration: Modules are mounted on a base unit. <br> $\rightarrow$ Modules are connected. (No base unit is required.) |
|  | A2USCPU | $\begin{aligned} & \text { L02CPU/ } \\ & \text { L02CPU-P } \end{aligned}$ | 1) I/O control: Refresh mode only <br> 2) Processing speed (LD instruction): $0.2 \mu \mathrm{~s} \rightarrow 0.04 \mu \mathrm{~s}$ <br> 3) PC MIX value: $0.9 \rightarrow 14$ <br> 4) Number of I/O points: $512 \rightarrow 1024$ <br> 5) Number of I/O device points: $8192 \rightarrow 8192$ <br> 6) Program capacity: 14 K steps $\rightarrow 20 \mathrm{~K}$ steps <br> 7) Number of file register points: $8 \mathrm{~K} \rightarrow 64 \mathrm{~K}$ <br> 8) Built-in function: None $\rightarrow$ Built-in I/O function and Ethernet function <br> 9) Number of extension base units: 1 base unit (max. 16 slots) $\rightarrow 2$ blocks (max. 30 modules) <br> 10) Applicable memory: Built-in RAM or $E^{2}$ PROM cassette (sold separately) <br> $\rightarrow$ Program memory, standard RAM, standard ROM, or memory card (sold separately) <br> 11) Microcomputer program: Not available <br> 12) Configuration: Modules are mounted on a base unit. <br> $\rightarrow$ Modules are connected. (No base unit is required.) <br> 13) Sequence instruction: AnA/AnU dedicated instructions are replaceable. ${ }^{*}$ |
|  | A2USHCPU-S1 | $\begin{aligned} & \text { L02CPU/ } \\ & \text { L02CPU-P } \end{aligned}$ | 1) I/O control: Refresh mode only <br> 2) Processing speed (LD instruction): $0.09 \mu \mathrm{~s} \rightarrow 0.04 \mu \mathrm{~s}$ <br> 3) PC MIX value: $2.0 \rightarrow 14$ <br> 4) Number of I/O points: $1024 \rightarrow 1024$ <br> 5) Number of I/O device points: $8192 \rightarrow 8192$ <br> 6) Program capacity: 30 K steps $\rightarrow 20 \mathrm{~K}$ steps <br> 7) Number of file register points: $8 \mathrm{~K} \rightarrow 64 \mathrm{~K}$ <br> 8) Built-in function: None $\rightarrow$ Built-in I/O function and Ethernet function <br> 9) Number of extension base units: 1 base unit (max. 16 slots) $\rightarrow 2$ blocks (max. 30 modules) <br> 10) Applicable memory: Built-in RAM or $E^{2}$ PROM cassette (sold separately) <br> $\rightarrow$ Program memory, standard RAM, standard ROM, or memory card (sold separately) <br> 11) Microcomputer program: Not available <br> 12) Configuration: Modules are mounted on a base unit. <br> $\rightarrow$ Modules are connected. (No base unit is required.) <br> 13) Sequence instruction: AnA/AnU dedicated instructions are replaceable. ${ }^{*}$ |
|  |  | L26CPU-BT/ <br> L26CPU-PBT | 1) I/O control: Refresh mode only <br> 2) Processing speed (LD instruction): $0.09 \mu \mathrm{~s} \rightarrow 0.0095 \mu \mathrm{~s}$ <br> 3) PC MIX value: $2.0 \rightarrow 60$ <br> 4) Number of I/O points: $1024 \rightarrow 4096$ <br> 5) Number of I/O device points: $8192 \rightarrow 8192$ <br> 6) Program capacity: 30 K steps $\rightarrow 260 \mathrm{~K}$ steps <br> 7) Number of file register points: $8 \mathrm{~K} \rightarrow 384 \mathrm{~K}$ <br> 8) Built-in function: None $\rightarrow$ Built-in I/O function, Ethernet function, and CC-Link function <br> 9) Number of extension base units: 1 base unit (max. 16 slots) <br> $\rightarrow 3$ blocks (max. 40 modules) <br> 10) Applicable memory: Built-in RAM or $E^{2} P R O M$ cassette (sold separately) $\rightarrow$ Program memory, Standard RAM, and Standard ROM <br> 11) Microcomputer program: Not available <br> 12) Configuration: Modules are mounted on a base unit. $\rightarrow$ Modules are connected. (No base unit is required.) <br> 13) Sequence instruction: AnA/AnU dedicated instructions are replaceable. ${ }^{*}{ }^{2}$ |

[^1]

| AnS/QnAS series model |  | L series alternative model |  |
| :---: | :---: | :---: | :---: |
| Product | Model | Model | Remarks (restrictions) |
| CPU module | Q2ASHCPU | $\begin{aligned} & \text { L02CPU/ } \\ & \text { L02CPU-P } \end{aligned}$ | 1) I/O control: Refresh mode only <br> 2) Processing speed (LD instruction): $0.075 \mu \mathrm{~s} \rightarrow 0.04 \mu \mathrm{~s}$ <br> 3) PC MIX value: $3.8 \rightarrow 14$ <br> 4) Number of I/O points: $512 \rightarrow 1024$ <br> 5) Number of I/O device points: $8192 \rightarrow 8192$ <br> 6) Program capacity: 28 K steps $\rightarrow 20 \mathrm{~K}$ steps <br> 7) Number of file register points: 0 K (Memory card (sold separately) is necessary.) $\rightarrow 64 \mathrm{~K}$ <br> 8) Built-in function: None $\rightarrow$ Built-in I/O function and Ethernet function <br> 9) Number of extension base units: 1 base unit (max. 16 slots) $\rightarrow 2$ blocks (max. 30 modules) <br> 10) Applicable memory: Built-in RAM or memory card (sold separately) <br> $\rightarrow$ Program memory, standard RAM, standard ROM, or memory card (sold separately) <br> 11) Microcomputer program: Not available <br> 12) Configuration: Modules are mounted on a base unit. <br> $\rightarrow$ Modules are connected. (No base unit is required.) |
|  |  | L26CPU-BT/ <br> L26CPU-PBT | 1) I/O control: Refresh mode only <br> 2) Processing speed (LD instruction): $0.075 \mu \mathrm{~s} \rightarrow 0.0095 \mu \mathrm{~s}$ <br> 3) PC MIX value: $1.3 \rightarrow 60$ <br> 4) Number of I/O points: $512 \rightarrow 4096$ <br> 5) Number of I/O device points: $8192 \rightarrow 8192$ <br> 6) Program capacity: 28 K steps $\rightarrow 260 \mathrm{~K}$ steps <br> 7) Number of file register points: OK (Memory card (sold separately) is necessary.) $\rightarrow 384 \mathrm{~K}$ <br> 8) Built-in function: None $\rightarrow$ Built-in I/O function, Ethernet function, and CC-Link function <br> 9) Number of extension base units: 1 base unit (max. 16 slots) <br> $\rightarrow 3$ blocks (max. 40 modules) <br> 10) Applicable memory: Built-in RAM or memory card (sold separately) <br> $\rightarrow$ Program memory, standard RAM, standard ROM, or memory card (sold separately) <br> 11) Microcomputer program: Not available <br> 12) Configuration: Modules are mounted on a base unit. <br> $\rightarrow$ Modules are connected. (No base unit is required.) |
|  | Q2ASHCPU-S1 | $\begin{aligned} & \text { L02CPU/ } \\ & \text { L02CPU-P } \end{aligned}$ | 1) I/O control: Refresh mode only <br> 2) Processing speed (LD instruction): $0.075 \mu \mathrm{~s} \rightarrow 0.04 \mu \mathrm{~s}$ <br> 3) PC MIX value: $3.8 \rightarrow 14$ <br> 4) Number of I/O points: $1024 \rightarrow 1024$ <br> 5) Number of I/O device points: $8192 \rightarrow 8192$ <br> 6) Program capacity: 60 K steps $\rightarrow 20 \mathrm{~K}$ steps <br> 7) Number of file register points: OK (Memory card (sold separately) is necessary.) $\rightarrow 64 \mathrm{~K}$ <br> 8) Built-in function: None $\rightarrow$ Built-in I/O function and Ethernet function <br> 9) Number of extension base units: 1 base unit (max. 16 slots) $\rightarrow 2$ blocks (max. 30 modules) <br> 10) Applicable memory: Built-in RAM or memory card (sold separately) <br> $\rightarrow$ Program memory, standard RAM, standard ROM, or memory card (sold separately) <br> 11) Microcomputer program: Not available <br> 12) Configuration: Modules are mounted on a base unit. <br> $\rightarrow$ Modules are connected. (No base unit is required.) |
|  |  | L26CPU-BT/ <br> L26CPU-PBT | 1) I/O control: Refresh mode only <br> 2) Processing speed (LD instruction): $0.075 \mu \mathrm{~s} \rightarrow 0.0095 \mu \mathrm{~s}$ <br> 3) PC MIX value: $1.3 \rightarrow 60$ <br> 4) Number of I/O points: $1024 \rightarrow 4096$ <br> 5) Number of I/O device points: $8192 \rightarrow 8192$ <br> 6) Program capacity: 60 K steps $\rightarrow 260 \mathrm{~K}$ steps <br> 7) Number of file register points: OK (Memory card (sold separately) is necessary.) $\rightarrow 384 \mathrm{~K}$ <br> 8) Built-in function: None $\rightarrow$ Built-in I/O function, Ethernet function, and CC-Link function <br> 9) Number of extension base units: 1 base unit (max. 16 slots) <br> $\rightarrow 3$ blocks (max. 40 modules) <br> 10) Applicable memory: Built-in RAM or memory card (sold separately) <br> $\rightarrow$ Program memory, standard RAM, standard ROM, or memory card (sold separately) <br> 11) Microcomputer program: Not available <br> 12) Configuration: Modules are mounted on a base unit. <br> $\rightarrow$ Modules are connected. (No base unit is required.) |

### 12.1.2 Replacement with $Q$ series

| AnS/QnAS series model |  | Q series alternative model |  |
| :---: | :---: | :---: | :---: |
| Product | Model | Model | Remarks (restrictions) |
| CPU module | A1SJHCPU | Q00UJCPU | 1) I/O control: Selectable (refresh or direct mode) $\rightarrow$ Refresh mode only <br> 2) Processing speed (LD instruction) : During refresh $0.33 \mu \mathrm{~s} \rightarrow 0.12 \mu \mathrm{~s}$ <br> 3) PC MIX value: $0.4 \rightarrow 4.92$ <br> 4) Number of I/O points: 256 <br> 5) Number of I/O device points: $2048 \rightarrow 8192$ <br> 6) Program capacity: 8 k steps $\rightarrow 10 \mathrm{k}$ steps <br> 7) Number of file register points: $8 \mathrm{k} \rightarrow 0$ <br> 8) Number of extension base units: $2 \rightarrow 2$ (Up to 1 extension base unit when a GOT is connected in a bus topology) <br> 9) Applicable memory: Built-in RAM/E ${ }^{2}$ PROM cassette (sold separately) $\rightarrow$ program memory/Standard ROM <br> 10) Microcomputer program: Available $\rightarrow$ Not available <br> 11) Configuration: Including 5 slot base unit, CPU module and power supply module |
|  |  | Q00UCPU | 1) I/O control: Selectable (refresh or direct mode) $\rightarrow$ Refresh mode only <br> 2) Processing speed (LD instruction) : During refresh $0.33 \mu \mathrm{~s} \rightarrow 0.08 \mu \mathrm{~s}$ <br> 3) PC MIX value: $0.4 \rightarrow 7.36$ <br> 4) Number of I/O points: $256 \rightarrow 1024$ <br> 5) Number of I/O device points: $2048 \rightarrow 8192$ <br> 6) Program capacity: 8 k steps $\rightarrow 10 \mathrm{k}$ steps <br> 7) Number of file register points: $8 k \rightarrow 64 k$ <br> 8) Number of extension base units: $1 \rightarrow 4$ (Up to 3 extension base units when a GOT is connected in a bus topology) <br> 9) Applicable memory: Built-in RAM/E ${ }^{2}$ PROM cassette (sold separately) <br> $\rightarrow$ program memory/Standard RAM/Standard ROM <br> 10) Microcomputer program: Available $\rightarrow$ Not available |
|  | $\begin{aligned} & \text { A1SCPUC24-R2*1 } \\ & \text { A1SHCPU } \end{aligned}$ | Q00UCPU | 1) I/O control: Selectable (refresh or direct mode) $\rightarrow$ Refresh mode only <br> 2) Processing speed (LD instruction): During refresh $0.33 \mu \mathrm{~s} \rightarrow 0.08 \mu \mathrm{~s}$ <br> 3) PC MIX value: $0.4 \rightarrow 7.36$ <br> 4) Number of I/O points: $256 \rightarrow 1024$ <br> 5) Number of I/O device points: $2048 \rightarrow 8192$ <br> 6) Program capacity: 8 k steps $\rightarrow 10 \mathrm{k}$ steps <br> 7) Number of file register points: $8 k \rightarrow 64 k$ <br> 8) Number of extension base units: $1 \rightarrow 4$ (Up to 3 extension base units when a GOT is connected in a bus topology) <br> 9) Applicable memory: Built-in RAM/E ${ }^{2}$ PROM cassette (sold separately) $\rightarrow$ program memory/Standard RAM/Standard ROM <br> 10) Microcomputer program: Available $\rightarrow$ Not available |

[^2]| AnS/QnAS series model |  | Q series alternative model |  |
| :---: | :---: | :---: | :---: |
| Product | Model | Model | Remarks (restrictions) |
| CPU module | A2SHCPU | Q01UCPU | 1) I/O control: Selectable (refresh or direct mode) $\rightarrow$ Refresh mode only <br> 2) Processing speed (LD instruction): During refresh $0.25 \mu \mathrm{~s} \rightarrow 0.06 \mu \mathrm{~s}$ <br> 3) PC MIX value: $0.5 \rightarrow 9.79$ <br> 4) Number of I/O points: $512 \rightarrow 1024$ <br> 5) Number of I/O device points: $2048 \rightarrow 8192$ <br> 6) Program capacity: 14 k steps $\rightarrow 15 \mathrm{k}$ steps <br> 7) Number of file register points: $8 \mathrm{k} \rightarrow 64 \mathrm{k}$ <br> 8) Number of extension base units: $1 \rightarrow 4$ (Up to 3 extension base units when a GOT is connected in a bus topology) <br> 9) Applicable memory: Built-in RAM/E ${ }^{2}$ PROM cassette (sold separately) <br> $\rightarrow$ program memory/Standard RAM/Standard ROM <br> 10) Microcomputer program: Available $\rightarrow$ Not available |
|  | A2USCPU | Q02UCPU | 1) I/O control: Refresh mode only <br> 2) Processing speed (LD instruction): $0.2 \mu \mathrm{~s} \rightarrow 0.04 \mu \mathrm{~s}$ <br> 3) PC MIX value: $0.9 \rightarrow 14$ <br> 4) Number of I/O points: $512 \rightarrow 2048$ <br> 5) Number of I/O device points: $8192 \rightarrow 8192$ <br> 6) Program capacity: 14 k steps $\rightarrow 20 \mathrm{k}$ steps <br> 7) Number of file register points: $8 \mathrm{k} \rightarrow 64 \mathrm{k}$ (Using memory card: max. 4086k points) <br> 8) Number of extension base units: $1 \rightarrow 4$ (Up to 3 extension base units when a GOT is connected in a bus topology) <br> 9) Applicable memory: Built-in RAM/E ${ }^{2}$ PROM cassette (sold separately) $\rightarrow$ program memory/Standard RAM/Standard ROM/memory card (sold separately) <br> 10) Microcomputer program: Not available <br> 11) Sequence instruction: AnA/AnU-dedicated instructions are replaceable. ${ }^{* 2}$ |
|  | A2USHCPU-S1 | Q02UCPU | 1) I/O control: Refresh mode only <br> 2) Processing speed (LD instruction): $0.09 \mu \mathrm{~s} \rightarrow 0.04 \mu \mathrm{~s}$ <br> 3) PC MIX value: $2.0 \rightarrow 14$ <br> 4) Number of I/O points: $1024 \rightarrow 2048$ <br> 5) Number of I/O device points: $8192 \rightarrow 8192$ <br> 6) Program capacity: 30 k steps $\rightarrow 20 \mathrm{k}$ steps <br> 7) Number of file register points: $8 \mathrm{k} \rightarrow 64 \mathrm{k}$ (Using memory card: max. 4086k points) <br> 8) Number of extension base units: $1 \rightarrow 4$ (Up to 3 extension base units when a GOT is connected in a bus topology) <br> 9) Applicable memory: Built-in RAM/E ${ }^{2}$ PROM cassette (sold separately) $\rightarrow$ program memory/Standard RAM/Standard ROM/memory card (sold separately) <br> 10) Microcomputer program: Not available <br> 11) Sequence instruction: AnA/AnU-dedicated instructions are replaceable. ${ }^{*}{ }^{2}$ |
|  |  | Q03UDCPU | 1) I/O control: Refresh mode only <br> 2) Processing speed (LD instruction): $0.09 \mu \mathrm{~s} \rightarrow 0.02 \mu \mathrm{~s}$ <br> 3) PC MIX value: $2.0 \rightarrow 28$ <br> 4) Number of I/O points: $1024 \rightarrow 4096$ <br> 5) Number of I/O device points: $8192 \rightarrow 8192$ <br> 6) Program capacity: 30 k steps $\rightarrow 30 \mathrm{k}$ steps <br> 7) Number of file register points: $8 \mathrm{k} \rightarrow 96 \mathrm{k}$ (Using memory card: max. 4086k points) <br> 8) Number of extension base units: $1 \rightarrow 7$ <br> 9) Applicable memory: Built-in RAM/E ${ }^{2}$ PROM cassette (sold separately) $\rightarrow$ program memory/Standard RAM/Standard ROM/memory card (sold separately) <br> 10) Microcomputer program: Not available <br> 11) Sequence instruction: AnA/AnU-dedicated instructions are replaceable. ${ }^{* 2}$ |

[^3]| AnS/QnAS series model |  | Q series alternative model |  |
| :---: | :---: | :---: | :---: |
| Product | Model | Model | Remarks (restrictions) |
| CPU module | Q2ASCPU | Q02UCPU | 1) I/O control: Refresh mode only <br> 2) Processing speed (LD instruction): $0.2 \mu \mathrm{~s} \rightarrow 0.04 \mu \mathrm{~s}$ <br> 3) PC MIX value: $1.3 \rightarrow 14$ <br> 4) Number of I/O points: $512 \rightarrow 2048$ <br> 5) Number of I/O device points: $8192 \rightarrow 8192$ <br> 6) Program capacity: 28 k steps $\rightarrow 20 \mathrm{k}$ steps <br> 7) Number of file register points: $0 k$ (Memory card (sold separately) is necessary.) <br> $\rightarrow 64 \mathrm{k}$ (Using memory card: max. 4086k points) <br> 8) Number of extension base units: $1 \rightarrow 4$ (Up to 3 extension base units when a GOT is connected in a bus topology) <br> 9) Applicable memory: Built-in RAM/memory card (sold separately) $\rightarrow$ program memory/Standard RAM/Standard ROM/memory card (sold separately) <br> 10) Microcomputer program: Not available |
|  |  | Q03UDCPU | 1) I/O control: Refresh mode only <br> 2) Processing speed (LD instruction): $0.2 \mu \mathrm{~s} \rightarrow 0.02 \mu \mathrm{~s}$ <br> 3) PC MIX value: $1.3 \rightarrow 28$ <br> 4) Number of I/O points: $512 \rightarrow 4096$ <br> 5) Number of I/O device points: $8192 \rightarrow 8192$ <br> 6) Program capacity: 14 k steps $\rightarrow 30 \mathrm{k}$ steps <br> 7) Number of file register points: 0 k (Memory card (sold separately) is necessary.) <br> $\rightarrow 96 \mathrm{k}$ (Using memory card: max. 4086k points) <br> 8) Number of extension base units: $1 \rightarrow 7$ <br> 9) Applicable memory: program memory/memory card (sold separately) $\rightarrow$ program memory/Standard RAM/Standard ROM/memory card (sold separately) <br> 10) Microcomputer program: Not available |
|  | Q2ASCPU-S1 | Q04UDHCPU | 1) I/O control: Refresh mode only <br> 2) Processing speed (LD instruction): $0.2 \mu \mathrm{~s} \rightarrow 0.0095 \mu \mathrm{~s}$ <br> 3) PC MIX value: $1.3 \rightarrow 60$ <br> 4) Number of I/O points: $1024 \rightarrow 4096$ <br> 5) Number of I/O device points: $8192 \rightarrow 8192$ <br> 6) Program capacity: 60 k steps $\rightarrow 40 \mathrm{k}$ steps <br> 7) Number of file register points: 0k (Memory card (sold separately) is necessary.) <br> $\rightarrow 128 \mathrm{k}$ (Using memory card: max. 4086k points) <br> 8) Number of extension base units: $1 \rightarrow 7$ <br> 9) Applicable memory: program memory/memory card (sold separately) <br> $\rightarrow$ program memory/Standard RAM/Standard ROM/memory card (sold separately) <br> 10) Microcomputer program: Not available |


| AnS/QnAS series model |  | Q series alternative model |  |
| :---: | :---: | :---: | :---: |
| Product | Model | Model | Remarks (restrictions) |
| CPU module | Q2ASCPU-S1 | Q06UDHCPU | 1) I/O control: Refresh mode only <br> 2) Processing speed (LD instruction): $0.2 \mu \mathrm{~s} \rightarrow 0.0095 \mu \mathrm{~s}$ <br> 3) PC MIX value: $1.3 \rightarrow 60$ <br> 4) Number of I/O points: $1024 \rightarrow 4096$ <br> 5) Number of I/O device points: $8192 \rightarrow 8192$ <br> 6) Program capacity: 60 k steps $\rightarrow 60 \mathrm{k}$ steps <br> 7) Number of file register points: 0 k (Memory card (sold separately) is necessary.) $\rightarrow 384 \mathrm{k}$ (Using memory card: max. 4086k points) <br> 8) Number of extension base units: $1 \rightarrow 7$ <br> 9) Applicable memory: program memory/memory card (sold separately) $\rightarrow$ program memory/Standard RAM/Standard ROM/memory card (sold separately) <br> 10) Microcomputer program: Not available |
|  | Q2ASHCPU | Q02UCPU | 1) I/O control: Refresh mode only <br> 2) Processing speed (LD instruction) : $0.075 \mu \mathrm{~s} \rightarrow 0.04 \mu \mathrm{~s}$ <br> 3) PC MIX value: $3.8 \rightarrow 14$ <br> 4) Number of I/O points: $512 \rightarrow 2048$ <br> 5) Number of I/O device points: $8192 \rightarrow 8192$ <br> 6) Program capacity: 28 k steps $\rightarrow 20 \mathrm{k}$ steps <br> 7) Number of file register points: Ok (Memory card (sold separately) is necessary.) <br> $\rightarrow 64 \mathrm{k}$ (Using memory card: max. 4086k points) <br> 8) Number of extension base units: $1 \rightarrow 4$ (Up to 3 extension base units when a GOT is connected in a bus topology) <br> 9) Applicable memory: program memory/memory card (sold separately) $\rightarrow$ program memory/Standard RAM/Standard ROM/memory card (sold separately) <br> 10) Microcomputer program: Not available |
|  |  | Q03UDCPU | 1) I/O control: Refresh mode only <br> 2) Processing speed (LD instruction) : $0.075 \mu \mathrm{~s} \rightarrow 0.02 \mu \mathrm{~s}$ <br> 3) PC MIX value: $3.8 \rightarrow 28$ <br> 4) Number of I/O points: $512 \rightarrow 4096$ <br> 5) Number of I/O device points: $8192 \rightarrow 8192$ <br> 6) Program capacity: 28 k steps $\rightarrow 30 \mathrm{k}$ steps <br> 7) Number of file register points: Ok (Memory card (sold separately) is necessary.) <br> $\rightarrow 96 \mathrm{k}$ (Using memory card: max. 4086k points) <br> 8) Number of extension base units: $1 \rightarrow 7$ <br> 9) Applicable memory: program memory/memory card (sold separately) <br> $\rightarrow$ program memory/Standard RAM/Standard ROM/memory card (sold separately) <br> 10) Microcomputer program: Not available |
|  | Q2ASHCPU-S1 | Q04UDHCPU | 1) I/O control: Refresh mode only <br> 2) Processing speed (LD instruction): $0.075 \mu \mathrm{~s} \rightarrow 0.0095 \mu \mathrm{~s}$ <br> 3) PC MIX value: $3.8 \rightarrow 60$ <br> 4) Number of I/O points: $1024 \rightarrow 4096$ <br> 5) Number of I/O device points: $8192 \rightarrow 8192$ <br> 6) Program capacity: 60k steps $\rightarrow 40 \mathrm{k}$ steps <br> 7) Number of file register points: 0 k (Memory card (sold separately) is necessary.) <br> $\rightarrow 128 \mathrm{k}$ (Using memory card: max. 4086 k points) <br> 8) Number of extension base units: $1 \rightarrow 7$ <br> 9) Applicable memory: program memory/memory card (sold separately) <br> $\rightarrow$ program memory/Standard RAM/Standard ROM/memory card (sold separately) <br> 10) Microcomputer program: Not available |
|  |  | Q06UDHCPU | 1) I/O control: Refresh mode only <br> 2) Processing speed (LD instruction): $0.075 \mu \mathrm{~s} \rightarrow 0.0095 \mu \mathrm{~s}$ <br> 3) PC MIX value: $3.8 \rightarrow 60$ <br> 4) Number of I/O points: $1024 \rightarrow 4096$ <br> 5) Number of I/O device points: $8192 \rightarrow 8192$ <br> 6) Program capacity: 60 k steps <br> 7) Number of file register points: 0k (Memory card (sold separately) is necessary.) <br> $\rightarrow 384 \mathrm{k}$ (Using memory card: max. 4086k points) <br> 8) Number of extension base units: $1 \rightarrow 7$ <br> 9) Applicable memory: program memory/memory card (sold separately) $\rightarrow$ program memory/Standard RAM/Standard ROM/memory card (sold separately) <br> 10) Microcomputer program: Not available |

## [Issue No.] FA-A-0142-C

### 12.2 I/O module

### 12.2.1 Replacement with $L$ series

| AnS/QnAS series model |  | L series alternative model |  |
| :---: | :---: | :---: | :---: |
| Product | Model | Model | Remarks (restrictions) |
| Input <br> module | A1SX10 | LX10 | 1) External wiring: Changed <br> 2) Number of slots: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> 4) Specifications: <br> Rated input voltage: Not changed <br> Operating voltage range: Not changed <br> Rated input current: Changed (approx. $6 \mathrm{~mA}(60 \mathrm{~Hz}) \rightarrow 8.2 \mathrm{~mA}(60 \mathrm{~Hz})$ ) <br> ON voltage/ON current: Not changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Changed <br> 5) Functions: Not changed |
|  | A1SX10EU | LX10 | 1) External wiring: Changed <br> 2) Number of slots: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> 4) Specifications: <br> Rated input voltage: Not changed <br> Operating voltage range: Not changed <br> Rated input current: Changed (approx. $7 \mathrm{~mA}(60 \mathrm{~Hz}) \rightarrow 8.2 \mathrm{~mA}(60 \mathrm{~Hz})$ ) <br> ON voltage/ON current: Not changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Changed <br> 5) Functions: Not changed |
|  | A1SX20 | LX28 | 1) External wiring: Changed <br> 2) Number of slots: Changed (Two modules are required.) <br> 3) Program: <br> Number of occupied I/O points: Changed <br> 4) Specifications: <br> Rated input voltage: Changed ( 200 to 240 VAC $\rightarrow 100$ to 240 VAC) <br> Operating voltage range: Not changed <br> Rated input current: Changed (approx. $9 \mathrm{~mA}(60 \mathrm{~Hz}) \rightarrow 16.4 \mathrm{~mA}(60 \mathrm{~Hz})$ ) <br> ON voltage/ON current: Changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Changed <br> 5) Functions: Not changed |


| AnS/QnAS series model |  | L series alternative model |  |
| :---: | :---: | :---: | :---: |
| Product | Model | Model | Remarks (restrictions) |
| Input <br> module | A1SX20EU | LX28 | 1) External wiring: Changed <br> 2) Number of slots: Changed (Two modules are required.) <br> 3) Program: <br> Number of occupied I/O points: Changed <br> 4) Specifications: <br> Rated input voltage: Changed ( 200 to 240 VAC $\rightarrow 100$ to 240VAC) <br> Operating voltage range: Not changed <br> Rated input current: Changed (approx. $11 \mathrm{~mA}(60 \mathrm{~Hz}) \rightarrow 16.4 \mathrm{~mA}(60 \mathrm{~Hz})$ ) <br> ON voltage/ON current: Changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Changed <br> 5) Functions: Not changed |
|  | A1SX30 | LX40C6 | 1) External wiring: Changed <br> 2) Number of slots: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> 4) Specifications: <br> Rated input voltage: Changed (12VDC is not applicable. 12VAC and 24VAC are not applicable.) <br> Rated input current: Changed $(8.5 \mathrm{~mA} \rightarrow 6 \mathrm{~mA})$ <br> ON voltage/ON current: Changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Changed <br> 5) Functions: Not changed <br> [When applying AC input] <br> Convert 12/24VAC to DC externally before input to the LX40C6. |
|  | A1SX40 | LX40C6 | 1) External wiring: Changed <br> 2) Number of slots: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> 4) Specifications: <br> Rated input voltage: Changed (12VDC is not applicable.) <br> Rated input current: Changed (approx. $7 \mathrm{~mA} \rightarrow 6 \mathrm{~mA}$ ) <br> ON voltage/ON current: Changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Changed <br> 5) Functions: Not changed |
|  | A1SX40-S1 | LX40C6 | 1) External wiring: Changed <br> 2) Number of slots: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> 4) Specifications: <br> Rated input voltage: Not changed <br> Rated input current: Changed (approx. $7 \mathrm{~mA} \rightarrow 6 \mathrm{~mA}$ ) <br> ON voltage/ON current: Changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Changed <br> Response time: Changed <br> 5) Functions: Not changed |


| AnS/QnAS series model |  | L series alternative model |  |
| :---: | :---: | :---: | :---: |
| Product | Model | Model | Remarks (restrictions) |
| Input module | A1SX40-S2 | LX40C6 | 1) External wiring: Changed <br> 2) Number of slots: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> 4) Specifications: <br> Rated input voltage: Not changed <br> Rated input current: Changed (approx. $7 \mathrm{~mA} \rightarrow 6 \mathrm{~mA}$ ) <br> ON voltage/ON current: Changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Changed <br> 5) Functions: Not changed |
|  | A1SX41 | LX41C4 | 1) External wiring: Not changed <br> 2) Number of slots: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> 4) Specifications: <br> Rated input voltage: Changed (12VDC is not applicable.) <br> Rated input current: Changed (approx. $7 \mathrm{~mA} \rightarrow 4 \mathrm{~mA}$ ) <br> ON voltage/ON current: Changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Changed <br> 5) Functions: Not changed |
|  | A1SX41-S1 | LX41C4 | 1) External wiring: Not changed <br> 2) Number of slots: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> 4) Specifications: <br> Rated input voltage: Not changed <br> Rated input current: Changed (approx. $7 \mathrm{~mA} \rightarrow 4 \mathrm{~mA}$ ) <br> ON voltage/ON current: Changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Changed <br> Response time: Changed <br> 5) Functions: Not changed |
|  | A1SX41-S2 | LX41C4 | 1) External wiring: Not changed <br> 2) Number of slots: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> 4) Specifications: <br> Rated input voltage: Not changed <br> Rated input current: Changed (approx. $7 \mathrm{~mA} \rightarrow 4 \mathrm{~mA}$ ) <br> ON voltage/ON current: Changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Changed <br> 5) Functions: Not changed |


| AnS/QnAS series model |  | L series alternative model |  |
| :---: | :---: | :---: | :---: |
| Product | Model | Model | Remarks (restrictions) |
| Input module | A1SX42 | LX42C4 | 1) External wiring: Not changed <br> 2) Number of slots: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> 4) Specifications: <br> Rated input voltage: Changed (12VDC is not applicable.) <br> Rated input current: Changed (approx. $5 \mathrm{~mA} \rightarrow 4 \mathrm{~mA}$ ) <br> ON voltage/ON current: Changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Changed <br> 5) Functions: Not changed |
|  | A1SX42-S1 | LX42C4 | 1) External wiring: Not changed <br> 2) Number of slots: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> 4) Specifications: <br> Rated input voltage: Not changed <br> Rated input current: Changed (approx. $5 \mathrm{~mA} \rightarrow 4 \mathrm{~mA}$ ) <br> ON voltage/ON current: Changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Changed <br> Response time: Changed <br> 5) Functions: Not changed |
|  | A1SX42-S2 | LX42C4 | 1) External wiring: Not changed <br> 2) Number of slots: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> 4) Specifications: <br> Rated input voltage: Not changed <br> Rated input current: Changed (approx. $5 \mathrm{~mA} \rightarrow 4 \mathrm{~mA}$ ) <br> ON voltage/ON current: Changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Changed <br> 5) Functions: Not changed |
|  | A1SX71 | LX41C4 | 1) External wiring: Not changed <br> 2) Number of slots: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> 4) Specifications: <br> Rated input voltage: Changed (5VDC and 12VDC are not applicable.) <br> Rated input current: Changed (approx. $7 \mathrm{~mA} \rightarrow 4 \mathrm{~mA}$ ) <br> ON voltage/ON current: Changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Changed <br> 5) Functions: Not changed |


| AnS/QnAS series model |  | L series alternative model |  |
| :---: | :---: | :---: | :---: |
| Product | Model | Model | Remarks (restrictions) |
| Input <br> module | A1SX80 | LX40C6 | 1) External wiring: Changed <br> 2) Number of slots: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> 4) Specifications: <br> Rated input voltage: Changed (12VDC is not applicable.) <br> Rated input current: Changed (approx. $7 \mathrm{~mA} \rightarrow 6 \mathrm{~mA}$ ) <br> ON voltage/ON current: Changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Changed <br> 5) Functions: Not changed |
|  | A1SX80-S1 | LX40C6 | 1) External wiring: Changed <br> 2) Number of slots: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> 4) Specifications: <br> Rated input voltage: Not changed <br> Rated input current: Changed (approx. $7 \mathrm{~mA} \rightarrow 6 \mathrm{~mA}$ ) <br> ON voltage/ON current: Changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Changed <br> Response time: Changed <br> 5) Functions: Not changed |
|  | A1SX80-S2 | LX40C6 | 1) External wiring: Changed <br> 2) Number of slots: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> 4) Specifications: <br> Rated input voltage: Not changed <br> Rated input current: Changed (approx. $7 \mathrm{~mA} \rightarrow 6 \mathrm{~mA}$ ) <br> ON voltage/ON current: Changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Changed <br> 5) Functions: Not changed |
|  | A1SX81 | LX41C4 | 1) External wiring: Not changed <br> (37-pin D-sub connector $\rightarrow 40$-pin connector) <br> 2) Number of slots: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> 4) Specifications: <br> Rated input voltage: Changed (12VDC is not applicable.) <br> Rated input current: Changed (approx. $7 \mathrm{~mA} \rightarrow 4 \mathrm{~mA}$ ) <br> ON voltage/ON current: Changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Changed <br> 5) Functions: Not changed |


| AnS/QnAS series model |  | L series alternative model |  |
| :---: | :---: | :---: | :---: |
| Product | Model | Model | Remarks (restrictions) |
| Input module | A1SX81-S2 | LX41C4 | 1) External wiring: Changed <br> (37-pin D-sub connector $\rightarrow 40$-pin connector) <br> 2) Number of slots: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> 4) Specifications: <br> Rated input voltage: Not changed (12VDC is not applicable.) <br> Rated input current: Changed (approx. $7 \mathrm{~mA} \rightarrow 4 \mathrm{~mA}$ ) <br> ON voltage/ON current: Changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Changed <br> 5) Functions: Not changed |
|  | A1SX82-S1 | LX42C4 | 1) External wiring: Not changed <br> 2) Number of slots: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> 4) Specifications: <br> Rated input voltage: Not changed <br> Rated input current: Changed (approx. $5 \mathrm{~mA} \rightarrow 4 \mathrm{~mA}$ ) <br> ON voltage/ON current: Changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Changed <br> Response time: Changed <br> 5) Functions: Not changed |
| Output module | A1SY10 <br> A1SY10EU | LY10R2 | 1) External wiring: Changed <br> 2) Number of slots: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> 4) Specifications: <br> Rated output voltage: Not changed <br> Rated output current: Not changed <br> (If the A1SY10EU is replaced with the LY10R2, the contact life span will be reduced to half.) <br> Wiring method for common: Changed <br> (8 points/common $\rightarrow 16$ points/common) <br> 5) Functions: Not changed |
|  | A1SY14EU | LY10R2 | 1) External wiring: Changed <br> 2) Number of slots: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> 4) Specifications: <br> Rated output voltage: Not changed <br> Rated output current: Not changed <br> (The contact life span will be reduced to half.) <br> Wiring method for common: Changed <br> (4 points/common $\rightarrow 16$ points/common) <br> 5) Functions: Not changed |
|  | A1SY18A | (None) | (None) |
|  | A1SY18AEU |  |  |


| AnS/QnAS series model |  | L series alternative model |  |
| :---: | :---: | :---: | :---: |
| Product | Model | Model | Remarks (restrictions) |
| Output module | A1SY22 | LY20S6 | 1) External wiring: Changed <br> 2) Number of slots: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> 4) Specifications: <br> Rated output voltage: Not changed <br> Rated output current: Not changed Wiring method for common: Changed (8 points/common $\rightarrow 16$ points/common) <br> 5) Function: Changed (no fuse) |
|  | A1SY28A | (None) | (None) |
|  | A1SY28EU | (None) | (None) |
|  | A1SY40 | LY40NT5P | 1) External wiring: Changed <br> 2) Number of slots: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> 4) Specifications: <br> Rated output voltage: Not changed <br> Rated output current: Not changed <br> Wiring method for common: Changed <br> (8 points/common $\rightarrow 16$ points/common) <br> 5) Functions: Changed (fuse $\rightarrow$ overheat and overload protection) |
|  | A1SY40P | LY40NT5P | 1) External wiring: Changed <br> 2) Number of slots: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> 4) Specifications: <br> Rated output voltage: Not changed <br> Rated output current: Not changed <br> Wiring method for common: Changed <br> ( 8 points/common $\rightarrow 16$ points/common) <br> 5) Functions: Not changed |
|  | A1SY41 | LY41NT1P | 1) External wiring: Not changed <br> 2) Number of slots: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> 4) Specifications: <br> Rated output voltage: Not changed <br> Rated output current: Not changed <br> 5) Functions: Changed (fuse $\rightarrow$ overheat and overload protection) |
|  | A1SY41P | LY41NT1P | 1) External wiring: Not changed <br> 2) Number of slots: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> 4) Specifications: <br> Rated output voltage: Not changed <br> Rated output current: Not changed <br> 5) Functions: Not changed |


| AnS/QnAS series model |  | L series alternative model |  |
| :---: | :---: | :---: | :---: |
| Product | Model | Model | Remarks (restrictions) |
| Output module | A1SY42 | LY42NT1P | 1) External wiring: Not changed <br> 2) Number of slots: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> 4) Specifications: <br> Rated output voltage: Not changed <br> Rated output current: Not changed <br> 5) Functions: Changed (fuse $\rightarrow$ overheat and overload protection) |
|  | A1SY42P | LY42NT1P | 1) External wiring: Not changed <br> 2) Number of slots: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> 4) Specifications: <br> Rated output voltage: Not changed <br> Rated output current: Not changed <br> 5) Functions: Not changed |
|  | A1SY50 | LY40NT5P | 1) External wiring: Changed <br> 2) Number of slots: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> 4) Specifications: <br> Rated output voltage: Not changed <br> Rated output current: Not changed <br> Wiring method for common: Changed <br> ( 8 points/common $\rightarrow 16$ points /common) <br> 5) Functions: Changed (fuse $\rightarrow$ overheat and overload protection) |
|  | A1SY60 | (None) | (None) |
|  | A1SY60E |  |  |
|  | A1SY68A |  |  |
|  | A1SY71 | (None) | Consider reexamining the external device to be connected. |
|  | A1SY80 | LY40PT5P | 1) External wiring: Changed <br> 2) Number of slots: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> 4) Specifications: <br> Rated output voltage: Not changed <br> Rated input current: Changed ( $0.8 \mathrm{~A} \rightarrow 0.5 \mathrm{~A}$ ) <br> Wiring method for common: Changed <br> ( 8 points/common $\rightarrow 16$ points/common) <br> 5) Functions: Changed (fuse $\rightarrow$ overheat and overload protection) |


| AnS/QnAS series model |  | L series alternative model |  |
| :---: | :---: | :---: | :---: |
| Product | Model | Model | Remarks (restrictions) |
| Output module | A1SY81 | LY41PT1P | 1) External wiring: changed (37-pin D-sub connector $\rightarrow 40$-pin connector) <br> 2) Number of slots: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> 4) Specifications: <br> Rated output voltage: Not changed <br> Rated output current: Not changed <br> 5) Functions: Changed (fuse $\rightarrow$ overheat and overload protection) |
|  | A1SY81EP | LY41PT1P | 1) External wiring: changed (37-pin D-sub connector $\rightarrow 40$-pin connector) <br> 2) Number of slots: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> 4) Specifications: <br> Rated output voltage: Not changed <br> Rated output current: Not changed <br> 5) Functions: Not changed |
|  | A1SY82 | LY42PT1P | 1) External wiring: Not changed <br> 2) Number of slots: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> 4) Specifications: <br> Rated output voltage: Not changed <br> Rated output current: Not changed <br> 5) Functions: Changed (fuse $\rightarrow$ overheat and overload protection) |
| I/O module | A1SH42 | LX41C4 $+$ <br> LY41NT1P | 1) External wiring: Not changed <br> 2) Number of slots: Changed (Two modules are required.) <br> 3) Program: <br> Number of occupied I/O points: Changed ( $32 \rightarrow 64(32 \times 2)$ ) <br> 4) Specifications: <br> (Input part) <br> Rated input voltage: Changed (12VDC is not applicable.) <br> Rated input current: Changed (approx. $5 \mathrm{~mA} \rightarrow 4 \mathrm{~mA}$ ) <br> ON voltage/ON current: Changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Changed <br> (Output part) <br> Rated output voltage: Not changed <br> Rated output current: Not changed <br> 5) Functions: Changed (fuse $\rightarrow$ overheat and overload protection) |


| AnS/QnAS series model |  | L series alternative model |  |
| :---: | :---: | :---: | :---: |
| Product | Model | Model | Remarks (restrictions) |
| I/O module | A1SH42P | LX41C4 <br> $+$ <br> LY41NT1P | 1) External wiring: Not changed <br> 2) Number of slots: Changed (Two modules are required.) <br> 3) Program: <br> Number of occupied I/O points: Changed ( $32 \rightarrow 64$ ( $32 \times 2$ ) ) <br> 4) Specifications: <br> (Input part) <br> Rated input voltage: Changed (12VDC is not applicable.) <br> Rated input current: Changed (approx. $5 \mathrm{~mA} \rightarrow 4 \mathrm{~mA}$ ) <br> ON voltage/ON current: Changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Changed <br> (Output part) <br> Rated output voltage: Not changed <br> Rated output current: Not changed <br> 5) Functions: Not changed |
|  | A1SH42-S1 | LX41C4 <br> LY41NT1P | 1) External wiring: Not changed <br> 2) Number of slots: Changed (Two modules are required.) <br> 3) Program: <br> Number of occupied I/O points: Changed ( $32 \rightarrow 64(32 \times 2)$ ) <br> 4) Specifications: <br> (Input part) <br> Rated input voltage: Not changed <br> Rated input current: Changed (approx. $5 \mathrm{~mA} \rightarrow 4 \mathrm{~mA}$ ) <br> ON voltage/ON current: Changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Changed <br> Response time: Changed <br> (Output part) <br> Rated output voltage: Not changed <br> Rated output current: Not changed <br> 5) Functions: Changed (fuse $\rightarrow$ overheat and overload protection) |
|  | A1SH42P-S1 | LX41C4 <br> LY41NT1P | 1) External wiring: Not changed <br> 2) Number of slots: Changed (Two modules are required.) <br> 3) Program: <br> Number of occupied I/O points: Changed ( $32 \rightarrow 64$ ( $32 \times 2$ )) <br> 4) Specifications: <br> (Input part) <br> Rated input voltage: Not changed <br> Rated input current: Changed (approx. $5 \mathrm{~mA} \rightarrow 4 \mathrm{~mA}$ ) <br> ON voltage/ON current: Changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Changed <br> Response time: Changed <br> (Output part) <br> Rated output voltage: Not changed <br> Rated output current: Not changed <br> 5) Functions: Not changed |



## [Issue No.] FA-A-0142-C

| AnS/QnAS series model |  | L series alternative model |  |
| :--- | :--- | :--- | :--- |
| Product | Model | Model | Remarks (restrictions) |
| Interrupt <br> module | A1SI61 | (None) | Consider using the interrupt function which is a built-in I/O function. |
| Dummy <br> module | A1SG62 | (None) | For the L series, a dummy unit is not required because of baseless configuration. <br> If the I/O assignment is the same as the one before the replacement, consider I/O assignment <br> through parameter settings. |
| Blank cover | A1SG60 | (None) | For the L series, a blank cover is not required because of baseless configuration. <br> If the I/O assignment is the same as the one before the replacement, consider I/O assignment <br> through parameter settings. |

### 12.2.2 Replacement with $Q$ series

| AnS/QnAS series model |  | Q series alternative model |  |
| :---: | :---: | :---: | :---: |
| Product | Model | Model | Remarks (restrictions) |
| Input <br> module | A1SX10 | QX10 | 1) External wiring: Changed <br> 2) Number of slots: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> 4) Specifications: <br> Rated input voltage: Not changed <br> Rated input current: Changed <br> ON voltage/ON current: Not changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Changed <br> 5) Functions: Not changed |
|  | A1SX10EU | QX10 | 1) External wiring: Changed <br> 2) Number of slots: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> 4) Specifications: <br> Rated input voltage: Not changed <br> Rated input current: Changed <br> ON voltage/ON current: Not changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Changed <br> 5) Functions: Not changed |
|  | A1SX20 | QX28 | 1) External wiring: Changed <br> 2) Number of slots: Changed (Two modules are required.) <br> 3) Program: <br> Number of occupied I/O points: Changed (16 points $\rightarrow 32$ points (16 points $\times 2$ modules)) <br> 4) Specifications: <br> Rated input voltage: Not changed <br> Rated input current: Changed <br> ON voltage/ON current: Changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Changed <br> 5) Functions: Not changed |
|  | A1SX20EU | QX28 | 1) External wiring: Changed <br> 2) Number of slots: Changed (Two modules are required.) <br> 3) Program: <br> Number of occupied I/O points: Changed (16 points $\rightarrow 32$ points (16 points $\times 2$ modules)) <br> 4) Specifications: <br> Rated input voltage: Not changed <br> Rated input current: Changed <br> ON voltage/ON current: Changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Changed <br> 5) Functions: Not changed |


| AnS/QnAS series model |  | Q series alternative model |  |
| :---: | :---: | :---: | :---: |
| Product | Model | Model | Remarks (restrictions) |
| Input module | A1SX30 | QX40 | Consider substituting the QX40 for it. [When applying DC input] <br> 1) External wiring: Changed <br> 2) Number of slots: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> 4) Specifications: <br> Rated input voltage: Changed (12VDC and AC input are not applicable.)* <br> Rated input current: Changed <br> ON voltage/ON current: Changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Changed <br> 5) Functions: Not changed <br> * When 12VDC is required, use the QX70. <br> [When applying AC input] <br> Convert 24VAC to DC externally before input to the QX40. |
|  | A1SX40 | $\begin{aligned} & \text { QX40 } \\ & (24 \mathrm{VDC}) \end{aligned}$ | 1) External wiring: Changed <br> 2) Number of slots: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> 4) Specifications: <br> Rated input voltage: Changed (12VDC is not applicable.) <br> Rated input current: Changed <br> ON voltage/ON current: Changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Changed <br> 5) Functions: Not changed |
|  |  | $\begin{aligned} & \text { QX70 } \\ & \text { (12VDC) } \end{aligned}$ | 1) External wiring: Changed <br> 2) Number of slots: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> 4) Specifications: <br> Rated input voltage: Changed (24VDC is not applicable.) <br> Rated input current: Changed <br> ON voltage/ON current: Changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Not changed <br> 5) Functions: Not changed |
|  | A1SX40-S1 | QX40-S1 | 1) External wiring: Changed <br> 2) Number of slots: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> 4) Specifications: <br> Rated input voltage: Not changed <br> Rated input current: Changed <br> ON voltage/ON current: Changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Changed <br> 5) Functions: Not changed |
|  | A1SX40-S2 | QX40 | 1) External wiring: Changed <br> 2) Number of slots: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> 4) Specifications: <br> Rated input voltage: Not changed <br> Rated input current: Changed <br> ON voltage/ON current: Changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Changed <br> 5) Functions: Not changed |


| AnS/QnAS series model |  | Q series alternative model |  |
| :---: | :---: | :---: | :---: |
| Product | Model | Model | Remarks (restrictions) |
| Input module | A1SX41 | QX41 <br> (24VDC) | 1) External wiring: Not changed <br> 2) Number of slots: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> 4) Specifications: <br> Rated input voltage: Changed (12VDC is not applicable.) <br> Rated input current: Changed (approx. $7 \mathrm{~mA} \rightarrow$ approx. 4 mA ) <br> ON voltage/ON current: Changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Changed <br> 5) Functions: Not changed |
|  |  | $\begin{aligned} & \text { QX41-S2 } \\ & (24 \mathrm{VDC}) \end{aligned}$ | 1) External wiring: Not changed <br> 2) Number of slots: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> 4) Specifications: <br> Rated input voltage: Changed (12VDC is not applicable.) <br> Rated input current: Changed (approx. $7 \mathrm{~mA} \rightarrow$ approx. 6 mA ) <br> ON voltage/ON current: Changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Changed <br> 5) Functions: Not changed |
|  |  | QX71 <br> (12VDC) | 1) External wiring: Not changed <br> 2) Number of slots: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> 4) Specifications: <br> Rated input voltage: Changed (24VDC is not applicable.) <br> Rated input current: Changed <br> ON voltage/ON current: Changed <br> OFF voltage/OFFcurrent: Changed <br> Input resistance: Not changed <br> 5) Functions: Not changed |


| AnS/QnAS series model |  | Q series alternative model |  |
| :---: | :---: | :---: | :---: |
| Product | Model | Model | Remarks (restrictions) |
| Input module | A1SX41-S1 | QX41-S1 | 1) External wiring: Not changed <br> 2) Number of slots: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> 4) Specifications: <br> Rated input voltage: Not changed <br> Rated input current: Changed <br> ON voltage/ON current: Changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Changed <br> 5) Functions: Not changed |
|  | A1SX41-S2 | QX41 | 1) External wiring: Not changed <br> 2) Number of slots: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> 4) Specifications: <br> Rated input voltage: Not changed <br> Rated input current: Changed (approx. $7 \mathrm{~mA} \rightarrow$ approx. 4 mA ) <br> ON voltage/ON current: Changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Changed <br> 5) Functions: Not changed |
|  |  | QX41-S2 | 1) External wiring: Not changed <br> 2) Number of slots: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> 4) Specifications: <br> Rated input voltage: Not changed <br> Rated input current: Changed (approx. $7 \mathrm{~mA} \rightarrow$ approx. 6 mA ) <br> ON voltage/ON current: Changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Changed <br> 5) Functions: Not changed |


| AnS/QnAS series model |  | Q series alternative model |  |
| :---: | :---: | :---: | :---: |
| Product | Model | Model | Remarks (restrictions) |
| Input module | A1SX42 | $\begin{aligned} & \text { QX42 } \\ & (24 \mathrm{VDC}) \end{aligned}$ | 1) External wiring: Not changed <br> 2) Number of slots: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> 4) Specifications: <br> Rated input voltage: Changed (12VDC is not applicable.) <br> Rated input current: Changed (approx. $5 \mathrm{~mA} \rightarrow$ approx. 4 mA ) <br> ON voltage/ON current: Changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Changed <br> 5) Functions: Not changed |
|  |  | $\begin{aligned} & \text { QX41-S2 } \\ & (24 \mathrm{VDC}) \end{aligned}$ | 1) External wiring: Not changed <br> 2) Number of slots: Changed (Two modules are required.) <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> (64 points $=32$ points $\times 2$ modules) <br> 4) Specifications: <br> Rated input voltage: Changed (12VDC is not applicable.) <br> Rated input current: Changed (approx. 5mA $\rightarrow$ approx. 6mA) <br> ON voltage/ON current: Changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Changed <br> 5) Functions: Not changed |
|  |  | $\begin{aligned} & \text { QX72 } \\ & (12 \mathrm{VDC}) \end{aligned}$ | 1) External wiring: Not changed <br> 2) Number of slots: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> 4) Specifications: <br> Rated input voltage: Changed (24VDC is not applicable.) <br> Rated input current: Changed <br> ON voltage/ON current: Changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Changed <br> 5) Functions: Not changed |


| AnS/QnAS series model |  | Q series alternative model |  |
| :---: | :---: | :---: | :---: |
| Product | Model | Model | Remarks (restrictions) |
| Input module | A1SX42-S1 | QX42-S1 | 1) External wiring: Not changed <br> 2) Number of slots: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> 4) Specifications: <br> Rated input voltage: Not changed <br> Rated input current: Changed <br> ON voltage/ON current: Changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Changed <br> 5) Functions: Not changed |
|  | A1SX42-S2 | QX42 | 1) External wiring: Not changed <br> 2) Number of slots: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> 4) Specifications: <br> Rated input voltage: Not changed <br> Rated input current: Changed (approx. $5 \mathrm{~mA} \rightarrow$ approx. 4 mA ) <br> ON voltage/ON current: Changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Changed <br> 5) Functions: Not changed |
|  |  | QX41-S2 | 1) External wiring: Not changed <br> 2) Number of slots: Changed (Two modules are required.) <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> ( 64 points $=32$ points $\times 2$ modules) <br> 4) Specifications: <br> Rated input voltage: Not changed <br> Rated input current: Changed (approx. $5 \mathrm{~mA} \rightarrow$ approx. 6 mA ) <br> ON voltage/ON current: Changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Changed <br> 5) Functions: Not changed |


| AnS/QnAS series model |  | Q series alternative model |  |
| :---: | :---: | :---: | :---: |
| Product | Model | Model | Remarks (restrictions) |
| Input module | A1SX71 | $\begin{aligned} & \text { QX41-S1 } \\ & (24 \mathrm{VDC}) \end{aligned}$ | 1) External wiring: Not changed <br> 2) Number of slots: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> 4) Specifications: <br> Rated input voltage: Changed (5VDC and 12VDC are not applicable.) <br> Rated input current: Changed <br> ON voltage/ON current: Changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Changed <br> 5) Functions: Not changed |
|  |  | $\begin{aligned} & \text { QX71 } \\ & \text { (5VDC, 12VDC) } \end{aligned}$ | 1) External wiring: Not changed <br> 2) Number of slots: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> 4) Specifications: <br> Rated input voltage: Changed (24VDC is not applicable.) <br> Rated input current: Not changed <br> ON voltage/ON current: Not changed <br> OFF voltage/OFF current: Not changed <br> Input resistance: Changed <br> 5) Functions: Not changed |
|  | A1SX80 | $\begin{aligned} & \text { QX80 } \\ & \text { (24VDC) } \end{aligned}$ | 1) External wiring: Changed <br> 2) Number of slots: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> 4) Specifications: <br> Rated input voltage: Changed (12VDC is not applicable.) <br> Rated input current: Changed <br> ON voltage/ON current: Changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Changed <br> 5) Functions: Not changed |
|  |  | $\begin{aligned} & \text { QX70 } \\ & \text { (12VDC) } \end{aligned}$ | 1) External wiring: Changed <br> 2) Number of slots: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> 4) Specifications: <br> Rated input voltage: Changed (24VDC is not applicable.) <br> Rated input current: Changed <br> ON voltage/ON current: Changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Not changed <br> 5) Functions: Not changed |


| AnS/QnAS series model |  | Q series alternative model |  |
| :---: | :---: | :---: | :---: |
| Product | Model | Model | Remarks (restrictions) |
| Input module | A1SX80-S1 | QX80 | 1) External wiring: Changed <br> 2) Number of slots: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> 4) Specifications: <br> Rated input voltage: Not changed <br> Rated input current: Changed <br> ON voltage/ON current: Changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Changed <br> 5) Functions: Not changed |
|  | A1SX80-S2 | QX80 | 1) External wiring: Changed <br> 2) Number of slots: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> 4) Specifications: <br> Rated input voltage: Not changed <br> Rated input current: Changed <br> ON voltage/ON current: Changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Changed <br> 5) Functions: Not changed |
|  | A1SX81 | QX81 <br> (24VDC) | 1) External wiring: Not changed <br> 2) Number of slots: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> 4) Specifications: <br> Rated input voltage: Changed (12VDC is not applicable.) <br> Rated input current: Changed (approx. $7 \mathrm{~mA} \rightarrow$ approx. 4 mA ) <br> ON voltage/ON current: Changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Changed <br> 5) Functions: Not changed |
|  |  | $\begin{aligned} & \text { QX81-S2 } \\ & (24 \mathrm{VDC}) \end{aligned}$ | 1) External wiring: Not changed <br> 2) Number of slots: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> 4) Specifications: <br> Rated input voltage: Changed (12VDC is not applicable.) <br> Rated input current: Changed (approx. $7 \mathrm{~mA} \rightarrow$ approx. 6 mA ) <br> ON voltage/ON current: Changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Changed <br> 5) Functions: Not changed |
|  |  | QX71 <br> (12VDC) | 1) External wiring: Not changed <br> 2) Number of slots: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> 4) Specifications: <br> Rated input voltage: Changed (24VDC is not applicable.) <br> Rated input current: Changed <br> ON voltage/ON current: Changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Not changed <br> 5) Functions: Not changed |


| AnS/QnAS series model |  | Q series alternative model |  |
| :---: | :---: | :---: | :---: |
| Product | Model | Model | Remarks (restrictions) |
| Input module | A1SX81-S2 | QX81 | 1) External wiring: Not changed <br> 2) Number of slots: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> 4) Specifications: <br> Rated input voltage: Not changed <br> Rated input current: Changed (approx. $7 \mathrm{~mA} \rightarrow$ approx. 4 mA ) <br> ON voltage/ON current: Changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Changed <br> 5) Functions: Not changed |
|  |  | QX81-S2 | 1) External wiring: Not changed <br> 2) Number of slots: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> 4) Specifications: <br> Rated input voltage: Not changed <br> Rated input current: Changed (approx. $7 \mathrm{~mA} \rightarrow$ approx. 6 mA ) <br> ON voltage/ON current: Changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Changed <br> 5) Functions: Not changed |
|  | A1SX82-S1 | QX82-S1 | 1) External wiring: Not changed <br> 2) Number of slots: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> 4) Specifications: <br> Rated input voltage: Not changed <br> Rated input current: Changed <br> ON voltage/ON current: Changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Changed <br> 5) Functions: Not changed |
| Output module | $\begin{aligned} & \text { A1SY10 } \\ & \text { A1SY10EU } \end{aligned}$ | QY10 | 1) External wiring: Changed <br> 2) Number of slots: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> 4) Specifications: <br> Rated output voltage: Not changed <br> Rated output current: Not changed <br> (However, the contact life span of the A1SY10EU is reduced to half.) <br> 5) Functions: Not changed |


| AnS/QnAS series model |  | Q series alternative model |  |
| :---: | :---: | :---: | :---: |
| Product | Model | Model | Remarks (restrictions) |
| Output module | A1SY14EU | QY10 | 1) External wiring: Changed <br> 2) Number of slots: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> 4) Specifications: <br> Rated output voltage: Not changed <br> Rated output current: Not changed <br> (However, contact life span is reduced to half.) <br> 5) Functions: Not changed |
|  | A1SY18A | QY18A | 1) External wiring: Changed <br> 2) Number of slots: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> 4) Specifications: <br> Rated output voltage: Not changed <br> Rated output current: Not changed <br> (However, contact life span is reduced to half.) <br> 5) Functions: Not changed |
|  | A1SY18AEU | QY18A | 1) External wiring: Changed <br> 2) Number of slots: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> 4) Specifications: <br> Rated output voltage: Not changed <br> Rated output current: Not changed <br> (However, contact life span is reduced to half.) <br> 5) Functions: Not changed |
|  | A1SY22 | QY22 | 1) External wiring: Changed <br> 2) Number of slots: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> 4) Specifications: <br> Rated output voltage: Not changed <br> Rated output current: Not changed <br> 5) Functions: Changed (no fuse) |
|  | A1SY28A | (None) | (None) |
|  | A1SY28EU | (None) |  |
|  | A1SY40 | QY40P | 1) External wiring: Changed <br> 2) Number of slots: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> 4) Specifications: <br> Rated output voltage: Not changed <br> Rated output current: Not changed <br> 5) Functions: Changed (fuse $\rightarrow$ overheat and overload protection) |


| AnS/QnAS series model |  | Q series alternative model |  |
| :---: | :---: | :---: | :---: |
| Product | Model | Model | Remarks (restrictions) |
| Output module | A1SY40P | QY40P | 1) External wiring: Changed <br> 2) Number of slots: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> 4) Specifications: <br> Rated output voltage: Not changed <br> Rated output current: Not changed <br> 5) Functions: Not changed |
|  | A1SY41 | QY41P | 1) External wiring: Not changed <br> 2) Number of slots: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> 4) Specifications: <br> Rated output voltage: Not changed <br> Rated output current: Not changed <br> 5) Functions: Changed (fuse $\rightarrow$ overheat and overload protection) |
|  | A1SY41P | QY41P | 1) External wiring: Not changed <br> 2) Number of slots: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> 4) Specifications: <br> Rated output voltage: Not changed <br> Rated output current: Not changed <br> 5) Functions: Not changed |
|  | A1SY42 | QY42P | 1) External wiring: Not changed <br> 2) Number of slots: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> 4) Specifications: <br> Rated output voltage: Not changed <br> Rated output current: Not changed <br> 5) Functions: Changed (fuse $\rightarrow$ overheat and overload protection) |
|  | A1SY42P | QY42P | 1) External wiring: Not changed <br> 2) Number of slots: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> 4) Specifications: <br> Rated output voltage: Not changed <br> Rated output current: Not changed <br> 5) Functions: Not changed |
|  | A1SY50 | QY50 | 1) External wiring: Changed <br> 2) Number of slots: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> 4) Specifications: <br> Rated output voltage: Not changed <br> Rated output current: Not changed <br> 5) Functions: Not changed |


| AnS/QnAS series model |  | Q series alternative model |  |
| :---: | :---: | :---: | :---: |
| Product | Model | Model | Remarks (restrictions) |
| Output module | A1SY60 | QY68A | 1) External wiring: Changed <br> 2) Number of slots: Changed (Two modules are required.) <br> 3) Program: <br> Number of occupied I/O points: Changed (16 points $\rightarrow 32$ points (16 points $\times 2$ modules)) <br> 4) Specifications: <br> Rated output voltage: Not changed <br> Rated output current: Not changed <br> 5) Functions: Changed (no fuse, independent common) |
|  | A1SY60E | QY68A | 1) External wiring: Changed <br> 2) Number of slots: Changed (Two modules are required.) <br> 3) Program: <br> Number of occupied I/O points: Changed (16 points $\rightarrow 32$ points (16 points $\times 2$ modules)) <br> 4) Specifications: <br> Rated output voltage: Not changed <br> Rated output current: Not changed <br> 5) Functions: Changed (no fuse, independent common) |
|  | A1SY68A | QY68A | 1) External wiring: Changed <br> 2) Number of slots: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> 4) Specifications: <br> Rated output voltage: Changed (48VDC is not applicable.) <br> Rate output current: Not changed <br> 5) Functions: Not changed |
|  | A1SY71 | QY71 | 1) External wiring: Not changed <br> 2) Number of slots: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> 4) Specifications: <br> Rated output voltage: Not changed <br> Rated output current: Not changed <br> 5) Functions: Not changed |
|  | A1SY80 | QY80 | 1) External wiring: Changed <br> 2) Number of slots: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> 4) Specifications: <br> Rated output voltage: Not changed <br> Rated output current: Changed <br> 5) Functions: Not changed |
|  | A1SY81 | QY81P | 1) External wiring: Not changed <br> 2) Number of slots: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> 4) Specifications: <br> Rated output voltage: Not changed <br> Rated output current: Not changed <br> 5) Functions: Changed (fuse $\rightarrow$ overheat and overload protection) |


| AnS/QnAS series model |  | Q series alternative model |  |
| :---: | :---: | :---: | :---: |
| Product | Model | Model | Remarks (restrictions) |
| Output module | A1SY81EP | QY81P | 1) External wiring: Not changed <br> 2) Number of slots: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> 4) Specifications: <br> Rated output voltage: Not changed <br> Rated output current: Not changed <br> 5) Functions: Not changed |
|  | A1SY82 | QY82P | 1) External wiring: Not changed <br> 2) Number of slots: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> 4) Specifications: <br> Rated output voltage: Not changed <br> Rated output current: Not changed <br> 5) Functions: Changed (fuse $\rightarrow$ overheat and overload protection) |
| 1/O module | A1SH42 | QH42P | 1) External wiring: Not changed <br> 2) Number of slots: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> 4) Specifications: <br> (Input part) <br> Rated input voltage: Changed (12VDC is not applicable.) <br> Rated input current: Changed <br> ON voltage/ON current: Changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Changed <br> (Output part) <br> Rated output voltage: Not changed <br> Rated output current: Not changed <br> 5) Functions: Changed (fuse $\rightarrow$ overheat and overload protection) |
|  | A1SH42P | QH42P | 1) External wiring: Not changed <br> 2) Number of slots: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> 4) Specifications: <br> (Input part) <br> Rated input voltage: Changed (12VDC is not applicable.) <br> Rated input current: Changed <br> ON voltage/ON current: Changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Changed <br> (Output part) <br> Rated output voltage: Not changed <br> Rated output current: Not changed <br> 5) Functions: Not changed |


| AnS/QnAS series model |  | Q series alternative model |  |
| :---: | :---: | :---: | :---: |
| Product | Model | Model | Remarks (restrictions) |
| I/O module | A1SH42-S1 | QH42P | 1) External wiring: Not changed <br> 2) Number of slots: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> 4) Specifications: <br> (Input part) <br> Rated input voltage: Not changed <br> Rated input current: Changed <br> ON voltage/ON current: Changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Changed <br> (Output part) <br> Rated output voltage: Not changed <br> Rated output current: Not changed <br> 5) Functions: Changed (fuse $\rightarrow$ overheat and overload protection) |
|  | A1SH42P-S1 | QH42P | 1) External wiring: Not changed <br> 2) Number of slots: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> 4) Specifications: <br> (Input part) <br> Rated input voltage: Not changed <br> Rated input current: Changed <br> ON voltage/ON current: Changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Changed <br> (Output part) <br> Rated output voltage: Not changed <br> Rated output current: Not changed <br> 5) Functions: Not changed |
|  | A1SX48Y18 | $\begin{aligned} & \text { QX40 } \\ & + \\ & \text { QY10 } \end{aligned}$ | 1) External wiring: Changed <br> 2) Number of slots: Changed (Two modules are required.) <br> 3) Program: <br> Number of occupied I/O points: Changed (16 points $\rightarrow 32$ points (16 points $\times 2$ modules)) <br> 4) Specifications: <br> (Input part) <br> Rated input voltage: Not changed <br> Rated input current: Changed <br> ON voltage/ON current: Changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Changed <br> (Output part) <br> Rated output voltage: Not changed <br> Rated output current: Not changed <br> 5) Functions: Not changed |


| AnS/QnAS series model |  | Q series alternative model |  |
| :---: | :---: | :---: | :---: |
| Product | Model | Model | Remarks (restrictions) |
| I/O module | A1SX48Y58 | QX48Y57 | 1) External wiring: Changed <br> 2) Number of slots: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed <br> 4) Specifications: <br> (Input part) <br> Rated input voltage: Not changed <br> Rated input current: Changed <br> ON voltage/ON current: Changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Changed <br> (Output part) <br> Rated output voltage: Not changed <br> Rated output current: Not changed <br> 5) Functions: Changed (number of output points: 8 points $\rightarrow 7$ points) |
|  | A1SJ-56DT | $\begin{aligned} & \text { QX40 } \\ & + \\ & \text { QY50 } \end{aligned}$ | 1) External wiring: Changed <br> 2) Number of slots: Changed ( 5 slots $\rightarrow 4$ slots) <br> 3) Program: <br> Number of occupied I/O points: Changed <br> (128 points (including 4 empty slots) $\rightarrow 64$ points ( 4 slots)) <br> 4) Specifications: <br> (Input part) <br> Rated input voltage: Not changed <br> Rated input current: Changed <br> ON voltage/ON current: Changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Changed <br> (Output part) <br> Rated output voltage: Not changed <br> Rated output current: Not changed <br> 5) Functions: Changed (no fuse $\rightarrow$ built-in fuse) |
|  | A1SJ-56DR | $\begin{aligned} & \text { QX40 } \\ & + \\ & \text { QY10 } \end{aligned}$ | 1) External wiring: Changed <br> 2) Number of slots: Changed ( 5 slots $\rightarrow 4$ slots) <br> 3) Program: <br> Number of occupied I/O points: Changed <br> (128 points (including 4 empty slots) $\rightarrow 64$ points ( 4 slots)) <br> 4) Specifications: <br> (Input part) <br> Rated input voltage: Not changed <br> Rated input current: Changed <br> ON voltage/ON current: Changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Changed <br> (Output part) <br> Rated output voltage: Not changed <br> Rated output current: Not changed <br> 5) Functions: Not changed |
| Dynamic scan I/O module | A1S42X | None | Consider converting input signals from dynamic to static and using the QX42. |
|  | A1S42Y | None | Consider converting input signals from dynamic to static and using the QY42P. |
| Interrupt module | A1SI61 | Q160 | 1) External wiring: Changed <br> 2) Number of slots: Not changed <br> 3) Program: <br> Number of occupied I/O points: Changed (32 points $\rightarrow 16$ points) <br> 4) Specifications: <br> Rated input voltage: Changed (12VDC is not applicable.) <br> Rated input current: Changed <br> ON voltage/ON current: Changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Changed <br> 5) Functions: Changed (condition setting: hardware switch $\rightarrow$ parameter) |

## [Issue No.] FA-A-0142-C

| AnS/QnAS series model |  | Q series alternative model |  |
| :--- | :--- | :--- | :--- |
| Product | Model | Model | Remarks (restrictions) |
| Dummy <br> module | A1SG62 | None | [Dummy module function] <br> Consider using the QG60 and I/O assignment setting. |
| Blank cover | A1SG60 | QG60 | No restrictions |

## Point/

When using the extension base unit of the A/QnA series, please refer to the following for details
[ $]$ Transition from MELSEC-A/QnA (Large Type) Series to Q Series Handbook (Fundamentals) L(NA)08043ENG

### 12.3 Intelligent function module

### 12.3.1 Replacement with $L$ series

| AnS/QnAS series |  | L series alternative model |  |
| :---: | :---: | :---: | :---: |
| Product | Model | Model | Remarks (restrictions) |
| Analog input module | A1S64AD | L60AD4 | 1) External wiring: Cable size is changed. <br> 2) Number of slots: Not changed <br> 3) Program: The number of occupied I/O points, I/O signals, and buffer memory addresses are changed. <br> 4) Performance specifications: Not changed <br> 5) Functional specifications: Not changed |
|  | A1S68AD | L60AD4 | 1) External wiring: Cable size is changed. <br> 2) Number of slots: Changed (Two modules are required.) <br> 3) Program: The number of occupied I/O points, I/O signals, and buffer memory addresses are changed. <br> 4) Performance specifications: Not changed <br> 5) Functional specifications: Not changed |
| Analog output module | A1S62DA | L60DA4 | 1) External wiring: Cable size is changed. <br> 2) Number of slots: Not changed <br> 3) Program: The number of occupied I/O points, I/O signals, and buffer memory addresses are changed. <br> 4) Performance specifications: Changed. Four channels per module and an external power supply (24VDC) are required. <br> 5) Functional specifications: Not changed |
|  | A1S68DAI | L60DA4 | 1) External wiring: Cable size is changed. <br> 2) Number of slots: Changed (Two modules are required.) <br> 3) Program: The number of occupied I/O points, I/O signals, and buffer memory addresses are changed. <br> 4) Performance specifications: Changed. An external power supply (24VDC) is required. <br> 5) Functional specifications: Not changed |
|  | A1S68DAV | L60DA4 | 1) External wiring: Cable size is changed. <br> 2) Number of slots: Changed (Two modules are required.) <br> 3) Program: The number of occupied I/O points, I/O signals, and buffer memory addresses are changed. <br> 4) Performance specifications: Changed. An external power supply (24VDC) is required. <br> 5) Functional specifications: Not changed |


| AnS/QnAS series |  | L series alternative model |  |
| :---: | :---: | :---: | :---: |
| Product | Model | Model | Remarks (restrictions) |
| High-speed counter module | A1SD62E | LD62 | 1) External wiring: Terminal block wiring $\rightarrow$ Connector wiring Cable size is changed. <br> 2) Number of slots: Not changed <br> 3) Counting speed: $200 \mathrm{~K}, 100 \mathrm{~K}$, or 10 KPPS <br> 4) Counting range: 32-bit signed binary (-2147483648 to 2147483647) <br> Program does not need to be reviewed. <br> 5) Program: The number of occupied I/O points, I/O signals, and buffer memory addresses are changed. <br> 6) Performance specifications: Output terminal type: sink output The input terminal filter characteristics are different. <br> 7) Functional specifications: Not changed |
|  |  | L02CPU-P L26CPU-PBT | 1) External wiring: Terminal block wiring $\rightarrow$ Connector wiring <br> Cable size is changed. <br> 2) Number of slots: Changed. 0 module (I/O function built in CPU) <br> 3) Counting speed: $200 \mathrm{~K}, 100 \mathrm{~K}, 50 \mathrm{~K}$, or 10 KPPS <br> 4) Counting range: 32-bit signed binary (-2147483648 to 2147483647 ) <br> Program does not need to be reviewed. <br> 5) Program: Incompatible (Need to be created) <br> 6) Performance specifications: The input terminal filter characteristics are different. <br> 7) Function specifications: Limit switch output function $\rightarrow$ Coincidence output function (Two coincidence detection output points can be set.) <br> No periodic pulse counter function |
|  | $\begin{aligned} & \text { A1SD62D } \\ & \text { A1SD62D-S1 } \end{aligned}$ | LD62D | 1) External wiring: Terminal block wiring $\rightarrow$ Connector wiring <br> Cable size is changed. <br> 2) Number of slots: Not changed <br> 3) Counting speed: $500 \mathrm{~K}, 200 \mathrm{~K}$, or 100 KPPS <br> 4) Counting range: 32-bit signed binary $\text { (-2147483648 to } 2147483647)$ <br> Program does not need to be reviewed. <br> 5) Program: The number of occupied I/O points, I/O signals, and buffer memory addresses are changed. <br> 6) Performance specifications: Number of channels is 2 . <br> 7) Functional specifications: Not changed |
|  |  | L02CPU <br> L26CPU-BT | 1) External wiring: Terminal block wiring $\rightarrow$ Connector wiring Cable size is changed. <br> 2) Number of slots: Changed. 0 module (I/O function built in CPU) <br> 3) Counting speed: $200 \mathrm{~K}, 100 \mathrm{~K}, 50 \mathrm{~K}$, or 10 KPPS <br> 4) Counting range: 32-bit signed binary (-2147483648 to 2147483647) <br> Program does not need to be reviewed. <br> 5) Program: Incompatible (Need to be created) <br> 6) Performance specifications: External input voltage 24 V only The input terminal filter characteristics are different. <br> 7) Function specifications: Limit switch output function $\rightarrow$ Coincidence output function (Two coincidence detection output points can be set.) <br> No periodic pulse counter function |


| AnS/QnAS series |  | L series alternative model |  |
| :---: | :---: | :---: | :---: |
| Product | Model | Model | Remarks (restrictions) |
| Positioning module | A1SD75M1 | LD77MH4 | 1) External wiring: Connector and wiring are changed. <br> 2) Number of slots: 2 (modules) <br> 3) Program: I/O signals and buffer memory assignment are changed. <br> The entire program needs to be reviewed according to the specifications change. <br> 4) Performance specifications: Backward compatible (4 axes) <br> 5) Function specifications: Partially changed <br> (Example: Manual pulse generator 1 /axis $\rightarrow 1$ /module) |
|  | A1SD75M2 | LD77MH4 | 1) External wiring: Connector and wiring are changed. <br> 2) Number of slots: 2 (modules) <br> 3) Program: I/O signals and buffer memory assignment are changed. <br> The entire program needs to be reviewed according to the specifications change. <br> 4) Performance specifications: Backward compatible (4 axes) <br> 5) Function specifications: Partially changed <br> (Example: Manual pulse generator 1 /axis $\rightarrow 1$ /module) |
|  | A1SD75M3 | LD77MH4 | 1) External wiring: Connector and wiring are changed. <br> 2) Number of slots: 2 (modules) <br> 3) Program: I/O signals and buffer memory assignment are changed. <br> The entire program needs to be reviewed according to the specifications change. <br> 4) Performance specifications: Backward compatible (4 axes) <br> 5) Function specifications: Partially changed <br> (Example: Manual pulse generator 1 /axis $\rightarrow 1$ /module) |

### 12.3.2 Replacement with $Q$ series

| AnS/QnAS series |  | Q series alternative model |  |
| :---: | :---: | :---: | :---: |
| Product | Model | Model | Remark (restrictions) |
| Analog input module | A1S64AD | Q64AD | 1) External wiring: Cable size is changed. <br> 2) Number of slots: Not changed <br> 3) Program: The number of occupied I/O points, I/O signals, and buffer memory addresses are changed. <br> 4) Performance specifications: Not changed <br> 5) Functional specifications: Not changed |
|  | A1S68AD | Q68ADV Q68ADI | 1) External wiring: Cable size is changed. <br> 2) Number of slots: Not changed <br> 3) Program: The number of occupied I/O points, I/O signals, and buffer memory addresses are changed. <br> 4) Performance specifications: Input signals (either V or I input) and $\mathrm{I} / \mathrm{O}$ characteristics are changed. <br> 5) Functional specifications: Not changed |
|  |  | Q68AD-G* ${ }^{*}$ | 1) External wiring: Cable size is changed. <br> 2) Number of slots: Not changed <br> 3) Program: The number of occupied I/O points, I/O signals, and buffer memory addresses are changed. <br> 4) Performance specifications: Changed Conversion speed ( $0.5 \mathrm{~ms} /$ channel $) \rightarrow$ sampling cycle (10ms/channel) + response speed (20ms) <br> 5) Functional specifications: Changed (Non-insulation $\rightarrow$ Insulation between channels) |
|  | A1S62DA | Q62DAN | 1) External wiring: Cable size is changed. <br> 2) Number of slots: Not changed <br> 3) Program: The number of occupied I/O points, I/O signals, and buffer memory addresses are changed. <br> 4) Performance specifications: Changed External power supply (24VDC) is required. <br> 5) Functional specifications: Not changed |
|  |  | Q64DAN | 1) External wiring: Cable size is changed. <br> 2) Number of slots: Not changed <br> 3) Program: The number of occupied I/O points, I/O signals, and buffer memory addresses are changed. <br> 4) Performance specifications: Changed ( $4 \mathrm{CH} /$ module) <br> External power supply (24VDC) is required. <br> 5) Functional specifications: Not changed |
|  | A1S68DAI | Q68DAIN | 1) External wiring: Cable size is changed. <br> 2) Number of slots: Not changed <br> 3) Program: The number of occupied I/O points, I/O signals, and buffer memory addresses are changed. <br> 4) Performance specifications: Changed External power supply (24VDC) is required. <br> 5) Functional specifications: Not changed |
|  | A1S68DAV | Q68DAVN | 1) External wiring: Cable size is changed. <br> 2) Number of slots: Not changed <br> 3) Program: The number of occupied I/O points, I/O signals, and buffer memory addresses are changed. <br> 4) Performance specifications: Changed External power supply (24VDC) is required. <br> 5) Functional specifications: Not changed |

[^4]| AnS/QnAS series |  | Q series alternative model |  |
| :---: | :---: | :---: | :---: |
| Product | Model | Model | Remark (restrictions) |
| Temperature input module | A1S68TD | Q64TD | 1) External wiring: Cable size is changed. <br> 2) Number of slots: Changed (Two modules are required.) <br> 3) Program: The number of occupied I/O points, I/O signals, and buffer memory addresses are changed. <br> 4) Performance specifications: Changed ( $4 \mathrm{CH} /$ module) <br> 5) Functional specifications: Not changed |
|  |  | $\begin{aligned} & \text { Q68TD-G-H01 } \\ & \text { Q68TD-G-H02 } \end{aligned}$ | 1) External wiring: Connector wiring and cable size are changed. <br> 2) Number of slots: Not changed <br> 3) Program: The number of occupied I/O points, I/O signals, and buffer memory addresses are changed. <br> 4) Performance specifications: Not changed <br> 5) Functional specifications: The disconnection detection function is not supported. (Only the Q68TD-G-H02 supports this function.) |
|  | A1S62RD3N | Q64RD | 1) External wiring: Cable size is changed. <br> 2) Number of slots: Not changed <br> 3) Program: The number of occupied I/O points, I/O signals, and buffer memory addresses are changed. <br> 4) Performance specifications: Changed ( $4 \mathrm{CH} /$ module) <br> 5) Functional specifications: Not changed |
|  |  | Q64RD-G | 1) External wiring: Cable size is changed. <br> 2) Number of slots: Not changed <br> 3) Program: The number of occupied I/O points, I/O signals, and buffer memory addresses are changed. <br> 4) Performance specifications: Changed ( $4 \mathrm{CH} /$ module) <br> 5) Functional specifications: Transformer isolation is provided between channels. |
|  | A1S62RD4N | Q64RD | 1) External wiring: Cable size is changed. <br> 2) Number of slots: Not changed <br> 3) Program: The number of occupied I/O points, I/O signals, and buffer memory addresses are changed. <br> 4) Performance specifications: Changed ( $4 \mathrm{CH} /$ module) <br> 5) Functional specifications: Not changed |
|  |  | Q64RD-G | 1) External wiring: Cable size is changed. <br> 2) Number of slots: Not changed <br> 3) Program: The number of occupied I/O points, I/O signals, and buffer memory addresses are changed. <br> 4) Performance specifications: Changed ( $4 \mathrm{CH} /$ module) <br> 5) Functional specifications: Transformer isolation is provided between channels. |


| AnS/QnAS series |  | Q series alternative model |  |
| :---: | :---: | :---: | :---: |
| Product | Model | Model | Remark (restrictions) |
| Heating-cooling temperature control module Temperature control module | A1S64TCTRT <br> Thermocouple, standard control | Q64TCTTN | 1) External wiring: Cable size is changed. <br> 2) Number of slots: Not changed <br> 3) Program: The number of occupied I/O points, I/O signals, and buffer memory addresses are changed. <br> 4) Performance specifications: Not changed <br> 5) Functional specifications: Changed |
|  | A1S64TCTRT <br> Thermocouple, heating-cooling control | Q64TCTTN | 1) External wiring: Cable size is changed. <br> 2) Number of slots: Not changed <br> 3) Program: The number of occupied I/O points, I/O signals, and buffer memory addresses are changed. <br> 4) Performance specifications: Not changed <br> 5) Functional specifications: Changed |
|  | A1S64TCTRT <br> Platinum resistance thermometer, standard control | Q64TCRTN | 1) External wiring: Cable size is changed. <br> 2) Number of slots: Not changed <br> 3) Program: The number of occupied I/O points, I/O signals, and buffer memory addresses are changed. <br> 4) Performance specifications: Not changed <br> 5) Functional specifications: Changed |
|  | A1S64TCTRT <br> Platinum resistance thermometer, heating-cooling control | Q64TCRTN | 1) External wiring: Cable size is changed. <br> 2) Number of slots: Not changed <br> 3) Program: The number of occupied I/O points, I/O signals, and buffer memory addresses are changed. <br> 4) Performance specifications: Not changed <br> 5) Functional specifications: Changed |
|  | A1S64TCTRTBW <br> Thermocouple, standard control | Q64TCTTBWN | 1) External wiring: Cable size is changed. <br> 2) Number of slots: Changed (Two slots are required. I/O assignment: 16 empty points for the first half, 16 intelligent points for the second half) <br> 3) Program: The number of occupied I/O points, I/O signals, and buffer memory addresses are changed. <br> 4) Performance specifications: Not changed <br> 5) Functional specifications: Changed |
|  | A1S64TCTRTBW <br> Thermocouple, heating-cooling control | Q64TCTTBWN | 1) External wiring: Cable size is changed. <br> 2) Number of slots: Changed (Two slots are required. I/O assignment: 16 empty points for the first half, 16 intelligent points for the second half) <br> 3) Program: The number of occupied I/O points, I/O signals, and buffer memory addresses are changed. <br> 4) Performance specifications: Not changed <br> 5) Functional specifications: Changed |
|  | A1S64TCTRTBW <br> Platinum resistance thermometer, standard control | Q64TCRTBWN | 1) External wiring: Cable size is changed. <br> 2) Number of slots: Changed (Two slots are required. I/O assignment: 16 empty points for the first half, 16 intelligent points for the second half) <br> 3) Program: The number of occupied I/O points, I/O signals, and buffer memory addresses are changed. <br> 4) Performance specifications: Not changed <br> 5) Functional specifications: Changed |

## [Issue No.] FA-A-0142-C

| AnS/QnAS series |  | Q series alternative model |  |
| :---: | :---: | :---: | :---: |
| Product | Model | Model | Remark (restrictions) |
| Heating-cooling temperature control module <br> Temperature control module | A1S64TCTRTBW <br> Platinum resistance thermometer, heating-cooling control | Q64TCRTBWN | 1) External wiring: Cable size is changed. <br> 2) Number of slots: Changed (Two slots are required. I/O assignment: 16 empty points for the first half, 16 intelligent points for the second half) <br> 3) Program: The number of occupied I/O points, I/O signals, and buffer memory addresses are changed. <br> 4) Performance specifications: Not changed <br> 5) Functional specifications: Changed |
|  | A1S64TCTT-S1 <br> Thermocouple, standard control | Q64TCTTN | 1) External wiring: Cable size is changed. <br> 2) Number of slots: Not changed <br> 3) Program: The number of occupied I/O points, I/O signals, and buffer memory addresses are changed. <br> 4) Performance specifications: Not changed <br> 5) Functional specifications: Changed |
|  | A1S64TCTTBW-S1 <br> Thermocouple, standard control | Q64TCTTBWN | 1) External wiring: Cable size is changed. <br> 2) Number of slots: Changed (Two slots are required. I/O assignment: 16 empty points for the first half, 16 intelligent points for the second half) <br> 3) Program: The number of occupied I/O points, I/O signals, and buffer memory addresses are changed. <br> 4) Performance specifications: Not changed <br> 5) Functional specifications: Changed |
|  | A1S64TCRT-S1 <br> Platinum resistance thermometer, standard control | Q64TCRTN | 1) External wiring: Cable size is changed. <br> 2) Number of slots: Not changed <br> 3) Program: The number of occupied I/O points, I/O signals, and buffer memory addresses are changed. <br> 4) Performance specifications: Not changed <br> 5) Functional specifications: Changed |
|  | A1S64TCRTBW-S1 <br> Platinum resistance thermometer, standard control | Q64TCRTBWN | 1) External wiring: Cable size is changed. <br> 2) Number of slots: Changed (Two slots are required. I/O assignment: 16 empty points for the first half, 16 intelligent points for the second half) <br> 3) Program:The number of occupied I/O points, I/O signals, and buffer memory addresses are changed. <br> 4) Performance specifications: Not changed <br> 5) Functional specifications: Changed |
|  | A1S62TCTT-S2 <br> Thermocouple, heating-cooling control | Q64TCTTN | 1) External wiring: Cable size is changed. <br> 2) Number of slots: Not changed <br> 3) Program: The number of occupied I/O points, I/O signals, and buffer memory addresses are changed. <br> 4) Performance specifications: Changed ( $2 \mathrm{CH} /$ module $\rightarrow 4 \mathrm{CH} /$ module) <br> 5) Functional specifications: Changed |
|  | A1S62TCTTBW-S2 <br> Thermocouple, heating-cooling control | Q64TCTTBWN | 1) External wiring: Cable size is changed. <br> 2) Number of slots: Changed (Two slots are required. I/O assignment: 16 empty points for the first half, 16 intelligent points for the second half) <br> 3) Program: The number of occupied I/O points, I/O signals, and buffer memory addresses are changed. <br> 4) Performance specifications: Changed ( $2 \mathrm{CH} /$ module $\rightarrow 4 \mathrm{CH} /$ module) <br> 5) Functional specifications: Changed |
|  | A1S62TCRT-S2 <br> Platinum resistance thermometer, heating-cooling control | Q64TCRTN | 1) External wiring: Cable size is changed. <br> 2) Number of slots: Not changed <br> 3) Program: The number of occupied I/O points, I/O signals, and buffer memory addresses are changed. <br> 4) Performance specifications: Changed ( $2 \mathrm{CH} /$ module $\rightarrow 4 \mathrm{CH} /$ module) <br> 5) Functional specifications: Changed |
|  | A1S62TCRTBW-S2 <br> Platinum resistance thermometer, heating-cooling control | Q64TCRTBWN | 1) External wiring: Cable size is changed. <br> 2) Number of slots: Changed (Two slots are required. I/O assignment: 16 empty points for the first half, 16 intelligent points for the second half) <br> 3) Program: The number of occupied I/O points, I/O signals, and buffer memory addresses are changed. <br> 4) Performance specifications: Changed ( $2 \mathrm{CH} /$ module $\rightarrow 4 \mathrm{CH} /$ module) <br> 5) Functional specifications: Changed |


| AnS/QnAS series |  | Q series alternative model |  |
| :--- | :--- | :--- | :--- |
| Product | Model | Model | Remark (restrictions) |

*2 An input filter system of the QD62-H01 and QD62-H02 is the same as that of A/AnS series high-speed counter modules. For this reason, modules can be replaced without considering the specifications of the existing pulse generator such as an encoder. When replacing the A1SD61, select a module based on the specifications such as the counting speed.

| AnS/QnAS series |  | Q series alternative model |  |
| :---: | :---: | :---: | :---: |
| Product | Model | Model | Remark (restrictions) |
| High-speed counter module | A1SD62 | QD62 | 1) External wiring: Terminal block wiring $\rightarrow$ Connector wiring Cable size is changed. <br> 2) Number of slots: Not changed <br> 3) Counting speed: Can be switched (200KPPS, 100KPPS, or 10KPPS). <br> 4) Counting range: <br> 32-bit signed binary (-2147483648 to 2147483647 ) <br> Program needs to be reviewed. <br> 5) Program: The number of occupied I/O points, I/O signals and buffer memory addresses are changed. <br> 6) Performance specifications: Not changed <br> 7) Function specifications: Not changed |
|  | A1SD62E | QD62E | 1) External wiring: Terminal block wiring $\rightarrow$ Connector wiring Cable size is changed. <br> 2) Number of slots: Not changed <br> 3) Counting speed: Can be switched (200KPPS, 100KPPS, or 10KPPS). <br> 4) Counting range: 32-bit signed binary (-2147483648 to 2147483647 ) Program needs to be reviewed. <br> 5) Program: The number of occupied I/O points, I/O signals and buffer memory addresses are changed. <br> 6) Performance specifications: Not changed <br> 7) Function specifications: Not changed |
|  | A1SD62D, A1SD62D-S1 | QD62D | 1) External wiring: Terminal block wiring $\rightarrow$ Connector wiring Cable size is changed. <br> 2) Number of slots: Not changed <br> 3) Counting speed: Can be switched (500KPPS, 200KPPS, 100KPPS, or 10KPPS). <br> 4) Counting range: 32-bit signed binary (-2147483648 to 2147483647 ) Program needs to be reviewed. <br> 5) Program: The number of occupied I/O points, I/O signals and buffer memory addresses are changed. <br> 6) Performance specifications: Not changed <br> 7) Function specifications: Not changed |


| AnS/QnAS series |  | Q series alternative model |  |
| :---: | :---: | :---: | :---: |
| Product | Model | Model | Remark (restrictions) |
| Positioning module | A1SD70 | QD73A1 | 1) External wiring: Not changed (External power supply (+/-15VDC) is not required.) <br> (The directions of connecting a connector are reverse.) <br> 2) Number of slots: Not changed (Two slots are occupied.) <br> 3) Program: Buffer memory assignment is changed. Some setting methods are changed. <br> 4) Performance specifications: Backward compatible <br> 5) Function specifications: Partially changed (some LED indicators not equipped, some setting methods changed) |
|  | A1SD75P1-S3 | QD75P1*3 (when an open collector is connected) QD75D1 ${ }^{* 3}$ (when a differential driver is connected) | 1) External wiring: Connector and wiring are changed. <br> 2) Number of slots: Not changed <br> 3) Program: I/O signals and buffer memory assignment are changed. The entire program needs to be reviewed according to the specifications change. <br> 4) Performance specifications: Not changed <br> 5) Function specifications: Partially changed <br> (Example: Manual pulse generator 1 /axis $\rightarrow 1$ /module) |
|  | A1SD75P2-S3 | QD75P2*3 (when an open collector is connected) <br> QD75D2 ${ }^{* 3}$ (when a differential driver is connected) | 1) External wiring: Connector and wiring are changed. <br> 2) Number of slots: Not changed <br> 3) Program: I/O signals and buffer memory assignment are changed. The entire program needs to be reviewed according to the specifications change. <br> 4) Performance specifications: Not changed <br> 5) Function specifications: Partially changed <br> (Example: Manual pulse generator 1 /axis $\rightarrow 1$ /module) |
|  | A1SD75P3-S3 | QD75P4*3 (when an open collector is connected) QD75D4*3 (when a differential driver is connected) | 1) External wiring: Connector and wiring are changed. <br> 2) Number of slots: Not changed <br> 3) Program: I/O signals and buffer memory assignment are changed. The entire program needs to be reviewed according to the specifications change. <br> 4) Performance specifications: Not changed <br> 5) Function specifications: Partially changed <br> (Example: Manual pulse generator 1 /axis $\rightarrow 1$ /module) |
|  | A1SD75M1 | QD75M1 | 1) External wiring: Connector and wiring are changed. <br> 2) Number of slots: Not changed <br> 3) Program: I/O signals and buffer memory assignment are changed. The entire program needs to be reviewed according to the specifications change. <br> 4) Performance specifications: Backward compatible <br> 5) Function specifications: Partially changed (Example: Manual pulse generator 1 /axis $\rightarrow 1$ /module) |
|  | A1SD75M2 | QD75M2 | 1) External wiring: Connector and wiring are changed. <br> 2) Number of slots: Not changed <br> 3) Program: I/O signals and buffer memory assignment are changed. The entire program needs to be reviewed according to the specifications change. <br> 4) Performance specifications: Backward compatible <br> 5) Function specifications: Partially changed <br> (Example: Manual pulse generator 1 /axis $\rightarrow 1$ /module) |
|  | A1SD75M3 | QD75M4 | 1) External wiring: Connector and wiring are changed. <br> 2) Number of slots: Not changed <br> 3) Program: I/O signals and buffer memory assignment are changed. The entire program needs to be reviewed according to the specifications change. <br> 4) Performance specifications: Backward compatible <br> 5) Function specifications: Partially changed (Example: Manual pulse generator 1 /axis $\rightarrow 1$ /module) |

[^5]
### 12.4 Network module

### 12.4.1 Replacement with $L$ series

| AnS/QnAS series |  | L series alternative model |  |
| :--- | :--- | :--- | :--- |
| Product | Model | Model | Remarks (restrictions) |
| CC-Link master/local <br> module | A1SJ61BT11 | LJ61BT11/ <br> L26CPU-BT/ <br> L26CPU-PBT | The specifications of the interface are the same. <br> Settings such as the station information setting are configured in the <br> CC-Link module configuration window of the network parameter <br> instead of using a program. <br> For details on modification, refer to the transition handbook. |
|  | A1SJ61QBT11 |  | The interface is backward compatible. <br> Each module has two channels instead of one channel. <br> Serial communication <br> module |
|  | A1SJ71UC24-R2 | LJ71C24 | For program modification, refer to the transition handbook. |

### 12.4.2 Replacement with $Q$ series

| AnS series |  | Q series alternative model |  |
| :--- | :--- | :--- | :--- |
| Product | Model | Model | Remarks (restrictions) |


| AnS series |  | Q series alternative model |  |
| :---: | :---: | :---: | :---: |
| Product | Model | Model | Remarks (restrictions) |
| MELSECNET/B data link module | A1SJ71AT21B | QJ71NT11B <br> QJ71BR11 <br> QJ71LP21-25 | It is recommended to replace the MELSECNET (II) and MELSECNET/B data link system with the MELSECNET/H network system. <br> When both a local station and a remote I/O station exist, the PLC to PLC network and remote I/O network are respectively required for these stations. <br> To replace the system gradually with the existing MELSECNET (II) used, it is recommended to use the A1SJ71AD23(B)Q, a MELSECNETII local station module. |
|  | A1SJ72T25B | QJ72LP25-25 <br> QJ72BR15 | When replacing a module in a remote I/O network master station with a QCPU, replace a module in a remote I/O station with a $Q$ series module. |
|  | A1SJ71AP21 <br> A1SJ71AP21-S3 <br> A1SJ71AR21 | QJ71LP21-25 <br> QJ71LP21G <br> QJ71BR11 | It is recommended to replace the MELSECNET (II) and MELSECNET/B data link system with the MELSECNET/H network system. <br> When both a local station and a remote I/O station exist, the PLC to PLC network and remote I/O network are respectively required for these stations. <br> To replace the system gradually with the existing MELSECNET (II) used, it is recommended to use the A1SJ71Aロ23(B)Q, a MELSECNETII local station module. |
| MELSECNET/10 <br> (PLC to PLC network) | A1SJ71LP21 | QJ71LP21-25 | No restriction |
|  | A1SJ71LR21 | QJ71BR11 | Coaxial loop system $\rightarrow$ Coaxial bus system |
|  | A1SJ71BR11 | QJ71BR11 | No restriction |
|  | A1SJ71QLP21 | QJ71LP21-25 |  |
|  | A1SJ71QLP21S | QJ71LP21S |  |
|  | A1SJ71QLR21 | QJ71BR11 | Coaxial loop system $\rightarrow$ Coaxial bus system |
|  | A1SJ71QBR11 | QJ71BR11 | No restriction |
| MELSECNET/10 (remote I/O network) master station module | A1SJ71LP21 | QJ71LP21-25 |  |
|  | A1SJ71LR21 | QJ71BR11 | Coaxial loop system $\rightarrow$ Coaxial bus system |
|  | A1SJ71BR11 | QJ71BR11 | No restriction |
|  | A1SJ71QLP21 | QJ71LP21-25 |  |
|  | A1SJ71QLP21S | QJ71LP21S |  |
|  | A1SJ71QLR21 | QJ71BR11 | Coaxial loop system $\rightarrow$ Coaxial bus system |
|  | A1SJ71QBR11 | QJ71BR11 | No restriction |
| MELSECNET/10 (remote I/O network) remote I/O station | A1SJ71QLP25 | QJ72LP25-25 | When replacing a module in a remote I/O network master station with a QCPU, replace a module in a remote I/O station with a Q series module. |
|  | A1SJ71QLR25 | QJ72BR15 |  |
|  | A1SJ72QBR15 | QJ72BR15 |  |
| CC-Link master/local module | A1SJ61BT11 A1SJ61QBT11 | QJ61BT11N | The specifications of the interface are the same. <br> Settings such as the station information setting are configured in the CC-Link module configuration window of the network parameter instead of using a program. <br> For details on modification, refer to the transition handbook. |
| MELSECNET/MINI-S3 master module | A1SJ71PT32-S3 | QJ61BT11N | It is recommended to replace the MELSECNET/MINI-S3 system with the CC-Link system. Using an A2C CC-Link module requires no change in wiring. |
| MELSEC-I/OLINK | A1SJ51T64 | N/A | It is recommended to replace the MELSEC-I/OLINK system with the CC-Link/LT or AnyWire system. <br> For details, refer to Section 12.5 . |
| JEMANET(OPCN-1) interface module | A1SJ71J92-S3 | N/A | It is recommended to replace the OPCN-1 system with the MELSECNET/H or CC-Link system. |
| B/NET interface module | A1SJ71B62-S3 | B-QIF | For details, please consult your local Mitsubishi representative. |

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| AnS series |  | Q series alternative model |  |
| :--- | :--- | :--- | :--- |
| Product | Model | Model | Remarks (restrictions) |
| Serial communication <br> module | A1SJ71UC24-R2 | QJ71C24N | The interface is backward compatible. <br> Each module has two channels instead of one channel. <br> Eor program modification, refer to the transition handbook. |
|  |  | QJ71C24N-R2 |  |
|  | A1SJ71UC24-R4 | QJ71C24N | The interface is backward compatible. |
|  |  | QJ71C24N-R4 | For program modification, refer to the transition handbook. |
|  |  | QJ71C24N | QJ71C24N-R2 |

### 12.5 MELSEC-I/OLINK

### 12.5.1 Replacement with AnyWire DB A20 series

The models listed below have been selected based on the hardware specifications.
Program modification or different wiring is required because the contents of the addresses of remote stations are different.

| I/OLINK series |  | DB A20 series alternative model |  |
| :---: | :---: | :---: | :---: |
| Product | Model | Model | Remarks (restrictions) |
| Master module | A1SJ51T64 | QJ51AW12D2 | It is recommended to replace a module with an AnyWire DB A20 module. For details, refer to the user's manual for the corresponding module. |
| Input module | AJ55TB3-4D (positive common type) | A20SB-04U | 1) External wiring: Changed <br> 2) Number of modules: Not changed <br> 3) Program: Changed <br> 4) Specifications: <br> Rated input voltage: Not changed <br> Rated input current: Not changed <br> ON voltage/ON current: Changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Not changed <br> 5) Functions: Changed (wiring: 3-wire $\rightarrow 2$-wire) (A negative common type cannot be used.) |
|  | AJ55TB3-4D (negative common type) | A20SB-04US | 1) External wiring: Changed <br> 2) Number of modules: Not changed <br> 3) Program: Changed <br> 4) Specifications: <br> Rated input voltage: Not changed <br> Rated input current: Not changed <br> ON voltage/ON current: Changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Not changed <br> 5) Functions: Changed (wiring: 3-wire $\rightarrow 2$-wire) (A positive common type cannot be used.) |
|  | AJ55TB3-8D <br> (positive common type) | A20SB-08UD | 1) External wiring: Changed <br> 2) Number of modules: Not changed <br> 3) Program: Changed <br> 4) Specifications: <br> Rated input voltage: Not changed <br> Rated input current: Not changed <br> ON voltage/ON current: Changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Not changed <br> 5) Functions: Changed (A negative common type cannot be used.) |


| I/OLINK series |  | DB A20 series alternative model |  |
| :---: | :---: | :---: | :---: |
| Product | Model | Model | Remarks (restrictions) |
| Input module | AJ55TB3-8D <br> (negative common type) | A20SB-08USD-1 | 1) External wiring: Changed <br> 2) Number of modules: Not changed <br> 3) Program: Changed <br> 4) Specifications: <br> Rated input voltage: Not changed <br> Rated input current: Not changed <br> ON voltage/ON current: Changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Not changed <br> 5) Functions: Changed (A positive common type cannot be used.) |
|  | AJ55TB3-16D <br> (positive common type) | A20SB-16UD | 1) External wiring: Changed <br> 2) Number of modules: Not changed <br> 3) Program: Changed <br> 4) Specifications: <br> Rated input voltage: Not changed <br> Rated input current: Not changed <br> ON voltage/ON current: Changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Not changed <br> 5) Functions: Changed (A negative common type cannot be used.) (8 points/common $\rightarrow 16$ points/common) |
|  | AJ55TB3-16D <br> (negative common type) | A20SB-16USD | 1) External wiring: Changed <br> 2) Number of modules: Not changed <br> 3) Program: Changed <br> 4) Specifications: <br> Rated input voltage: Not changed <br> Rated input current: Not changed <br> ON voltage/ON current: Changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Not changed <br> 5) Functions: Changed (A positive common type cannot be used.) (8 points/common $\rightarrow 16$ points/common) |
| Output module | AJ55TB2-4R | A20PB-04RS | 1) External wiring: Changed <br> 2) Number of modules: Not changed <br> 3) Program: Changed <br> 4) Specifications: <br> Rated input voltage: Changed (The voltage that can be used is equivalent.) <br> Rated input current: Not changed <br> Maximum switching frequency: Changed ( 3600 times/hour $\rightarrow 20$ times/minute) <br> 5) Functions: Changed (4 points/common $\rightarrow$ All points independent) |


| I/OLINK series |  | DB A20 series alternative model |  |
| :---: | :---: | :---: | :---: |
| Product | Model | Model | Remarks (restrictions) |
| Output module | AJ55TB2-8R | A20PB-08RS | 1) External wiring: Changed <br> 2) Number of modules: Not changed <br> 3) Program: Changed <br> 4) Specifications: <br> Rated input voltage: Changed (The voltage that can be used is equivalent.) <br> Rated input current: Not changed <br> Maximum switching frequency: Changed ( 3600 times/hour $\rightarrow 20$ times/minute) <br> 5) Functions: Changed (8 points/common $\rightarrow$ All points independent) |
|  | AJ55TB2-16R | A20PB-16RS | 1) External wiring: Changed <br> 2) Number of modules: Not changed <br> 3) Program: Changed <br> 4) Specifications: <br> Rated input voltage: Changed (The voltage that can be used is equivalent.) <br> Rated input current: Not changed <br> Maximum switching frequency: Changed (3600 times/hour $\rightarrow 20$ times/minute) <br> 5) Functions: Changed (8 points/common $\rightarrow$ All points independent) |
|  | AJ55TB2-4T | A20PB-04U | 1) External wiring: Changed <br> 2) Number of modules: Not changed <br> 3) Program: Changed <br> 4) Specifications: <br> Rated input voltage: Changed (12VDC is not applicable.) <br> Rated input current: Changed ( $0.5 \mathrm{~A} /$ point $\rightarrow 0.2 \mathrm{~A} /$ point) <br> 5) Functions: Changed (Surge suppressor: Supported $\rightarrow$ Not supported) |
|  | AJ55TB2-8T | A20PB-08U | 1) External wiring: Changed <br> 2) Number of modules: Not changed <br> 3) Program: Changed <br> 4) Specifications: <br> Rated input voltage: Changed (12VDC is not applicable.) <br> Rated input current: Changed ( $0.5 \mathrm{~A} /$ point $\rightarrow 0.2 \mathrm{~A} /$ point) <br> 5) Functions: Changed (Surge suppressor: Supported $\rightarrow$ Not supported) |
|  | AJ55TB2-16T | A20PB-16U | 1) External wiring: Changed <br> 2) Number of modules: Not changed <br> 3) Program: Changed <br> 4) Specifications: <br> Rated input voltage: Changed (12VDC is not applicable.) <br> Rated input current: Changed ( $0.5 \mathrm{~A} /$ point $\rightarrow 0.2 \mathrm{~A} /$ point ) <br> 5) Functions: Changed (Surge suppressor: Supported $\rightarrow$ Not supported) |


| I/OLINK series |  | DB A20 series alternative model |  |
| :---: | :---: | :---: | :---: |
| Product | Model | Model | Remarks (restrictions) |
| I/O module | AJ55TB32-4DR (positive common type) | $\begin{aligned} & \text { A20SB-04U } \\ & + \text { A20PB-04RS } \end{aligned}$ | 1) External wiring: Changed <br> 2) Number of modules: Changed (Two modules are required.) <br> 3) Program: Changed <br> 4) Specifications: <br> (Input part) <br> Rated input voltage: Not changed <br> Rated input current: Not changed <br> ON voltage/ON current: Changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Not changed <br> (Output part) <br> Rated load voltage: Changed (The voltage that can be used is equivalent.) <br> Rated load current: Not changed <br> Maximum switching frequency: Changed (3600 times/hour $\rightarrow 20$ times/minute) <br> 5) Functions: Changed <br> (Input part) <br> Number of input points: $2 \rightarrow 4$ <br> Wiring: 3-wire $\rightarrow 2$-wire <br> A negative common type cannot be used. <br> (Output part) <br> Number of output points: $2 \rightarrow 4$ <br> 2 points/common $\rightarrow$ All points independent |
|  | AJ55TB32-4DR (negative common type) | $\begin{aligned} & \text { A20SB-04US } \\ & + \text { A20PB-04RS } \end{aligned}$ | 1) External wiring: Changed <br> 2) Number of modules: Changed (Two modules are required.) <br> 3) Program: Changed <br> 4) Specifications: <br> (Input part) <br> Rated input voltage: Not changed <br> Rated input current: Not changed <br> ON voltage/ON current: Changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Not changed <br> (Output part) <br> Rated load voltage: Changed (The voltage that can be used is equivalent.) <br> Rated load current: Not changed <br> Maximum switching frequency: Changed (3600 times/hour $\rightarrow 20$ times/minute) <br> 5) Functions: Changed <br> (Input part) <br> Number of input points: $2 \rightarrow 4$ <br> Wiring: 3-wire $\rightarrow 2$-wire <br> A positive common type cannot be used. <br> (Output part) <br> Number of output points: $2 \rightarrow 4$ <br> 2 points/common $\rightarrow$ All points independent |


| I/OLINK series |  | DB A20 series alternative model |  |
| :---: | :---: | :---: | :---: |
| Product | Model | Model | Remarks (restrictions) |
| I/O module | AJ55TB32-8DR (positive common type) | A20SB-04U <br> + A20PB-04RS | 1) External wiring: Changed <br> 2) Number of modules: Changed (Two modules are required.) <br> 3) Program: Changed <br> 4) Specifications: <br> (Input part) <br> Rated input voltage: Not changed <br> Rated input current: Not changed <br> ON voltage/ON current: Changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Not changed <br> (Output part) <br> Rated load voltage: Changed (The voltage that can be used is equivalent.) <br> Rated load current: Not changed <br> Maximum switching frequency: Changed (3600 times/hour $\rightarrow 20$ times/minute) <br> 5) Functions: Changed <br> (Input part) <br> Wiring: 3-wire $\rightarrow 2$-wire <br> A negative common type cannot be used. <br> (Output part) <br> 4 points/common $\rightarrow$ All points independent |
|  | AJ55TB32-8DR (negative common type) | A20SB-04US <br> + A20PB-04RS | 1) External wiring: Changed <br> 2) Number of modules: Changed (Two modules are required.) <br> 3) Program: Changed <br> 4) Specifications: <br> (Input part) <br> Rated input voltage: Not changed <br> Rated input current: Not changed <br> ON voltage/ON current: Changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Not changed <br> (Output part) <br> Rated load voltage: Changed (The voltage that can be used is equivalent.) <br> Rated load current: Not changed <br> Maximum switching frequency: Changed (3600 times/hour $\rightarrow 20$ times/minute) <br> 5) Functions: Changed <br> (Input part) <br> Wiring: 3-wire $\rightarrow 2$-wire <br> A positive common type cannot be used. <br> (Output part) <br> 4 points/common $\rightarrow$ All points independent |


| I/OLINK series |  | DB A20 series alternative model |  |
| :--- | :--- | :--- | :--- | :--- |
| Product | Model | Model | Remarks (restrictions) |


| I/OLINK series |  | DB A20 series alternative model |  |
| :---: | :---: | :---: | :---: |
| Product | Model | Model | Remarks (restrictions) |
| I/O module | AJ55TB32-4DT | A20XB-16UD | 1) External wiring: Changed <br> 2) Number of modules: Not changed <br> 3) Program: Changed <br> 4) Specifications: <br> (Input part) <br> Rated input voltage: Not changed <br> Rated input current: Not changed <br> ON voltage/ON current: Changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Not changed <br> (Output part) <br> Rated load voltage: Not changed <br> Rated load current: Changed ( $0.5 \mathrm{~A} /$ point $\rightarrow 0.2 \mathrm{~A} /$ point) <br> 5) Functions: Changed <br> (Input part) <br> Number of input points: $2 \rightarrow 8$ <br> (Output part) <br> Number of output points: $2 \rightarrow 8$ <br> Surge suppressor: Supported $\rightarrow$ Not supported |
|  | AJ55TB32-8DT | A20XB-16UD | 1) External wiring: Changed <br> 2) Number of modules: Not changed <br> 3) Program: Changed <br> 4) Specifications: <br> (Input part) <br> Rated input voltage: Not changed <br> Rated input current: Not changed <br> ON voltage/ON current: Changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Not changed <br> (Output part) <br> Rated load voltage: Not changed <br> Rated load current: Changed ( $0.5 \mathrm{~A} /$ point $\rightarrow 0.2 \mathrm{~A} /$ point) <br> 5) Functions: Changed <br> (Input part) <br> Number of input points: $4 \rightarrow 8$ <br> (Output part) <br> Number of output points: $4 \rightarrow 8$ <br> Surge suppressor: Supported $\rightarrow$ Not supported |
|  | AJ55TB32-16DT | A20XB-16UD | 1) External wiring: Changed <br> 2) Number of modules: Not changed <br> 3) Program: Changed <br> 4) Specifications: <br> (Input part) <br> Rated input voltage: Not changed <br> Rated input current: Not changed <br> ON voltage/ON current: Changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Not changed <br> (Output part) <br> Rated load voltage: Not changed <br> Rated load current: Changed ( $0.5 \mathrm{~A} /$ point $\rightarrow 0.2 \mathrm{~A} /$ point) <br> 5) Functions: Changed <br> (Input part) <br> Not changed <br> (Output part) <br> Surge suppressor: Supported $\rightarrow$ Not supported |

### 12.5.2 Replacement with CC-Link/LT

| MELSEC-I/OLINK |  | Alternative models for CC-Link/LT |  |
| :---: | :---: | :---: | :---: |
| Product | Model | Model | Remarks (restrictions) |
| Input <br> module | AJ55TB3-4D | CL1X4-D1B2 | 1) External wiring: Changed <br> 2) Number of modules: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed. (in 4-point mode) <br> 4) Specifications: <br> Rated input voltage: Not changed <br> Rated input current: Changed <br> ON voltage/ON current: Changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Changed <br> 5) Functions: Changed (3-wire type $\rightarrow 2$-wire type) |
|  | AJ55TB3-8D | CL2X8-D1B2 | 1) External wiring: Changed <br> 2) Number of modules: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed. (in 4-point mode) <br> 4) Specifications: <br> Rated input voltage: Not changed <br> Rated input current: Changed <br> ON voltage/ON current: Changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Changed <br> 5) Functions: Changed (3-wire type $\rightarrow$ 2-wire type) |
|  | AJ55TB3-16D | CL2X8-D1B2 | 1) External wiring: Changed <br> 2) Number of modules: Changed (Two modules are required.) <br> 3) Program: <br> Number of occupied I/O points: Not changed. (in 4-point mode) <br> 4) Specifications: <br> Rated input voltage: Not changed <br> Rated input current: Changed <br> ON voltage/ON current: Changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Changed <br> 5) Functions: Changed (3-wire type $\rightarrow 2$-wire type) |
| Output module | AJ55TB2-4R | CL1Y4-R1B2 | 1) External wiring: Changed <br> 2) Number of modules: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed. (in 4-point mode) <br> 4) Specifications: <br> Rated output voltage: Changed <br> Rated output current: Not changed <br> 5) Functions: Not changed |


| MELSEC-I/OLINK |  | Alternative models for CC-Link/LT |  |
| :---: | :---: | :---: | :---: |
| Product | Model | Model | Remarks (restrictions) |
| Output module | AJ55TB2-8R | CL1Y4-R1B2 | 1) External wiring: Changed <br> 2) Number of modules: Changed (Two modules are required.) <br> 3) Program: <br> Number of occupied I/O points: Not changed. (in 4-point mode) <br> 4) Specifications: <br> Rated output voltage: Changed <br> Rated output current: Not changed <br> 5) Functions: Not changed |
|  | AJ55TB2-16R | CL1Y4-R1B2 | 1) External wiring: Changed <br> 2) Number of modules: Changed (Four modules are required.) <br> 3) Program: <br> Number of occupied I/O points: Not changed. (in 4-point mode) <br> 4) Specifications: <br> Rated output voltage: Changed <br> Rated output current: Not changed <br> 5) Functions: Not changed |
|  | AJ55TB2-4T | CL1Y4-T1B2 | 1) External wiring: Changed <br> 2) Number of modules: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed. (in 4-point mode) <br> 4) Specifications: <br> Rated output voltage: Not changed <br> Rated output current: Changed <br> 5) Functions: Not changed |
|  | AJ55TB2-8T | CL2Y8-TP1B2 | 1) External wiring: Changed <br> 2) Number of modules: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed. (in 4-point mode) <br> 4) Specifications: <br> Rated output voltage: Not changed <br> Rated output current: Changed <br> 5) Functions: Not changed |
|  | AJ55TB2-16T | CL2Y8-TP1B2 | 1) External wiring: Changed <br> 2) Number of modules: Changed (Two modules are required.) <br> 3) Program: <br> Number of occupied I/O points: Not changed. (in 4-point mode) <br> 4) Specifications: <br> Rated output voltage: Not changed <br> Rated output current: Changed <br> 5) Functions: Not changed |


| MELSEC-I/OLINK |  | Alternative models for CC-Link/LT |  |
| :---: | :---: | :---: | :---: |
| Product | Model | Model | Remarks (restrictions) |
| I/O module | AJ55TB32-4DR | CL1XY4-DR1B2 | 1) External wiring: Changed <br> 2) Number of modules: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed. (in 4-point mode) <br> 4) Specifications: <br> Rated input voltage: Not changed <br> Rated input current: Changed <br> ON voltage/ON current: Changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Changed <br> Rated output voltage: Changed <br> Rated output current: Not changed <br> 5) Functions: Changed (input: 3-wire type $\rightarrow$ 2-wire type) |
|  | AJ55TB32-8DR | CL1XY8-DR1B2 | 1) External wiring: Changed <br> 2) Number of modules: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed. (in 4-point mode) <br> 4) Specifications: <br> Rated input voltage: Not changed <br> Rated input current: Changed <br> ON voltage/ON current: Changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Changed <br> Rated output voltage: Changed <br> Rated output current: Not changed <br> 5) Functions: Changed (input: 3-wire type $\rightarrow 2$-wire type) |
|  | AJ55TB32-16DR | CL1XY8-DR1B2 | 1) External wiring: Changed <br> 2) Number of modules: Changed (Two modules are required.) <br> 3) Program: <br> Number of occupied I/O points: Not changed. (in 4-point mode) <br> 4) Specifications: <br> Rated input voltage: Not changed <br> Rated input current: Changed <br> ON voltage/ON current: Changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Changed <br> Rated output voltage: Changed <br> Rated output current: Not changed <br> 5) Functions: Changed (input: 3-wire type $\rightarrow$ 2-wire type) |


| MELSEC-I/OLINK |  | Alternative models for CC-Link/LT |  |
| :---: | :---: | :---: | :---: |
| Product | Model | Model | Remarks (restrictions) |
| I/O module | AJ55TB32-4DT | CL1XY4-DT1B2 | 1) External wiring: Changed <br> 2) Number of modules: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed. (in 4-point mode) <br> 4) Specifications: <br> Rated input voltage: Not changed <br> Rated input current: Changed <br> ON voltage/ON current: Changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Changed <br> Rated output voltage: Changed <br> Rated output current: Changed <br> 5) Functions: Changed (input: 3-wire type $\rightarrow$ 2-wire type) |
|  | AJ55TB32-8DT | CL1XY8-DT1B2 | 1) External wiring: Changed <br> 2) Number of modules: Not changed <br> 3) Program: <br> Number of occupied I/O points: Not changed. (in 4-point mode) <br> 4) Specifications: <br> Rated input voltage: Not changed <br> Rated input current: Changed <br> ON voltage/ON current: Changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Changed <br> Rated output voltage: Changed <br> Rated output current: Changed <br> 5) Functions: Changed (input: 3-wire type $\rightarrow$ 2-wire type) |
|  | AJ55TB32-16DT | CL1XY8-DT1B2 | 1) External wiring: Changed <br> 2) Number of modules: Changed (Two modules are required.) <br> 3) Program: <br> Number of occupied I/O points: Not changed. (in 4-point mode) <br> 4) Specifications: <br> Rated input voltage: Not changed <br> Rated input current: Changed <br> ON voltage/ON current: Changed <br> OFF voltage/OFF current: Changed <br> Input resistance: Changed <br> Rated output voltage: Changed <br> Rated output current: Changed <br> 5) Functions: Changed (input: 3-wire type $\rightarrow$ 2-wire type) |

## [Issue No.] FA-A-0142-C

REVISIONS

| Version | Print Date | Revision |
| :--- | :--- | :--- |
| - | October 2012 | First edition |
| A | November 2012 | The modem interface module (Q6TEL) is added to the list of models to be discontinued. |
| B |  | CPU module (A2ASCPU, A2ASCPU-S1, A2ASCPU-S30, A2SHCPU-S1, A1SJHCPU-S8), <br> base unit (A1S32B-E, A1S33B-E, A1S35B-E, A1S38B-E, A1S52B-S1, A1S55B-S1, <br> A1S58B-S1, A1S65B-S1, A1S68B-S1), battery (A8BAT-SET), the MODBUS interface <br> module (A1SJ71UC24-R2-S2, A1SJ71UC24-R4-S2), MELSECNET/10 network module <br> (A1SJ71LP21GE, A1SJ71QLP21GE), PROFIBUS interface module (A1SJ71PB92D, <br> A1SJ71PB93D), Devicenet interface module (A1SJ71DN91) are added to the list of models <br> to be discontinued. |
| C | March 2015 | The A1SD75-C01H and A1SD75-C01HA is deleted from the list of models to be discontinued <br> (resumption of production). |


[^0]:    *1 A CPU module with the computer link function is replaced by a CPU module or an LJ71C24-R2.

[^1]:    *2 The instruction for file registers and special function modules need to be replaced with those for the $L$ series.

[^2]:    *1 A CPU module with the computer link function is replaced by a CPU module or an LJ71C24-R2.

[^3]:    *2 The instruction for file registers and special function modules need to be replaced with those for the $Q$ series.

[^4]:    *1 The Q68AD-G cannot be mounted on the $Q$ series large type base unit (Q3DBL, Q6ロBL, Q55BL).

[^5]:    *3 The QD75P $\square N$ and QD75D $\square N$ are backward-compatible with the QD75P $\square$ and QD75DD. Programs upon replacement are the same.
    The performance, such as start time and data refreshing cycle, has been improved; therefore, check the processing timing and modify the sequence program if required.

