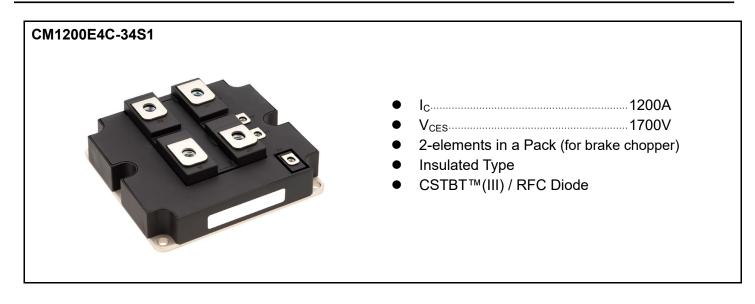


< High Voltage Insulated Gate Bipolar Transistor: HVIGBT >

CM1200E4C-34S1

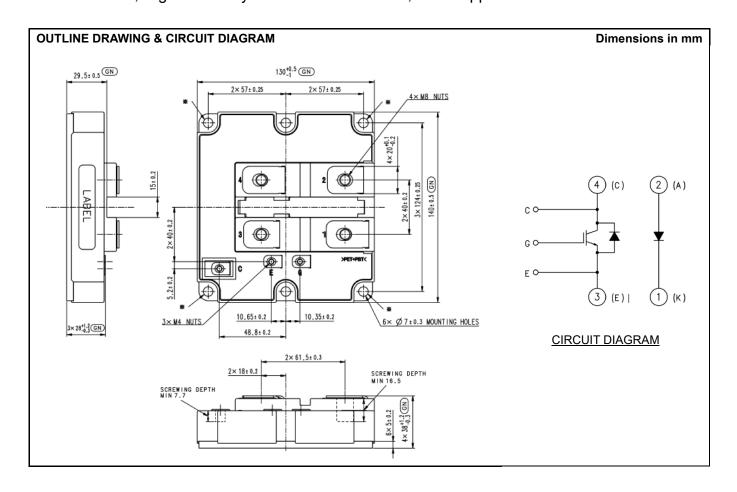
HIGH POWER SWITCHING USE INSULATED TYPE

5th-Version HVIGBT (High Voltage Insulated Gate Bipolar Transistor) Modules



APPLICATION

Traction drives, High Reliability Converters / Inverters, DC choppers



< High Voltage Insulated Gate Bipolar Transistor : HVIGBT >

CM1200E4C-34S1 HIGH POWER SWITCHING USE INSULATED TYPE

5th-Version HVIGBT (High Voltage Insulated Gate Bipolar Transistor) Modules

MAXIMUM RATINGS

Item	Symbol	Conditions			Unit			
Collector-emitter voltage	\/	$V_{GF} = 0 \text{ V}$ $T_i = -40 \sim +150 ^{\circ}\text{C}$			V			
Gate-emitter short-circuited	V _{CES}	$T_i = -50 ^{\circ}\text{C}$			V			
Gate-emitter voltage Collector-emitter short-circuited	V _{GES}	$V_{CE} = 0 \text{ V}$ $T_{j} = 25 ^{\circ}\text{C}$			$V_{CE} = 0 \text{ V}$ $T_j = 25 \text{ °C}$			V
Depotitive needs reverse veltage	.,	$T_i = -40 \sim +150 ^{\circ}\text{C}$		1700	V			
Repetitive peak reverse voltage	V _{RRM}	(Note 1)	T _j = -50 °C	1650				
Collector current	Ic	$T_c = 90 ^{\circ}\text{C}$, DC		1200	Α			
(Repetitive peak) Collector current	I _{CRM}	Pulse (Note 2)		2400	Α			
Emitter current	Ι _Ε	DC (Note 3)		1200	Α			
(Repetitive peak) Emitter current	I _{ERM}	Pulse (Note 2, 3)			Α			
Forward current	I _F	DC (Note 1)			Α			
Repetitive peak forward current	I _{FRM}	Pulse (Note 1, 2)			Α			
Total power dissipation	P _{tot}	Γ _c = 25 °C , IGBT part(Note 4)			W			
Isolation voltage	V _{isol}	Charged part to the baseplate RMS sinusoidal, 60Hz 1min			V_{rms}			
Partial discharge charge	Q_{pd}	Charged part to the baseplate, RMS sinusoidal, 60 Hz V_1 = 3500 V, V_2 = 2600 V, (acc. to IEC 61287-1)			рС			
Junction temperature	T _i	Maximum temperature range in off-state or on-state(non-switching)	Maximum temperature range in off-state or on-state(non-switching)					
Storage temperature	T _{stg}	Maximum case temperature range in off-state			°C			
Operating junction temperature	T _{jop}	Maximum junction temperature range for switching operation			°C			
Turn-off cllector current	I _{C(off)}	$V_{GE} = \pm 15 \text{ V}$, $L_s = 70 \text{ nH}$, $R_{G(off)} = 3.3 \Omega$, $V_{CC} \le 1200 \text{ V}$, $V_{CE} \le 1700 \text{ V}$		2400	Α			
Short-circuit withstand pulse duration	t _{pSC}	· · · · · · · · · · · · · · · · · · ·		10	μs			
Reverse recovery power dissipation	P _{rr}			1.1	MW			
Reverse recovery power dissipation	P_{rr}	I _F = 2400 A , L _s = 70 nH , V _{CC} ≤1200V, di/dt≤8000A/us,V _{RM} ≤1700V(Note 1)	V, di/dt≤8000A/us,V _{RM} ≤1700V(Note 1) T _i = 150 °C		MW			
Non-repetitive surge forward current	I _{FSM}	$t_{\rm p}$ = 10 ms , $V_{\rm R}$ = 50 V , F(t)weibull=1%, Half sine wave(Note 1) $T_{\rm i}$ = 150 °C		5.1	kA			
I2t value	l ² t	$t_p = 10 \text{ ms}$, $V_R = 50 \text{ V}$, $F(t)$ weibull=1%, Half sine wave(Note 1) $T_i = 150 \text{ °C}$			kA ² s			

ELECTRICAL CHARACTERISTICS

Item	Symbol	Conditions			Limits		Unit
10111	Symbol	Conditions		Min.	Тур.	Max.	Offic
0-11			T _j = 25 °C	-	-	4.0	mΑ
Collector-emitter cut-off current Gate-emitter short-circuited	I _{CES}	V _{CE} = 1700 V , V _{GE} = 0 V	T _j = 125 °C	-	1.8	-	mΑ
Gate-entitler short-circuited			T _j = 150 °C	-	•	40.0	mA
			T _j = 25 °C	-	•	2.5	mΑ
Peak reverse recovery current	I _{RRM}	V _{RM} = 1700 V(Note 1)	T _j = 125 °C	-	1.1	-	mA
			T _j = 150 °C	-	-	25	mΑ
Gate-emitter threshold voltage	$V_{GE(th)}$	$V_{CE} = 10 \text{ V}$, $I_{C} = 120 \text{mA}$	T _j = 25 °C	5.40	6.00	6.60	V
Gate leakage current Collector-emitter short-circuited	I _{GES}	V _{CE} = 0 V , V _{GE} = ±20 V	T _j = 25 °C	-0.5	1	0.5	μA
Gate charge	Q_{G}	V _{CC} = 850 V , I _C = 1200 A , V _{GE} = ±15 V	T _i = 25 °C	-	12.0	-	μC
Input capacitance	C _{ies}	V _{CE} = 10 V , V _{GE} = 0 V , f = 100kHz	T _i = 25 °C	-	216	-	nF
Output capacitance	C _{oes}	V _{CE} = 10 V , V _{GE} = 0 V , f = 100kHz	T _i = 25 °C	-	8.0	-	nF
Reverse transfer capacitance	C _{res}	V _{CE} = 10 V , V _{GE} = 0 V , f = 100kHz	T _j = 25 °C	-	1.6	-	nF
		I_C = 1200 A , V_{GE} = +15 V Between Collector - Eemitter auxiliary terminal(Note 5)	T _i = 25 °C	-	1.95	-	V
Collector-emitter saturation voltage	V_{CEsat}		T _j = 125 °C	-	2.25	-	V
			T _i = 150 °C	-	2.30	2.80	V
		4000 4 1/4 0 1/4	T _i = 25 °C	-	2.20	-	V
Emitter-collector voltage	V_{EC}	I _E = 1200 A , V _{GE} = 0 V Between Collector - Eemitter auxiliary terminal(Note 3,5)	T _i = 125 °C	-	2.35	-	V
			T _i = 150 °C	-	2.35	2.85	V
Forward voltage		I _F = 1200 A(Note 1,5)	T _i = 25 °C	-	2.20	-	V
	$V_{FM(Chip)}$		T _i = 125 °C	-	2.35	-	V
			T _i = 150 °C	-	2.35	2.85	V
			T _i = 25 °C	-	2.43	-	V
Forward voltage	V _{FM(Terminal)}	I _F = 1200 A (Note 1,5)	T _j = 125 °C	-	2.63	-	V
			T _i = 150 °C	-	2.64	-	V

ELECTRICAL CHARACTERISTICS

Item	Symbol	Conditions			Limits		Unit
	-,		_	Min.	Тур.	Max.	
Turn-on delay time	$t_{d(on)}$		T _i = 150 °C	-	-	1.10	μs
Rise time	ţ,		$T_j = 150 ^{\circ}\text{C}$	-	-	0.41	μs
Turn-on (switching) energy per pulse 10% integral		 \/ = 850 \/ = 1200 \	$T_i = 25 ^{\circ}\text{C}$	-	265	-	mJ
	E _{on(10%)}	$V_{CC} = 850 \text{ V}$, $I_{C} = 1200 \text{ A}$, $V_{GE} = \pm 15 \text{ V}$, $L_{s} = 70 \text{ nH}$	T _i = 125 °C	-	350	-	mJ
		$R_{G(on)} = 1.3 \Omega$, $R_{G(off)} = 3.3 \Omega$	T _i = 150 °C	-	355	-	mJ
		Inductive load(Note 6)	T _i = 25 °C	-	290	-	mJ
Turn-on (switching) energy	E _{on}		T _i = 125 °C	-	370	-	mJ
per pulse	Oil		T _i = 150 °C	-	380	-	mJ
			T _i = 25 °C	-	0.30	_	μs
Reverse recovery time			$T_i = 25 ^{\circ}\text{C}$	-	0.40	_	μs
The verse recovery unite	t _{rr}		$T_i = 150 ^{\circ}\text{C}$	_	0.45	_	μs
				-	735	_	A
D			T _j = 25 °C	ļ		-	
Reverse recovery current	I _{rr}		T _i = 125 °C	-	865		A
			T _j = 150 °C	-	875	-	Α
Reverse recovery charge			$T_j = 25 ^{\circ}\text{C}$	-	190	-	μC
10% integral	Q _{rr(10%)}	$V_{CC} = 850 \text{ V}$, $I_{E} = 1200 \text{ A}$, $V_{GE} = \pm 15 \text{ V}$, $L_{s} = 70 \text{ nH}$	T _j = 125 °C	-	295	-	μC
1070 mograf			T _j = 150 °C	-	365	-	μC
		$R_{G(on)} = 1.3 \Omega$, $R_{G(off)} = 3.3 \Omega$	T _i = 25 °C	-	265	-	μC
Reverse recovered charge	Q_{rr}	Inductive load(Note 3,6,7)	T _i = 125 °C	-	340	-	μC
			T _i = 150 °C	-	420	-	μC
	1	1	T _i = 25 °C	-	90	-	mJ
Reverse recovery energy	E _{rec(10%)}		T _i = 125 °C	-	150	-	mJ
per pulse 10% integral	Frec(10%)		T _i = 150 °C	-	195	-	mJ
			$T_i = 150^{\circ} \text{C}$	_	150	_	mJ
Reverse recovery energy per pulse	_		_	-	190	_	mJ
	E _{rec}		T _j = 125 °C	1			— .
			T _i = 150 °C	-	240	-	mJ
Reverse recovery time			$T_j = 25 ^{\circ}\text{C}$	-	0.30	-	μs
	t _{rr}		T _i = 125 °C	-	0.40	-	μs
			$T_j = 150 ^{\circ}\text{C}$	-	0.45	-	μs
Reverse recovery current	I _{rr}		$T_j = 25 ^{\circ}\text{C}$	-	735	-	Α
			T _j = 125 °C	-	865	-	Α
			T _i = 150 °C	-	875	-	Α
			T _i = 25 °C	-	190	-	μC
Reverse recovery charge	Q _{rr(10%)}		T _i = 125 °C	-	295	-	μC
10% integral	11(10%)	$V_{CC} = 850 \text{ V}$, $I_F = 1200 \text{ A}$, $V_{GE} = \pm 15 \text{ V}$, $L_s = 70 \text{ nH}$	T _i = 150 °C	-	365	-	μC
		$R_{G(on)} = 1.3 \Omega$, $R_{G(off)} = 3.3 \Omega$	T _i = 25 °C	-	265	-	μC
Reverse recovered charge	Q_{rr}	Inductive load(Note 1,6,7)	$T_i = 125 ^{\circ}\text{C}$	-	340	_	μC
The verse recovered charge	Q rr			-	420	_	μC
			T _j = 150 °C			-	<u> </u>
Reverse recovery energy	_		T _i = 25 °C	-	90	-	mJ
per pulse 10% integral	E _{rec(10%)}		T _j = 125 °C	-	150	-	mJ
			T _j = 150 °C	-	195	-	mJ
Reverse recovery energy			T _j = 25 °C	-	150	-	mJ
per pulse	E _{rec}		T _i = 125 °C	-	190	-	mJ
F P			T _j = 150 °C	-	240	-	mJ
			T _i = 25 °C	L -	1.20		μs
Turn-off delay time	$t_{d(off)}$		T _i = 125 °C	-	1.30	-	μs
Fall time Turn-off (switching) energy per pulse 10% integral			T _i = 150 °C	-	1.32	-	μs
			T _i = 25 °C	-	0.12	-	μs
	t _f		T _i = 125 °C	-	0.15	-	μs
		$V_{CC} = 850 \text{ V}$, $I_{C} = 1200 \text{ A}$, $V_{GE} = \pm 15 \text{ V}$, $L_{s} = 70 \text{ nH}$	T _i = 150 °C	-	0.17	-	μs
		$R_{G(on)} = 1.3 \Omega$, $R_{G(off)} = 3.3 \Omega$		-	200	-	mJ
		Inductive load(Note 6) $\frac{1_1-25}{1}$			280	-	
			T _i = 125 °C	-			mJ
			T _j = 150 °C	-	310	-	mJ
Turn-off (switching) energy	l _		T _i = 25 °C	-	260	-	mJ
per pulse	E _{off}		$T_{j} = 125 ^{\circ}\text{C}$	-	360	-	mJ
1 1			T _i = 150 °C	-	400		mJ

< High Voltage Insulated Gate Bipolar Transistor : HVIGBT >

CM1200E4C-34S1 HIGH POWER SWITCHING USE INSULATED TYPE

5th-Version HVIGBT (High Voltage Insulated Gate Bipolar Transistor) Modules

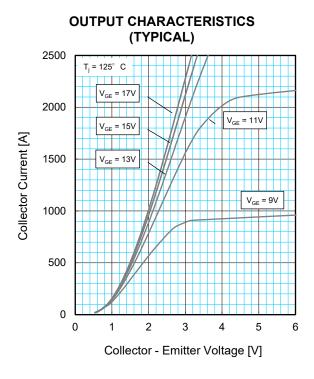
THERMAL CHARACTERISTICS

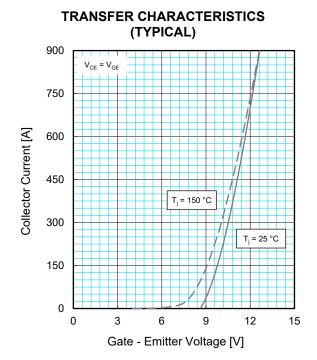
Item	Symbol	Conditions		Limits		
Tien Symbol		Conditions	Min.	Тур.	Max.	Unit
Thermal resistance junction to case, IGBT	$R_{th(j-c)Q}$	nction to Case, IGBT part, 1/2 module -		-	18.5	K/kW
Thermal resistance Junction to case, DIODE	$R_{th(j-c)D}$	unction to Case, FWDi part, 1/2 module		-	38.0	K/kW
Thermal resistance Junction to case, DIODE	R _{th(j-c)D}	unction to Case, Clamp-Di part, 1/2 module		-	38.0	K/kW
Contact thermal resistance case to heatsink	R _{th(c-s)}	Case to heat sink, 1/2 module λ_{grease} = 1 W/m·k, $D_{(c-s)}$ = 100 μm	-	16.0	-	K/kW

MECHANICAL CHARACTERISTICS

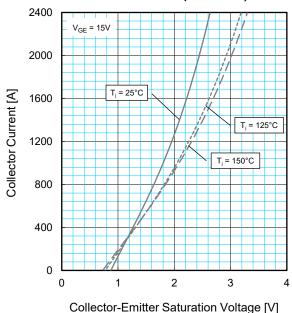
Itom	Cumbal	Conditions		Limits		
Item	Symbol			Тур.	Max.	Unit
	M _t	Main terminals screw: M8	7.0	-	20.0	N⋅m
Mounting torque		Mounting screw: M6	3.0	-	6.0	N⋅m
		Auxiliary terminals screw: M4	1.0	-	3.0	N·m N·m kg - mm mm nH nH mΩ
Mass	m	-	-	8.0	-	kg
Comparative tracking index	CTI	-	600	-	-	-
Clearance distance in air	d _a	Collector main terminal - Emitter main terminal Terminal - Baseplate		-	1	mm
Creepage distance along surface	d _s	Collector main terminal - Emitter main terminal		-	-	mm
Creepage distance along surface	d_s	Terminal - Baseplate	32.0	-	-	mm
Internal inductance (C-E)	L _{P(C-E)}	/2 module, IGBT part, T _C =25°C		22	-	nΗ
Internal inductance (A-K)	$L_{P(A-K)}$	1/2 module, Clamp-Di part, T _C =25°C	-	22		nΗ
Internal lead resistance, CC'-EE'	R _{CC+EE}	1/2 module, IGBT part, T _C =25°C	-	0.19	-	mΩ
Internal lead resistance, AA'-KK'		1/2 module, Clamp-Di part, T _C =25°C	-	0.19	•	mΩ
	$K' \mid R_{AA'+KK'}$	1/2 module, Clamp-Di part, T _C =125°C	-	0.23	•	mΩ
		1/2 module, Clamp-Di part, T _C =150°C	-	0.24	-	mΩ

- Note1. The symbols represent characteristics of the clamp diode (Clamp-Di).
- Note 2. Pulse width and repetition rate should be such that junction temperature (T_j) does not exceed T_{jopmax} rating.
- Note3. The symbols represent characteristics of the anti-parallel, emitter to collector free-wheel diode (FWD_i).
- Note4. Junction temperature (T_j) should not exceed T_{jmax} rating (150°C).
- Note5. Pulse width and repetition rate should be such as to cause negligible temperature rise.
- Note6. The integration range of switching energies ($E_{on(10\%)}$, $E_{rec(10\%)}$, $E_{off(10\%)}$) is from $10\%V_{CE}$ to $10\%I_{C}(10\%I_{E})$.
- Note7. The integration range of reverse recovery charge($Q_{rr(10\%)}$) is from $I_E = 0A$ to $10\%I_E$.

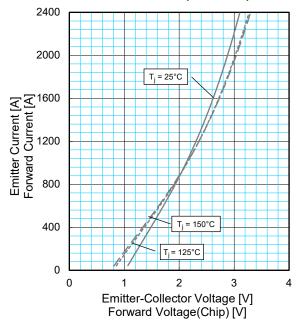




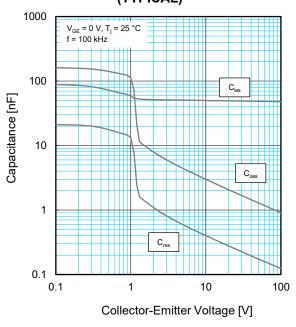
COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)



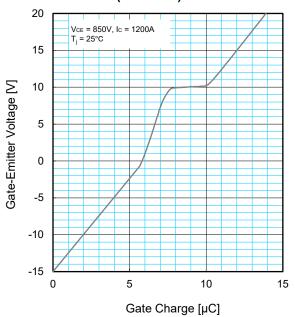
DIODE FORWARD CHARACTERISTICS (TYPICAL)



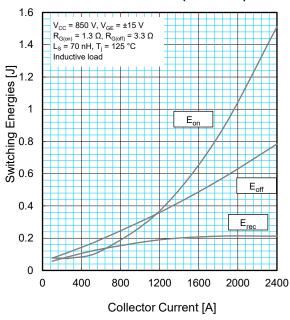
CAPACITANCE CHARACTERISTICS (TYPICAL)



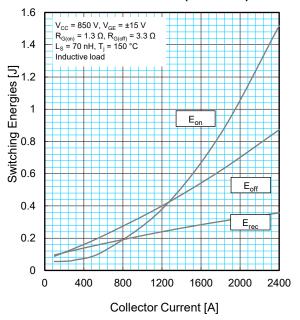
GATE CHARGE CHARACTERISTICS (TYPICAL)



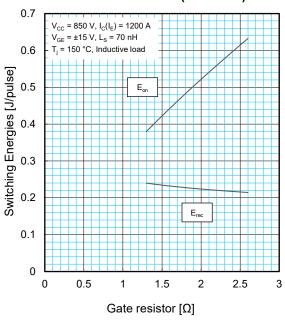
HALF-BRIDGE SWITCHING ENERGY CHARACTERISTICS (TYPICAL)



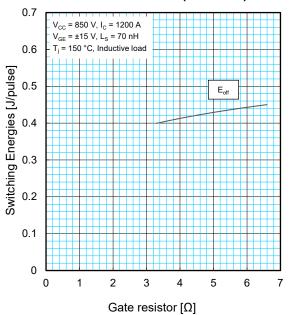
HALF-BRIDGE SWITCHING ENERGY CHARACTERISTICS (TYPICAL)



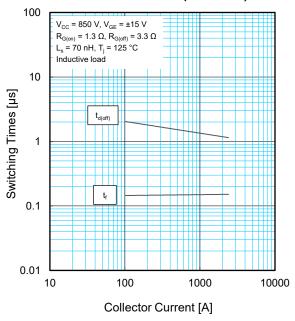
HALF-BRIDGE SWITCHING ENERGY CHARACTERISTICS (TYPICAL)



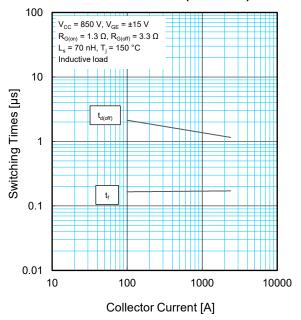
HALF-BRIDGE SWITCHING ENERGY CHARACTERISTICS (TYPICAL)



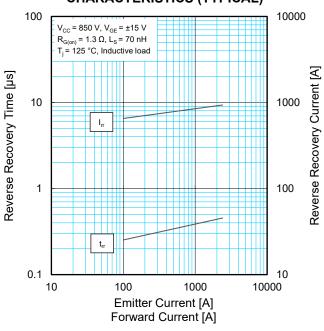
HALF-BRIDGE SWITCHING TIME CHARACTERISTICS (TYPICAL)



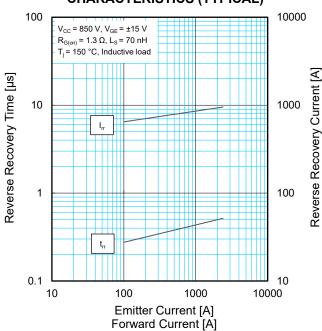
HALF-BRIDGE SWITCHING TIME CHARACTERISTICS (TYPICAL)



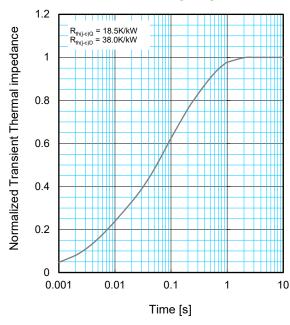
FREE-WHEEL DIODE REVERSE RECOVERY CHARACTERISTICS (TYPICAL)



FREE-WHEEL DIODE REVERSE RECOVERY CHARACTERISTICS (TYPICAL)



TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS

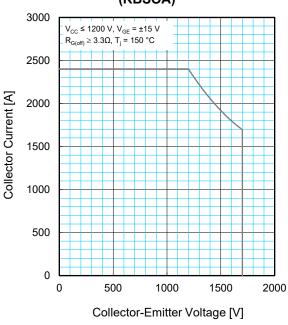


$$Z_{th(j-c)}(t) = \sum_{i=1}^{n} R_i \left\{ 1 - exp^{\left(-\frac{t}{\tau_i}\right)} \right\}$$

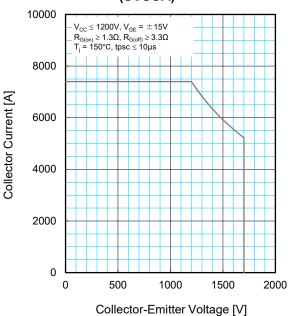
	1	1 2		4
R _i [K/kW] :	0.0096	0.1893	0.4044	0.3967
τ _i [sec.] :	0.0001	0.0058	0.0602	0.3512

INSULATED TYPE

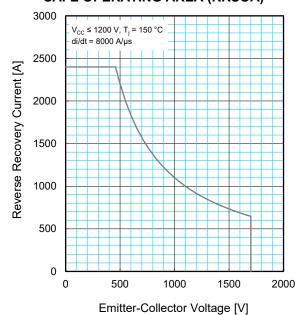
REVERSE BIAS SAFE OPERATING AREA (RBSOA)



SHORT CIRCUIT SAFE OPERATING AREA (SCSOA)



FREE-WHEEL DIODE REVERSE RECOVERY SAFE OPERATING AREA (RRSOA)



CM1200E4C-34S1
HIGH POWER SWITCHING USE INSULATED TYPE

5th-Version HVIGBT (High Voltage Insulated Gate Bipolar Transistor) Modules

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Dec. 2024 (HVM-1145-A)

5th-Version HVIGBT (High Voltage Insulated Gate Bipolar Transistor) Modules

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