

< High Voltage Insulated Gate Bipolar Transistor: HVIGBT >

CM1200E4C-66X

HIGH POWER SWITCHING USE INSULATED TYPE

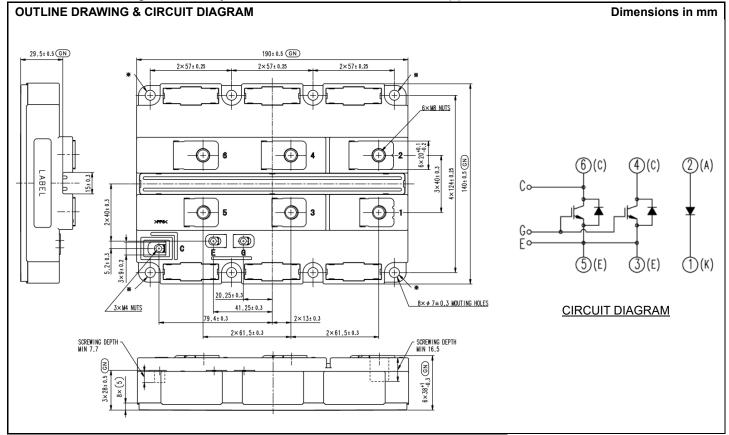
CM1200E4C-66X



- I_C......1200A
- V_{CES}......3300V
- 2-element in a Pack (for brake chopper)
- Insulated Type
- CSTBTTM(III) / RFC Diode
- Flat baseplate

APPLICATION

Traction drives, High Reliability Converters / Inverters, DC choppers



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MAXIMUM RATINGS

ltem	Symbol	Conditions	Ratings	Unit
Collector-emitter voltage	V	V _{GE} =0V, T _j =-40~ +150°C	3300	V
Collector-enfitter voltage	V _{CES}	V_{GE} =0V, T_j =-50°C	3200	V
Gate-emitter voltage	V_{GES}	V _{CE} =0V, T _j =25°C	±20	V
Denetitive neek reverse valtege	V	T _j =-40~+150°C	3300	V
Repetitive peak reverse voltage	V_{RRM}	T _j =-50°C	3200	v
Collector current	Ic	Tc=110°C, DC	1200	А
Collector current	I _{CRM}	Pulse (Note 1)	2400	А
Emitter current (FWDi forward	I _E	Tc=90°C, DC	1200	А
current)	I _{ERM}	Pulse (Note 1)	2400	А
Forward current	l _F	Tc=90°C, DC	1200	А
Forward current	I _{FRM}	Pulse (Note 1)	2400	А
Total power dissipation	P _{tot}	Tc=25°C, IGBT part (Note 2)	11900	W
Isolation voltage	V _{isol}	T _j =25°C, Charged part to the baseplate RMS sinusoidal, 60Hz 1min	6000	V _{rms}
Partial discharge	Q_{pd}	V1=3500V, V2=2600V, Charged part to the baseplate RMS sinusoidal, 60Hz	10	рС
Junction temperature	T _j	Maximum temperature range in off-state or on-state(non-switching)	-50 ~ 150	°C
Storage temperature	T _{stg}	Maximum case temperature range in onstate	-55 ~ 150	°C
Operating junction temperature	T _{jop}	Maximum junction temperature range for switching operation	-50 ~ 150	°C
Maximum turn-off switching current	$I_{C(off)}$	V_{GE} =±15V, V_{CC} ≤ 2500V, L_{S} ≤ 150nH T_{J} =Top	2400	А
(Short circuit) Gate pulse width	t _{pSC}	V_{GE} =±15V, $L_{S} \le 80$ nH, $V_{CC} \le 2500$ V T_{j} =Top	10	μs
Maximum reverse recovery instantaneous power (FWDi)		I_{C} =2400A, T_{j} =150°C, V_{CC} ≤ 2500V, L_{S} ≤ 150nH di_{on}/d_{t} ≤ 7333A/ μ s	3.0	
Maximum reverse recovery instantaneous power (Clamp-Di)	Prr	I_F =2400A, T_j =150°C, V_{CC} ≤ 2500V, L_S ≤ 150nH di_F/d_t ≤ 7333A/ μ s	3.0	MW
Surge (non-repetitive) forward current(Clamp-Di)	I _{FSM}	T _j = 150°C start, Half sine wave 50Hz 1cycle, Peak value	10.6	kA
Surge current load integral (Clamp-Di)	l ² t	T _j = 150°C start, Half sine wave 50Hz 1cycle	561	kA²⋅s

ELECTRICAL CHARACTERISTICS

ltom	Cumhal	Conditions		Lmits			Linit
Item	Symbol			Min.	Тур,	Max.	Unit
Collector-emitter cut-off current			T _i =25°C	-	-	4.0	
	I _{CES}	V _{CE} =3300V	T _i =125°C	-	4.0	-	mA
		V _{GE} =0V	T _i =150°C	-	24.0	48.0	
		V _R =3300V	T _j =25°C	-	-	2.0	
Repetitive reverse current	I _{RRM}		T _i =125°C	-	2.0	-	mA
(Clamp-Di)			T _i =150°C	-	12.0	24.0	
Gate-emitter threshold voltage	$V_{GE(th)}$	V _{CE} =10V, Ic=120mA	T _i =25°C	6.50	7.00	7.50	V
		V _{CE} =0V, V _{GE} =20V		0.0	-	0.5	
Gate-emitter leakage current	I_{GES}	V _{CE} =0V, V _{GE} =-20V	T _j =25°C	-0.5	-	0.0	μA
Gate charge	Q_{G}	V _{CC} =1800V, I _C =12000A V _{GE} =±15V	T _j =25°C	-	9.0	-	μC
Input capacitance	C _{ies}		T _j =25°C	-	139	-	nF
Output capacitance	C _{oes}	V _{CE} =10V, V _{GE} =0V, f=100kHz	T _i =25°C	-	9.3	-	nF
Reverse transfer capacitance	C _{res}		T _i =25°C	-	1.3	-	nF
		I _C =1200A	T _i =25°C	-	2.00	-	
Collector-emitter saturation	V_{CEsat}	V _{GE} =15V (Excluding package drop) (Note 3)	T _i =125°C	-	2.50	-	
voltage	CLSat		T _i =150°C		2.60	3.10	
			T₁=25°C	_	2.20	_	
Emitter-collector voltage	V_{EC}	I _E =1200A V _{GE} =0V (Excluding package drop) ^(Note 3)	T _i =125°C	_	2.40	_	V
			T _i =150°C	_	2.50	3.00	ľ
	.,	I _F =1200A (Excluding package drop) (Note 3)	T _i =150°C		2.20		
Forward valtage (Clamp Di)					2.40	-	V
Forward voltage(Clamp-Di)	V _{FM}		T _j =125°C	-			
Turne on delevitime			T _j =150°C	-	2.50	3.00	
Turn-on delay time Rise time	t _{d(on)}	-	T _j =150°C T _j =150°C	-	-	0.90	μs
Nise unie	L _r	V _{CC} =1800V, I _C =1200A	T _i =150 C		1.95	0.50	μs
Turn-on switching energy per	E _{on(10%)}	V_{GE} =±15V $R_{G(on)}$ =2.2 Ω , $R_{G(off)}$ =18 Ω	T _i =125°C		2.15	_	J
pulse(Note 4)	—on(10%)		T _i =150°C	-	2.25	-	
		L _S =150nH Inductive load	T _i =25°C	-	2.00	-	
Turn-on switching energy per pulse	Eon	inductive load	T _i =125°C	-	2.25	-	J
puise			T _j =150°C	-	2.35	-	
Turn-off delay time	$t_{d(off)}$		T _j =150°C	-	-	4.25	μs
Fall time	t _f		T _j =150°C	-	-	0.80	μs
Turn-off switching energy per	E _{off(10%)}	V_{CC} =1800V, I_{C} =1200A V_{GE} =±15V $R_{G(on)}$ =2.2 Ω , $R_{G(off)}$ =18 Ω L_{S} =150nH	T _j =25°C	-	1.55	-	J
pulse(Note 4)			T _j =125°C	-	2.00	-	
. , ,			T _j =150°C	-	2.05	-	
Turn-off switching energy per		Inductive load	T _j =25°C	-	1.65	-	J
pulse	Eoff		T _j =125°C	-	2.10	-	
			T _j =150°C	-	2.25	-	

ELECTRICAL CHARACTERISTICS (continuation)

ltem	Cuma had	Conditions	Conditions		Lmits		
	Symbol	Conditions		Min.	Тур,	Max.	Unit
Reverse recovery time (FWDi)	t _{rr}		T _j =150°C	-	1.15	-	μs
		1	T _i =25°C	-	-	-	A
Reverse recovery current	I _{rr}		T _i =125°C	_	1550	-	
(FWDi)	"		T _i =150°C	-	1650	-	
		1	T _i =25°C	_	-	_	
Reverse recovery current	Q _{rr(10%)}		T _i =125°C	_	1600	_	μC
(FWDi) ^(Note 5)	Sir(10%)		T _i =150°C	_	1650	_	
		-	T _i =25°C	_	-	_	
Reverse recovery current	Qrr		T _i =125°C	-	1750	-	μC
(FWDi)	<u> </u>	T _i =125 C T _i =150°C			1800	-	"
			T _i =25°C	-	1.15	_	
Reverse recovery energy per	E _{rec(10%)}		T _i =125°C	-	1.65	-	
pulse (FWDi) ^(Note 4)	-lec(1070)	T _i =15		-	1.85	-	
		1	T _i =25°C	_	1.25	_	+
Reverse recovery energy per	Erec	$\begin{array}{cccccccccccccccccccccccccccccccccccc$,	_	1.75	_	J
pulse (FWDi)	Lico			-	1.95	_	1
Reverse recovery time			,			_	
(Clamp-Di)	t _{rr}		T _j =150°C	-	1.15	-	μs
	I _{rr}	Inductive load	T _j =25°C	-	-	-	
Reverse recovery current (Clamp-Di)		T _j =125°C T _j =150°C T _j =25°C T _i =125°C		-	1550	-	A
(Clamp Bi)				-	1650	-	
				-	-	-	μC
Reverse recovery current (Clamp-Di) ^(Note 5)	Q _{rr(10%)}			-	1600	-	
(Clamp-DI)	, ,		T _i =150°C	-	1650	-	1
		T _i =25°C	T _i =25°C	-	-	-	
Reverse recovery current (Clamp-Di)	Qrr	T _i =125°C		-	1750	-	μC
(Clamp-Di)			T _i =150°C	-	1800	-	1
Reverse recovery energy per pulse (Clamp-Di) ^(Note 4)		1	T _j =25°C	-	1.15	-	
	E _{rec(10%)}	T _j =125		-	1.65	-	J
			T _i =150°C	-	1.85	-	1
		1	T _i =25°C	-	1.25	-	
Reverse recovery energy per	Erec		T _i =125°C	-	1.75	-	J
pulse (Clamp-Di)	2.00		T _i =150°C		1.95	_	1

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THERMAL CHARACTERISTICS

Item	Symbol	Conditions	Lmits			Unit
	Symbol	Conditions	Min.	Тур,	Max.	Unit
Thermal resistance	R _{th(j-c)Q}	Junction to case IGBT part	-	-	10.5	
	$R_{th(j-c)D}$	Junction to case FWDi part, FWDi part	-	-	16.5	
		Junction to case FWDi part, Clamp-Di part	ı	-	16.5	K/kW
Contact thermal resistance		Case to heat sink, Composite of IGBT part and FWDi part λ_{grease} =1W/m·K, D _(c-s) =80µm	-	7.5	-	
	R _{th(c-s)}	Case to heat sink, Clamp-Di part, λ_{grease} =1W/m • K $D_{(c-s)}$ =80 μ m	-	15.0	-	

MECHANICAL CHARACTERISTICS

ltem	C) made al	O Illi	Lmits			1.1
	Symbol	Conditions	Min.	Тур,	Max.	Unit
	M _t	Main terminal screw: M8	7.0	-	19.0	N∙m
Mounting torque	M _s	Mounting screw: M6	3.0	-	6.0	N∙m
	M _t	Auxiliary terminal screw: M4 (Note 6)	1.0	-	2.0	N∙m
Mass, Weight	m	-	-	1.2	-	kg
Comparative tracking index	СТІ	-	600	-	-	_
Clearance	d _a	Between C-E(G) Between Each terminal to Baseplate	19.5	-	-	mm
Creepage distance	d _s	Between C-E(G) Between Each terminal to Baseplate	32.0	-	-	mm
Parasitic stray inductance	L _{P(C-E)}	IGBT part	-	12.0	-	nH
Internal lead resistance (IGBT·FWDi part)	R _{CC'+EE'}	Tc=25°C	-	0.14	-	m0
Internal lead resistance (Clamp-Di part)	R _{AA'+KK'}	Tc=25°C	-	0.27	-	mΩ

Note1. Pulse width and repetition rate should be such that junction temperature (Tj) does not exceed Tjopmax rating.

Note2. Junction temperature (T_j) should not exceed T_{jmax} rating (150°C).

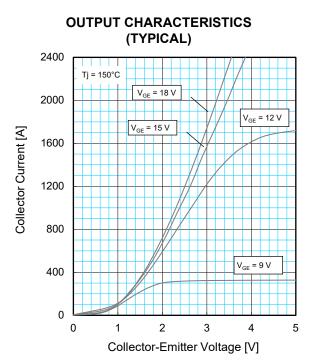
Note3. Pulse width and repetition rate should be such as to cause negligible temperature rise.

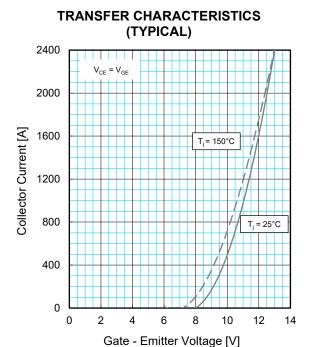
Note4. The integration range of switching energies is from $10\%V_{CE}$ to $10\%I_{C}(10\%I_{E})$.

Note5. The integration range of reverse recovery charge is from I_E = 0A to 10% I_E .

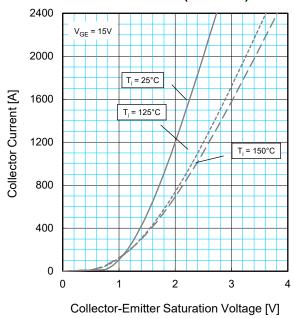
Note6. The maximum specified value is 3.0Nm under the condition of using PCB mounted on the power module.

PERFORMANCE CURVES

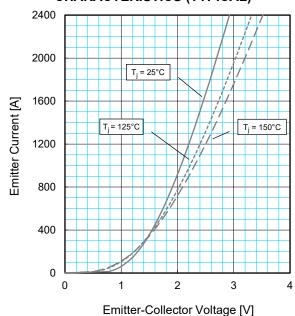




COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)

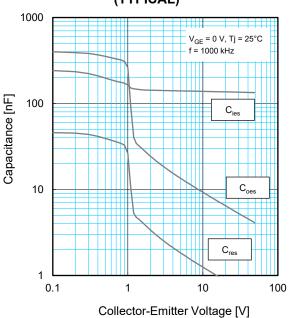


DIODE FORWARD CHARACTERISTICS (TYPICAL)

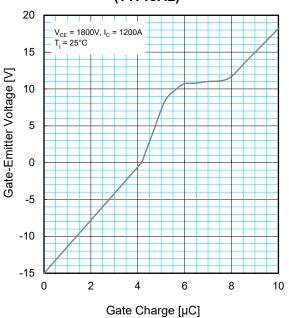


PERFORMANCE CURVES

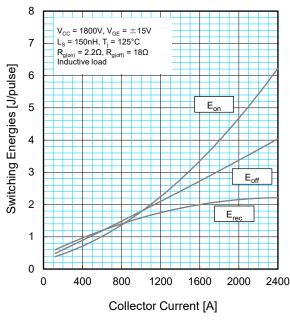
CAPACITANCE CHARACTERISTICS (TYPICAL)



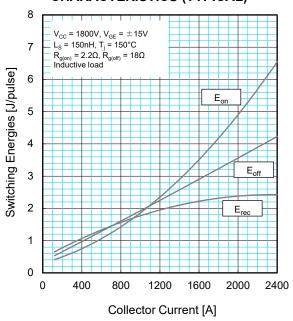
GATE CHARGE CHARACTERISTICS (TYPICAL)



HALF-BRIDGE SWITCHING ENERGY CHARACTERISTICS (TYPICAL)

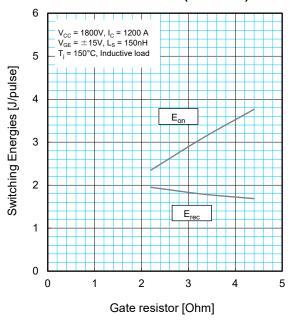


HALF-BRIDGE SWITCHING ENERGY CHARACTERISTICS (TYPICAL)

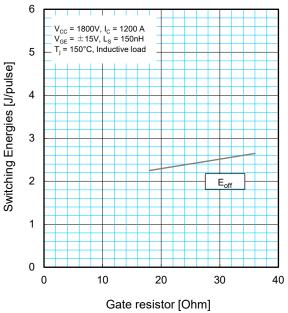


PERFORMANCE CURVES

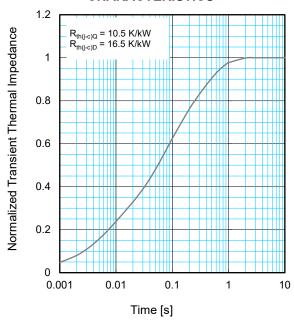
HALF-BRIDGE SWITCHING ENERGY CHARACTERISTICS (TYPICAL)



HALF-BRIDGE SWITCHING ENERGY CHARACTERISTICS (TYPICAL)



TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS

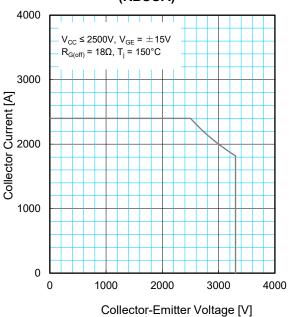


$$Z_{th(j-c)}(t) = \sum_{i=1}^{n} R_{i} \left\{ 1 - \exp^{\left(-\frac{t}{\tau_{i}}\right)} \right\}$$

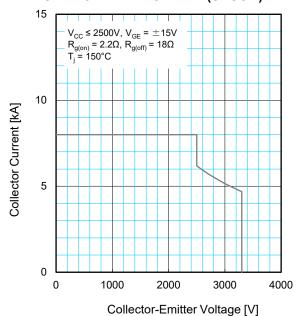
$$\frac{1}{R_{i} [K/kW]:} \begin{array}{c|cccc} 1 & 2 & 3 & 4 \\ 0.0096 & 0.1893 & 0.4044 & 0.3967 \\ \hline \tau_{i} [sec]: & 0.0001 & 0.0058 & 0.0602 & 0.3512 \end{array}$$

PERFORMANCE CURVES

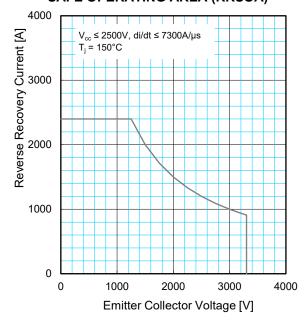
REVERSE BIAS SAFE OPERATING AREA (RBSOA)



SHORT CIRCUIT SAFE OPERATING AREA (SCSOA)



FREE-WHEEL DIODE REVERSE RECOVERY SAFE OPERATING AREA (RRSOA)



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