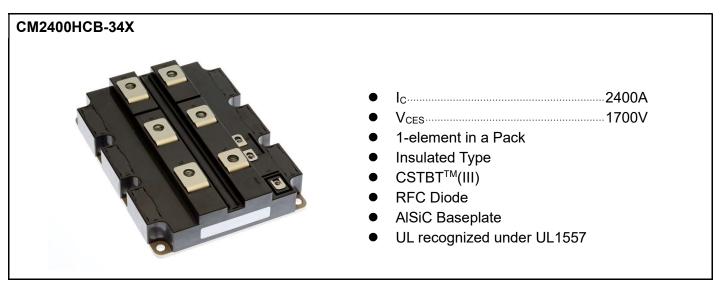


< High Voltage Insulated Gate Bipolar Transistor: HVIGBT >

## **CM2400HCB-34X**

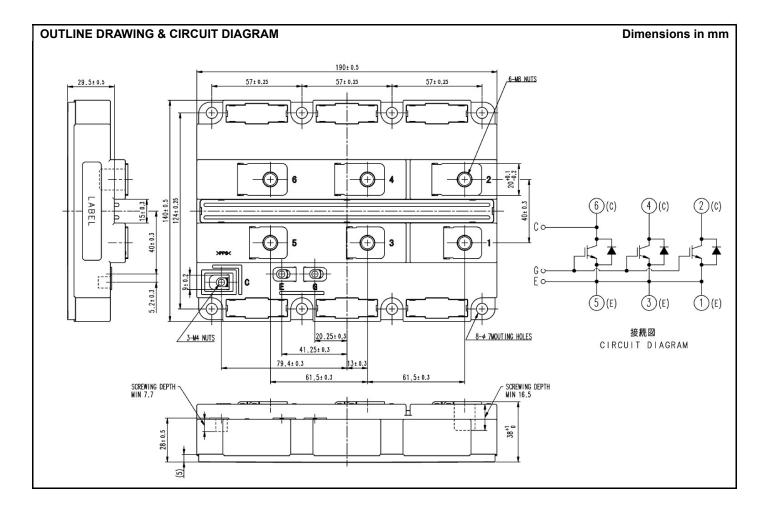
HIGH POWER SWITCHING USE INSULATED TYPE

6th-Version HVIGBT (High Voltage Insulated Gate Bipolar Transistor) Modules



#### **APPLICATION**

Traction drives, High Reliability Converters / Inverters, DC choppers



**HIGH POWER SWITCHING USE** 

INSULATED TYPE

6th-Version HVIGBT (High Voltage Insulated Gate Bipolar Transistor) Modules

#### **MAXIMUM RATINGS**

Symbol	Item	Conditions	Ratings	Unit
l ,,	Collector emitter voltage	V <sub>GE</sub> = 0V, T <sub>j</sub> = -40+150°C	1700	V
V <sub>CES</sub>	Collector-emitter voltage	$V_{GE} = 0V, T_j = -50^{\circ}C$	1650	V
$V_{GES}$	Gate-emitter voltage	V <sub>CE</sub> = 0V, T <sub>i</sub> = 25°C	±20	V
Ic	Calla stan assumant	DC, T <sub>c</sub> = 95°C	2400	Α
I <sub>CRM</sub>	Collector current	Pulse (Note 1)	4800	Α
IE	Emitter current (ALL C)	DC, T <sub>c</sub> = 75°C	2400	Α
I <sub>ERM</sub>	Emitter current (Note 2)	Pulse (Note 1)	4800	Α
P <sub>tot</sub>	Maximum power dissipation (Note 3)	T <sub>c</sub> = 25°C, IGBT part	13800	W
V <sub>iso</sub>	Isolation voltage	RMS, sinusoidal, f = 60Hz, t = 1min	6000	V
Ve	Partial discharge extinction voltage	RMS, sinusoidal, f = 60Hz, Q <sub>PD</sub> ≤ 10pC	2600	V
Tj	Junction temperature	_	<b>−</b> 50 ~ <b>+</b> 150	°C
T <sub>jop</sub>	Operating junction temperature	_	<b>−</b> 50 ~ <b>+</b> 150	ů
T <sub>stg</sub>	Storage temperature	_	<b>−</b> 55 ~ <b>+</b> 150	°C
t <sub>psc</sub>	Short circuit pulse width	$V_{CC} \le 1200V, V_{CE} \le V_{CES}, V_{GE} = 15V, T_j = 150^{\circ}C$	6.5	μs

#### **ELECTRICAL CHARACTERISTICS**

Symbol	Itom	Conditions		Limits			Unit
Symbol	Item			Min	Тур	Max	Unit
I <sub>CES</sub>			T <sub>j</sub> = 25°C		_	4.0	
	Collector cutoff current	$V_{CE} = V_{CES}, V_{GE} = 0V$	T <sub>i</sub> = 125°C	_	3.5	_	mA
			T <sub>j</sub> = 150°C	_	_	40.0	
$V_{GE(th)}$	Gate-emitter threshold voltage	$V_{CE} = 10V, I_{C} = 240mA, T_{j} = 25^{\circ}C$		5.5	6.0	6.5	V
I <sub>GES</sub>	Gate leakage current	$V_{GE} = V_{GES}$ , $V_{CE} = 0V$ , $T_j = 25$ °C		-0.5	_	0.5	μA
C <sub>ies</sub>	Input capacitance	\\ - 10\\ \\ - 0\\ f - 100\\ \\ -			817	_	nF
C <sub>oes</sub>	Output capacitance	$V_{CE} = 10V, V_{GE} = 0V, f = 100kHz$			17.8	_	
C <sub>res</sub>	Reverse transfer capacitance	T <sub>j</sub> = 25°C		_	7.2	_	
$Q_G$	Total gate charge	$V_{CC} = 900V$ , $I_C = 2400A$ , $V_{GE} = \pm 1$	5V		51.0		μC
		L = 2400A (v. )	T <sub>j</sub> = 25°C		1.60	_	
V <sub>CEsat</sub>	Collector-emitter saturation voltage	I <sub>C</sub> = 2400A (Note 4) V <sub>GE</sub> = 15V	T <sub>j</sub> = 125°C		1.85	_	V
		V <sub>GE</sub> - 15V	T <sub>j</sub> = 150°C	1	1.95	2.45	
t <sub>d(on)</sub>	Turn-on delay time		T <sub>j</sub> = 150°C		_	1.50	μs
t <sub>r</sub>	Rise time	V <sub>CC</sub> = 900V	T <sub>j</sub> = 150°C		_	0.50	μs
	Turn-on switching energy (per pulse) (Note 7)	$\begin{split} I_{C} &= 2400A \\ V_{GE} &= \pm 15V \\ R_{G(on)} &= 0.62\Omega \\ L_{S} &= 75nH \\ Inductive load \end{split}$	T <sub>j</sub> = 25°C		0.40	_	J
E <sub>on(10%)</sub>			T <sub>j</sub> = 125°C		0.70	_	
			T <sub>j</sub> = 150°C	_	0.75	_	
	Turn-on switching energy (per pulse) (Note 5)		T <sub>j</sub> = 25°C		0.50	_	
E <sub>on</sub>			T <sub>j</sub> = 125°C		0.75	_	
			T <sub>j</sub> = 150°C	_	0.80	_	
	Turn-off delay time		T <sub>j</sub> = 25°C	_	6.00	_	
$t_{d(off)}$			T <sub>j</sub> = 125°C		6.20	_	μs
			T <sub>j</sub> = 150°C		6.35	10.0	
	Fall time	V <sub>cc</sub> = 900V	T <sub>j</sub> = 25°C	_	0.30	_	
t <sub>f</sub>		I <sub>C</sub> = 2400A	T <sub>j</sub> = 125°C		0.32	_	μs
		V <sub>GE</sub> = ±15V	T <sub>j</sub> = 150°C		0.34	1.00	
E <sub>off(10%)</sub>	Turn-off switching energy (per pulse) (Note 7)	$R_{G(off)} = 5.6\Omega$	T <sub>j</sub> = 25°C	_	0.95	95 —	
		L <sub>S</sub> = 75nH Inductive load	T <sub>j</sub> = 125°C	_	1.10	_	J
	(per pulse) (Note 7)		T <sub>j</sub> = 150°C	_	1.20	_	
	Turn off quitabing anargy		T <sub>j</sub> = 25°C	_	1.00	_	
E <sub>off</sub>	Turn-off switching energy (per pulse) (Note 5)		T <sub>j</sub> = 125°C	_	1.15	_	J
			T <sub>j</sub> = 150°C	_	1.25	_	

HIGH POWER SWITCHING USE

**INSULATED TYPE** 

6th-Version HVIGBT (High Voltage Insulated Gate Bipolar Transistor) Modules

#### **ELECTRICAL CHARACTERISTICS**

Cumbal	Item		Conditions		Limits			Unit
Symbol					Min	Тур	Max	Offic
			1 - 24004 a	T <sub>j</sub> = 25°C	1	1.80	_	
V <sub>EC</sub>	Emitter-collector voltage	(Note 2)	I <sub>E</sub> = 2400A (Note 4)	T <sub>j</sub> = 125°C	_	1.95	_	V
			$V_{GE} = 0V$	T <sub>j</sub> = 150°C		1.95	2.45	
				T <sub>j</sub> = 25°C	_	0.40	_	
t <sub>rr</sub>	Reverse recovery time	(Note 2)		T <sub>j</sub> = 125°C	_	0.55	_	μs
				T <sub>j</sub> = 150°C	_	0.60	_	1
				T <sub>j</sub> = 25°C	_	1790	_	
I <sub>rr</sub>	Reverse recovery current	(Note 2)		T <sub>j</sub> = 125°C	_	1930	_	Α
				T <sub>j</sub> = 150°C	_	1980	_	
			V <sub>CC</sub> = 900V	T <sub>j</sub> = 25°C	_	430	_	
Q <sub>rr(10%)</sub>	Reverse recovery charge	(Note 2,6)	I <sub>E</sub> = 2400A	T <sub>i</sub> = 125°C	_	720	_	μC
			V <sub>GE</sub> = ±15V	T <sub>j</sub> = 150°C	_	820	_	
			$R_{G(on)} = 0.62\Omega$	T <sub>j</sub> = 25°C	_	480	_	
Q <sub>rr</sub>	Reverse recovery charge	(Note 2,5)	L <sub>s</sub> = 75nH	T <sub>i</sub> = 125°C	_	785	_	μC
			Inductive load	T <sub>j</sub> = 150°C	_	890	_	
	Poverse receivery energy			T <sub>j</sub> = 25°C	_	0.22	_	
E <sub>rec(10%)</sub>	Reverse recovery energy (per pulse)	(Note 2,7)		T <sub>i</sub> = 125°C	_	0.40	_	J
	(hei haise)	(NOIE 2,7)		T <sub>i</sub> = 150°C	_	0.46	_	
	Poverse receivery energy		T <sub>j</sub> = 25°C	_	0.25	_		
E <sub>rec</sub>	Reverse recovery energy (per pulse)	(Note 2,5)		T <sub>i</sub> = 125°C		0.45	_	J
	(per puise)	(11018 2,5)		T <sub>i</sub> = 150°C	_	0.55	_	

#### THERMAL CHARACTERISTICS

Symbol	Item	Conditions	Limits			Unit
		Conditions		Тур	Max	Offic
R <sub>th(j-c)Q</sub>	Thermal resistance	Junction to Case, IGBT part	_	_	9.0	K/kW
$R_{th(j-c)D}$	Thermal resistance	Junction to Case, FWDi part	_	_	12.5	K/kW
R <sub>th(c-s)</sub>	Contact thermal resistance	Case to heat sink $\lambda_{grease} = 1W/m^*K$ , $D_{(c-s)} = 80\mu m$	_	5.7	_	K/kW

#### **MECHANICAL CHARACTERISTICS**

Symbol	Item	O and distance	Limits			I Incid
		Conditions		Тур	Max	Unit
M <sub>t</sub>		M8 : Main terminals screw	7.0	_	19.0	N⋅m
Ms	Mounting torque	M6 : Mounting screw	3.0		6.0	N⋅m
Mt		M4 : Auxiliary terminals screw (Note 8)	1.0	_	3.0	N⋅m
m	Mass		_	1.2		kg
CTI	Comparative tracking index		600	_		_
da	Clearance		19.5	_	_	mm
d <sub>s</sub>	Creepage distance		32.0	_		mm
L <sub>P CE</sub>	Parasitic stray inductance		_	8.0		nΗ
R <sub>CC'+EE'</sub>	Internal lead resistance	T <sub>C</sub> = 25°C	_	0.09		mΩ

Note1. Pulse width and repetition rate should be such that junction temperature (T<sub>j</sub>) does not exceed T<sub>jopmax</sub> rating.

 $Note 2. \hspace{0.5cm} \textbf{The symbols represent characteristics of the anti-parallel, emitter to collector free-wheel diode (FWD_i)}. \\$ 

Note3. Junction temperature (T<sub>j</sub>) should not exceed T<sub>jmax</sub> rating (150°C).

Note4. Pulse width and repetition rate should be such as to cause negligible temperature rise.

Note5. Definition of all items is according to IEC 60747, unless otherwise specified.

Note6. The integration range of reverse recovery charge is from  $I_E$  = 0A to 10% $I_E$ .

Note 7. The integration range of switching energies is from  $10\%V_{CE}$  to  $10\%I_{C}(10\%I_{E})$ .

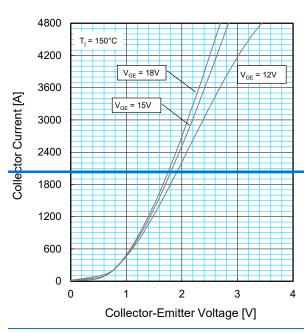
Note8. The maximum specified value is under the condition of using PCB mounted on the power module. In case no PCB is used this maximum torque

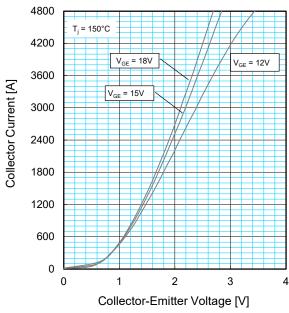
for M4 screw is 2.0 Nm.

6th-Version HVIGBT (High Voltage Insulated Gate Bipolar Transistor) Modules

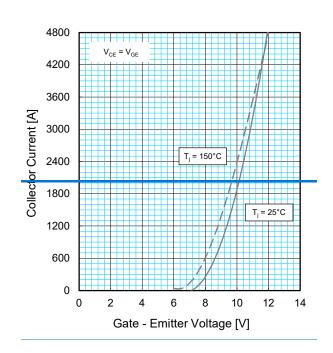
#### **PERFORMANCE CURVES**

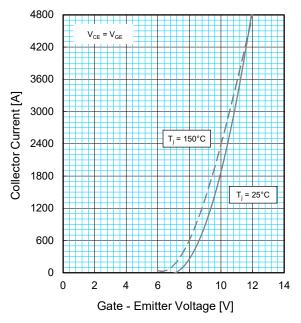
# OUTPUT CHARACTERISTICS (TYPICAL)





# TRANSFER CHARACTERISTICS (TYPICAL)



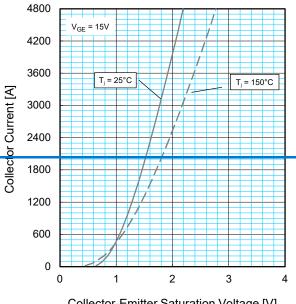


HIGH POWER SWITCHING USE

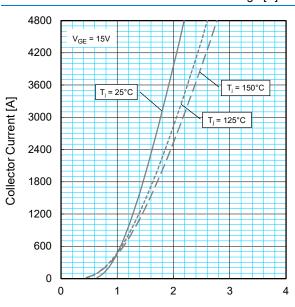
**INSULATED TYPE** 6th-Version HVIGBT (High Voltage Insulated Gate Bipolar Transistor) Modules



#### FREE-WHEEL DIODE FORWARD **CHARACTERISTICS (TYPICAL)**

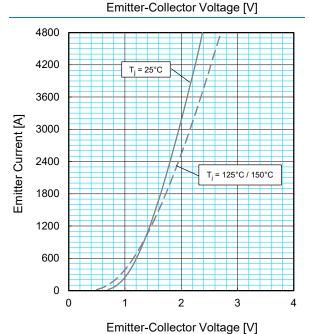


Collector-Emitter Saturation Voltage [V]



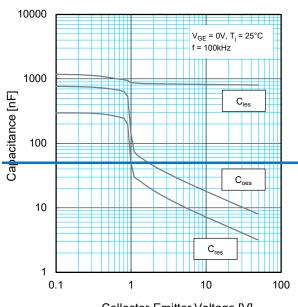
Collector-Emitter Saturation Voltage [V]

4800 4200 T<sub>i</sub> = 25°C 3600 Emitter Current [A] 3000 2400 1800 1200 600 0

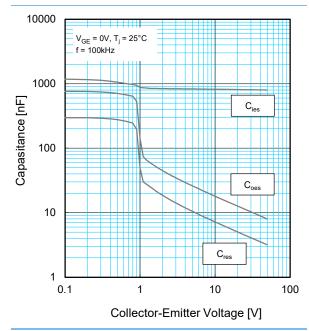


#### **PERFORMANCE CURVES**

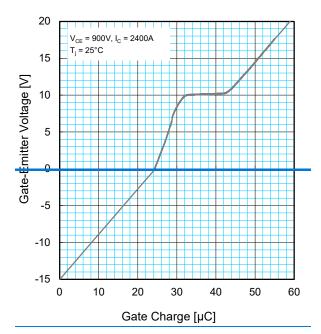
# CAPACITANCE CHARACTERISTICS (TYPICAL)

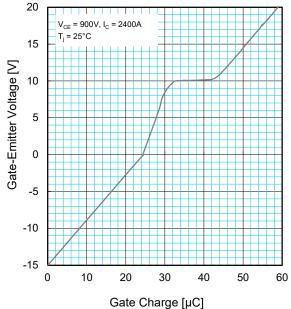


Collector-Emitter Voltage [V]



# GATE CHARGE CHARACTERISTICS (TYPICAL)

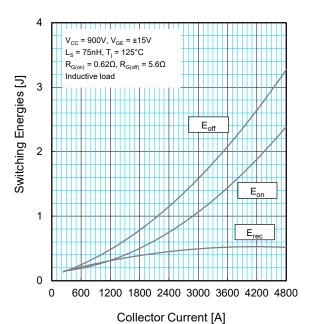


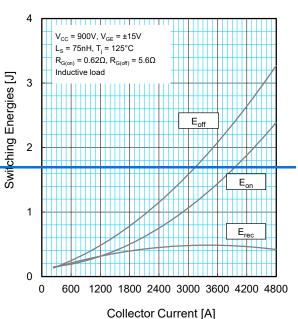


#### **HIGH POWER SWITCHING USE**

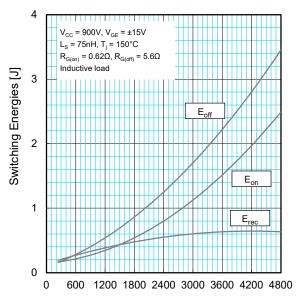
INSULATED TYPE 6th-Version HVIGBT (High Voltage Insulated Gate Bipolar Transistor) Modules

## HALF-BRIDGE SWITCHING ENERGY CHARACTERISTICS (TYPICAL)

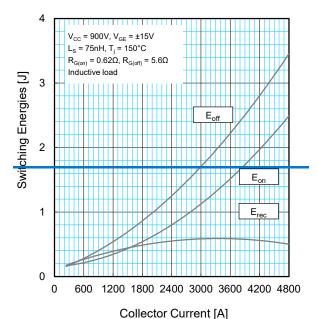




## HALF-BRIDGE SWITCHING ENERGY CHARACTERISTICS (TYPICAL)



Collector Current [A]



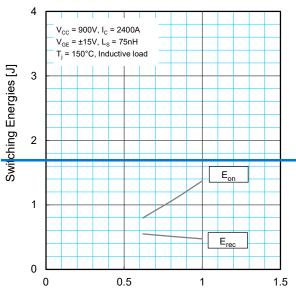
HIGH POWER SWITCHING USE

**INSULATED TYPE** 

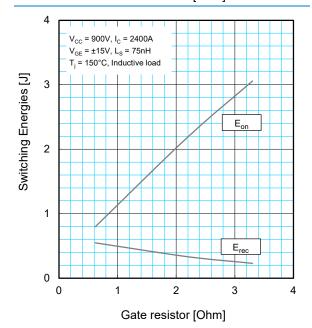
6th-Version HVIGBT (High Voltage Insulated Gate Bipolar Transistor) Modules

#### **PERFORMANCE CURVES**

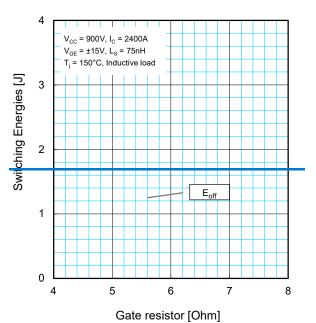
## HALF-BRIDGE SWITCHING ENERGY CHARACTERISTICS (TYPICAL)



Gate resistor [Ohm]



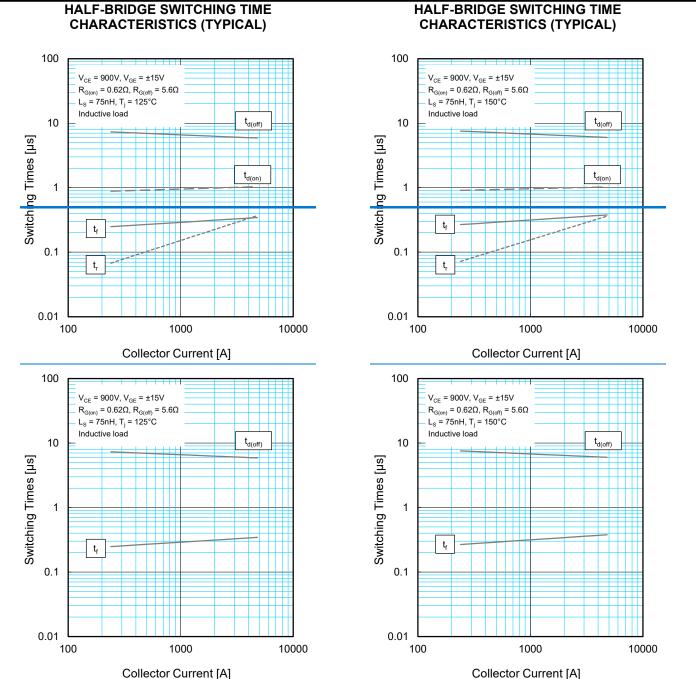
## HALF-BRIDGE SWITCHING ENERGY CHARACTERISTICS (TYPICAL)



6 V<sub>CC</sub> = 900V, I<sub>C</sub> = 2400A  $V_{GE} = \pm 15V, L_{S} = 75nH$ 5 T<sub>i</sub> = 150°C, Inductive load Switching Energies [J] 4 3 2 1 0 0 5 10 25 20 Gate resistor [Ohm]

**HIGH POWER SWITCHING USE** 



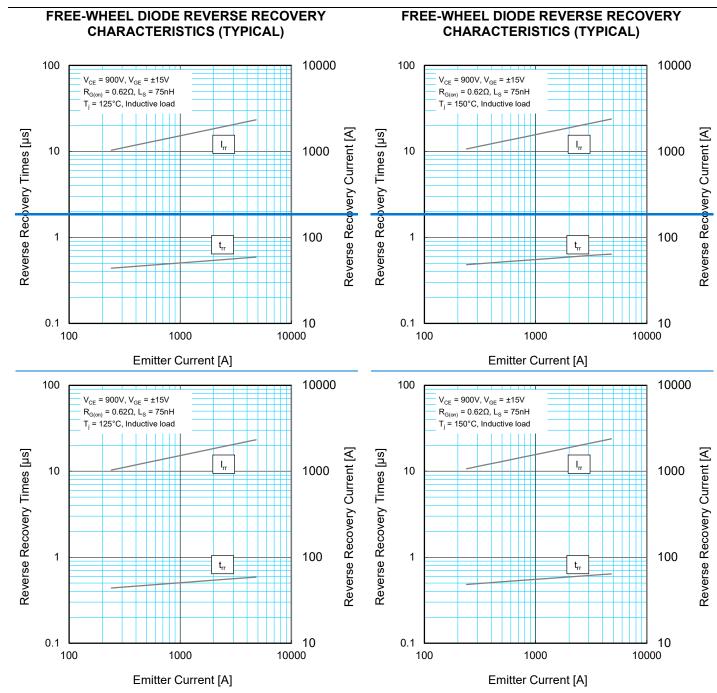


HIGH POWER SWITCHING USE

**INSULATED TYPE** 

6th-Version HVIGBT (High Voltage Insulated Gate Bipolar Transistor) Modules

#### **PERFORMANCE CURVES**

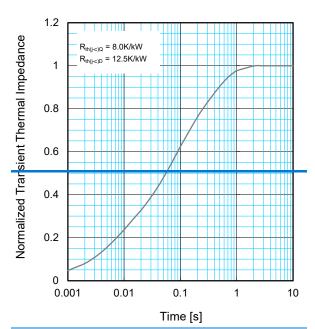


HIGH POWER SWITCHING USE

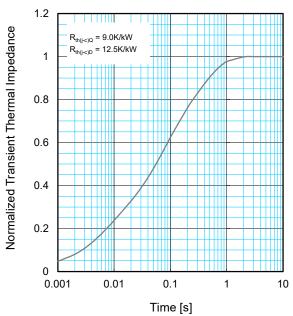
INSULATED TYPE

6th-Version HVIGBT (High Voltage Insulated Gate Bipolar Transistor) Modules

## TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS



$$Z_{th(j-c)}(t) = \sum_{i=1}^{n} R_{i} \left\{ 1 - \exp\left(-\frac{t}{\tau_{i}}\right) \right\}$$



	1	2	3	4
R <sub>i</sub> / R <sub>th(j-c)</sub> :	0.0096	0.1893	0.4044	0.3967
τ <sub>i</sub> [sec] :	0.0001	0.0058	0.0602	0.3512

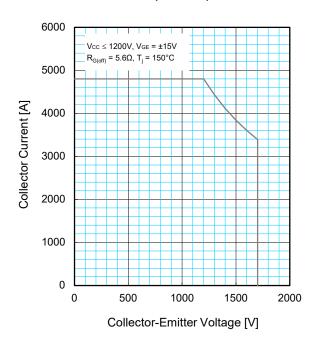
**HIGH POWER SWITCHING USE** 

**INSULATED TYPE** 

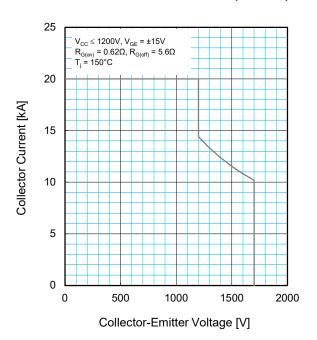
6th-Version HVIGBT (High Voltage Insulated Gate Bipolar Transistor) Modules

**PERFORMANCE CURVES** 

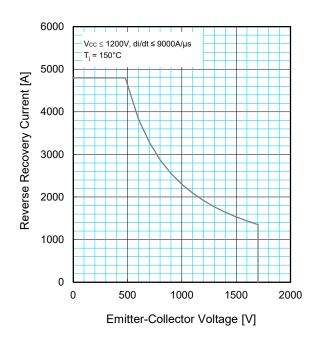
## REVERSE BIAS SAFE OPERATING AREA (RBSOA)



## SHORT CIRCUIT SAFE OPERATING AREA (SCSOA)



## FREE-WHEEL DIODE REVERSE RECOVERY SAFE OPERATING AREA (RRSOA)



< High Voltage Insulated Gate Bipolar Transistor : HVIGBT >

CM2400HCB-34X
HIGH POWER SWITCHING USE
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6th-Version HVIGBT (High Voltage Insulated Gate Bipolar Transistor) Modules

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Dec. 2022 (HVM-1106-B)

6th-Version HVIGBT (High Voltage Insulated Gate Bipolar Transistor) Modules

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