

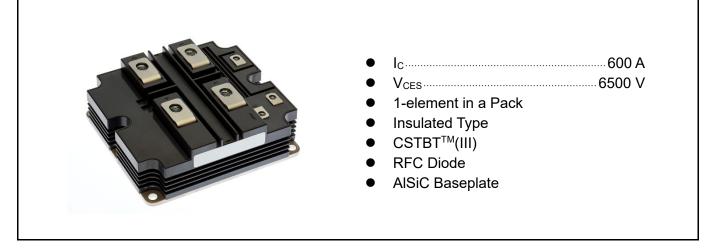
< High Voltage Insulated Gate Bipolar Transistor: HVIGBT >

CM600HG-130X

HIGH POWER SWITCHING USE INSULATED TYPE

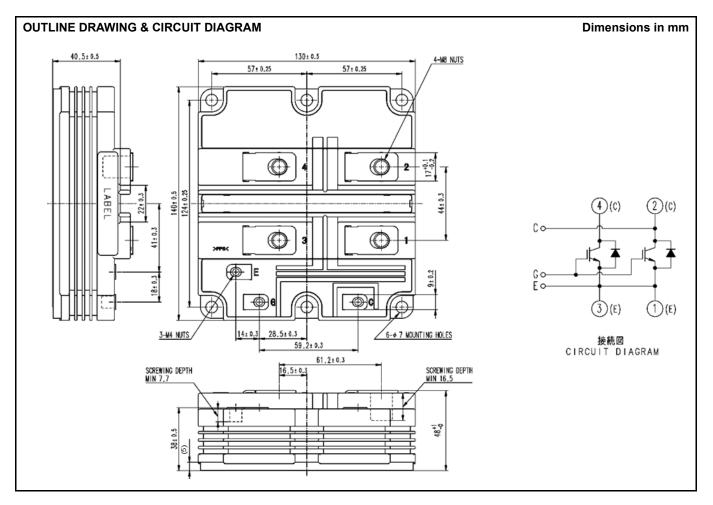
5th-Version HVIGBT (High Voltage Insulated Gate Bipolar Transistor) Modules

CM600HG-130X



APPLICATION

Traction drives, High Reliability Converters / Inverters, DC choppers



< High Voltage Insulated Gate Bipolar Transistor: HVIGBT MODULE > CM600HG-130X HIGH POWER SWITCHING USE INSULATED TYPE 5th-Version HVIGBT (High Vol

5th-Version HVIGBT (High Voltage Insulated Gate Bipolar Transistor)

MAXIMUM RATINGS (Tj=25°C, unless otherwise specified)

Symbol	Item	Conditions	Ratings	Unit
V _{CES}	Collector-emitter voltage	V _{GE} = 0V, T _j = +150°C	6500	
		$V_{GE} = 0V, T_j = +25^{\circ}C$	6300	V
		$V_{GE} = 0V, T_j = -50^{\circ}C$	5700	
V _{GES}	Gate-emitter voltage	$V_{CE} = 0V, T_j = 25^{\circ}C$	±20	V
lc	Callester surrent	DC, T _C = 110°C	600	Α
ICRM	Collector current	Pulse (Note 1)	1200	Α
IE	Empitter august (Note 2)	DC, T _C = 95°C	600	Α
IERM	Emitter current (Note 2)	Pulse (Note 1)	1200	Α
Ptot	Maximum power dissipation (Note 3)	T _c = 25°C, IGBT part	8300	W
Viso	Isolation voltage	RMS, sinusoidal, f = 60Hz, t = 1min.	10200	V
Q _{pd}	Partial discharge	V1 = 6900Vrms, V2 = 5100Vrms 60Hz	10	рС
Tj	Junction temperature	—	-50 ~ +150	°C
Tjop	Operating junction temperature	—	-50 ~ +150	°C
T _{stg}	Storage temperature	—	-55 ~ +150	°C
t _{psc}	Short circuit pulse width	$V_{CC} = 4500V, V_{CE} \le V_{CES}, V_{GE} = \pm 15V$ $T_j = 150^{\circ}C$	10	μs

ELECTRICAL CHARACTERISTICS (Tj=25°C, unless otherwise specified)

Symbol	Item	Conditions			Limits		Unit
Symbol	Item	Conditio	115	Min.	Тур.	Max.	Unit
Ices	Collector cutoff current	$\lambda = \lambda $	T _j = 25°C	—	—	4.0	
		V _{CE} = V _{CES} V _{GE} = 0V	T _j = 125°C	—	3.5	—	mA
		VGE – UV	T _j = 150°C	—	—	100	
V _{GE(th)}	Gate-emitter threshold voltage	V _{CE} = 10V, I _C = 60mA	λ, Τ _j = 25°C	6.5	7.0	7.5	V
I _{GES}	Gate leakage current	$V_{GE} = V_{GES}, V_{CE} = 0V_{SE}$, T _j = 25°C	-0.5	_	0.5	μA
Cies	Input capacitance		f - 100kH-	—	101		
Coes	Output capacitance	$T_i = 25^{\circ}C$			4.1	—	nF
Cres	Reverse transfer capacitance	1] = 20 0		—	0.5	—	
Q_{G}	Total gate charge	$V_{CC} = 3600V, I_C = 600$ $V_{GE} = \pm 15V, T_j = 25^{\circ}C$		—	6.6	—	μC
		Ic = 600A (Note 4)	Tj = 25°C	—	2.50		
VCEsat	Collector-emitter saturation voltage	$V_{GE} = 15V$	T _j = 125°C	—	3.20	—	V
		VGE - TJV	T _j = 150°C	—	3.30	3.80	
t _{d(on)}	Turn-on delay time		T _j = 150°C	—	—	1.45	μs
tr	Rise time	$V_{CC} = 3600V \\ I_{C} = 600A \\ V_{GE} = \pm 15V \\ R_{G(on)} = 6.2\Omega$	T _j = 150°C	—		0.50	μs
	Turn-on switching energy ^(Note 7) (per pulse)		T _j = 25°C	—	4.15	—	
Eon(10%)			T _j = 125°C	—	4.50	—	J
		$L_s = 225 nH$	T _j = 150°C	—	5.10	—	
	Turn-on switching energy (Note 5)		T _j = 25°C	—	4.40	4.40 —	
Eon	(per pulse)	Inductive load	T _j = 125°C	—	4.85	—	J
			T _j = 150°C	—	5.50	—	
			T _j = 25°C	—	5.90	—	
t _{d(off)}	Turn-off delay time		T _j = 125°C	—	7.00	—	μs
			T _j = 150°C	—	7.00	10.5	
		$V_{CC} = 3600V$	T _j = 25°C	—	0.40	—	
t _f	Fall time	$I_{\rm C} = 600 \text{A}$	T _j = 125°C	—	0.80		μs
		$V_{GE} = \pm 15V$ $R_{G(off)} = 45\Omega$	T _j = 150°C	—	0.80	—	
	Turn-off switching energy ^(Note 7) (per pulse)	$L_{s} = 225 nH$	T _j = 25°C	—	2.40	—	
E _{off(10%)}			T _j = 125°C	—	3.85	—	J
		Inductive load	T _j = 150°C	—	4.00		
	Turn off switching energy (Note 5)		T _j = 25°C	—	2.55	—	
Eoff	Turn-off switching energy ^(Note 5) (per pulse)	-	T _j = 125°C	—	4.10		J
			T _j = 150°C	—	4.25		

< High Voltage Insulated Gate Bipolar Transistor: HVIGBT MODULE > CM600HG-130X **HIGH POWER SWITCHING USE INSULATED TYPE**

5th-Version HVIGBT (High Voltage Insulated Gate Bipolar Transistor)

ELECTRICAL CHARACTERISTICS

Symbol	Item	Conditions		Limits			Unit
Symbol			15	Min.	Тур.	Max.	Unit
		I = COOA (Note 4)	T _j = 25°C	_	2.50		
VEC	Emitter-collector voltage (Note 2)	I _E = 600A ^(Note 4) V _{GE} = 0V	T _j = 125°C	_	3.20		V
		VGE - UV	T _j = 150°C		3.30	3.80	
			T _j = 25°C		1.70		
trr	Reverse recovery time (Note 2)		T _j = 125°C		2.05		μs
			T _j = 150°C		2.15	—	
	Reverse recovery current (Note 2)		T _j = 25°C		950		A
Irr		$V_{CC} = 3600V$ $I_E = 600A$ $V_{GE} = \pm 15V$ $R_{G(on)} = 6.2\Omega$ $L_S = 225nH$	T _j = 125°C		900		
			T _j = 150°C		900		
	Reverse recovery charge (Note 2,6)		T _j = 25°C		1200	—	μC
Qrr(10%)			T _j = 125°C		1550	—	
			T _j = 150°C		1600		
			T _j = 25°C		1200		
Qrr	Reverse recovery charge (Note 2,5)	23 220111	T _j = 125°C		1550	—	μC
		Inductive load	T _j = 150°C		1650		
	Reverse recovery energy (Note 2,7) (per pulse)		T _j = 25°C		2.35		
Erec(10%)			T _j = 125°C		3.40		J
			T _j = 150°C		3.65		
	Reverse recovery energy ^(Note 2,5) (per pulse)		T _j = 25°C		2.50		
Erec			T _j = 125°C		3.60		J
			T _j = 150°C	—	4.00		

THERMAL CHARACTERISTICS

Symbol	Itom	Conditions	Limits			Unit
Symbol Item		Conditions	Min.	Тур.	Max.	Unit
Rth(j-c)Q	Thermal resistance	Junction to Case, IGBT part		_	15.0	K/kW
Rth(j-c)D	Thermal resistance	Junction to Case, FWDi part		_	24.0	K/kW
R _{th(c-s)}	Contact thermal resistance	Case to heat sink λ _{grease} = 1W/m·K, D _(c-s) = 80μm		7.5	_	K/kW

MECHANICAL CHARACTERISTICS

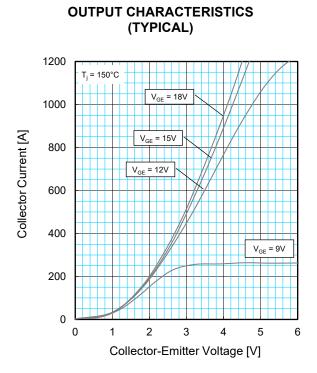
Sumbol	Item	Conditions	Limits			Unit
Symbol		Conditions	Min.	Тур.	Max.	Unit
Mt		Main terminals screw: M8	7.0	_	19.0	N∙m
Ms	Mounting torque	Mounting screw: M6	3.0	_	6.0	N∙m
Mt	Auxiliary terminals screw: M4		1.0	_	3.0	N∙m
m	Mass	—		1.0	_	kg
CTI	Comparative tracking index	—	600			
da	Clearance	—	26.0	_		mm
ds	Creepage distance	—	56.0	_		mm
L _{P(C-E)}	Parasitic stray inductance	—		20.5		nH
Rcc'+ee'	Internal lead resistance	$T_{\rm C} = 25^{\circ}{\rm C}$		0.18	_	mΩ

Note1. Pulse width and repetition rate should be such that junction temperature (T_i) does not exceed T_{iopmax} rating.

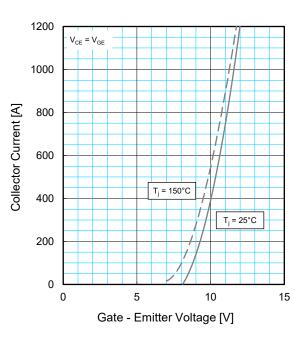
Note2. The symbols represent characteristics of the anti-parallel, emitter to collector free-wheel diode (FWD_i).

Note4. Pulse width and repetition rate should be such as to cause negligible temperature rise. Note5. Definition of all items is according to IEC 60747, unless otherwise specified. Note6. The integration range of reverse recovery charge is from I_E = 0A to 10%I_E. Note7. The integration range of switching energies is from 10%V_{CE} to 10%I_C(I_E).

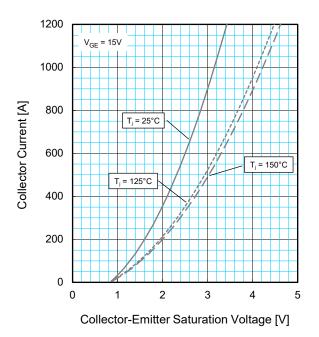
Note3. Junction temperature (T_j) should not exceed T_{jmax} rating (150°C).



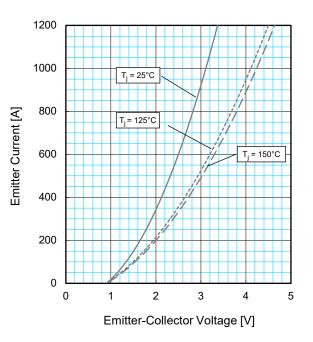
TRANSFER CHARACTERISTICS (TYPICAL)

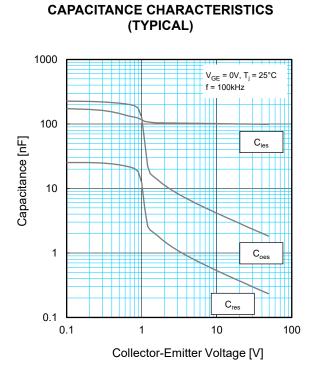


COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)

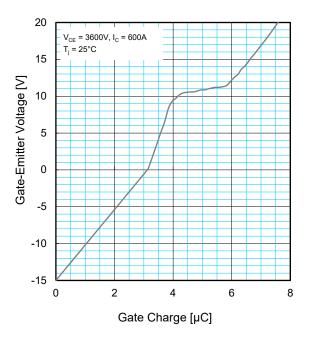


FREE-WHEEL DIODE FORWARD CHARACTERISTICS (TYPICAL)

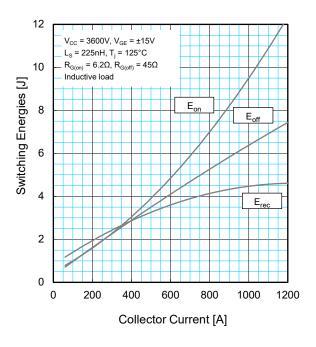




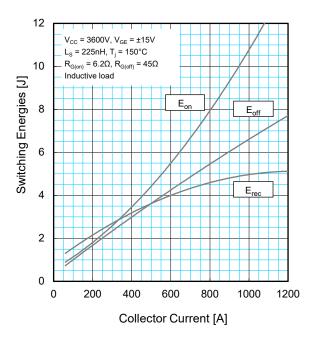
GATE CHARGE CHARACTERISTICS (TYPICAL)

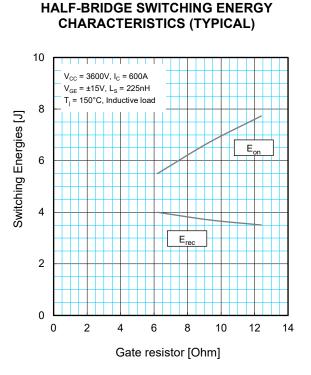


HALF-BRIDGE SWITCHING ENERGY CHARACTERISTICS (TYPICAL)

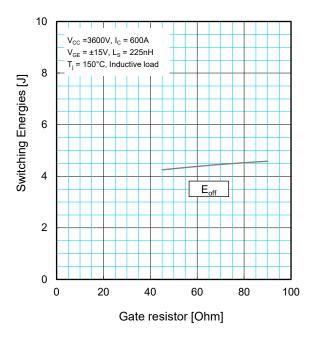


HALF-BRIDGE SWITCHING ENERGY CHARACTERISTICS (TYPICAL)

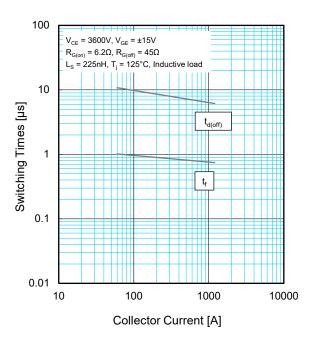




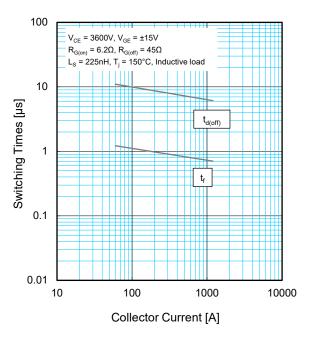
HALF-BRIDGE SWITCHING ENERGY CHARACTERISTICS (TYPICAL)

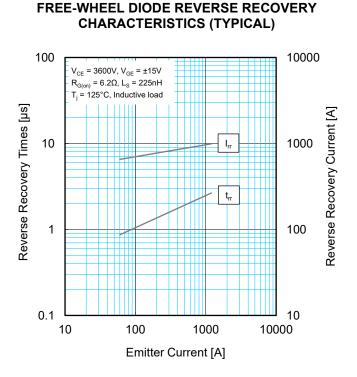


HALF-BRIDGE SWITCHING TIME CHARACTERISTICS (TYPICAL)

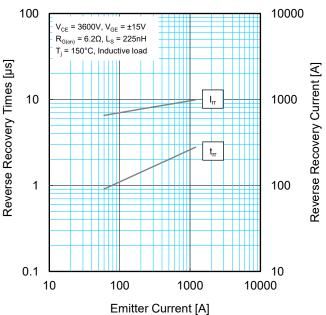


HALF-BRIDGE SWITCHING TIME CHARACTERISTICS (TYPICAL)

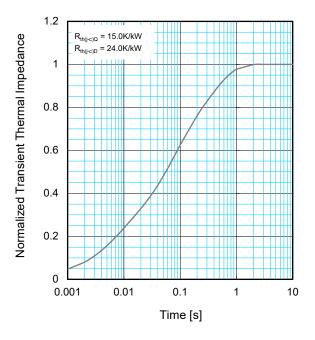




FREE-WHEEL DIODE REVERSE RECOVERY CHARACTERISTICS (TYPICAL)

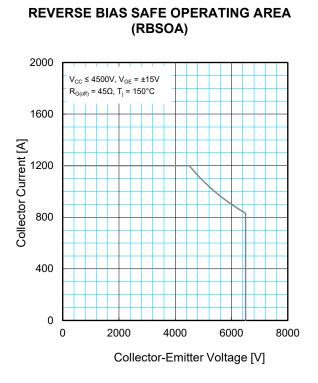


TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS

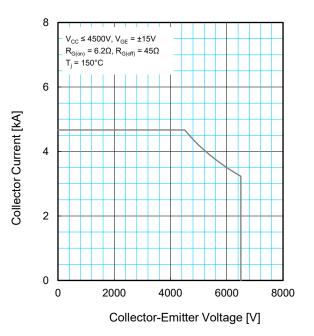


$Z_{th(j-c)}(t) = \sum_{i=1}^{n} I$	$R_i\left\{1-exp^{\left(-\frac{t}{\tau_i}\right)}\right\}$
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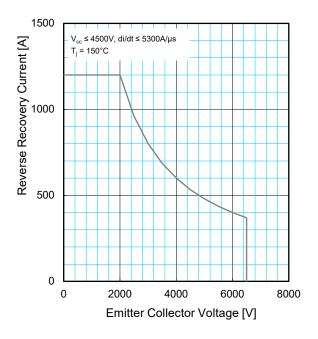
	1	2	3	4
R _i / R _{th(j-c)}	0.0096	0.1893	0.4044	0.3967
τ _i [s]	0.0001	0.0058	0.0602	0.3512



SHORT CIRCUIT SAFE OPERATING AREA (SCSOA)



FREE-WHEEL DIODE REVERSE RECOVERY SAFE OPERATING AREA (RRSOA)



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5th-Version HVIGBT (High Voltage Insulated Gate Bipolar Transistor)

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