

<IGBT Modules>

CM300DX-34T/CM300DXP-34T

HIGH POWER SWITCHING USE INSULATED TYPE

		Collector current I_{C}
	14	Collector-emitter voltage V_{CES} 1 7 0 0 V
		Maximum junction temperature T _{vjmax} 175 °C
DX		●Flat base type
		 Copper base plate (Nickel-plating)
		 RoHS Directive compliant
		 Tin-plating pin terminals
		Collector current Ic 300 A
		Collector-emitter voltage V_{CES} 1 7 0 0 V
		Maximum junction temperature T_{vjmax} 175 °C
DXP		●Flat base type
		 Copper base plate (Nickel-plating)
	a and the	 RoHS Directive compliant
		 Tin-plating pressfit terminals
	dual switch (half-bridge)	 UL Recognized under UL1557, File No. E323585

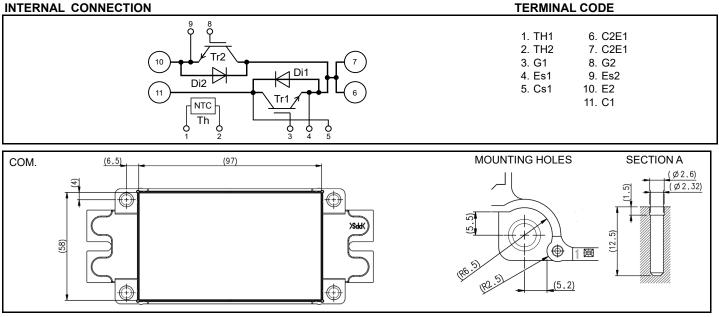
APPLICATION

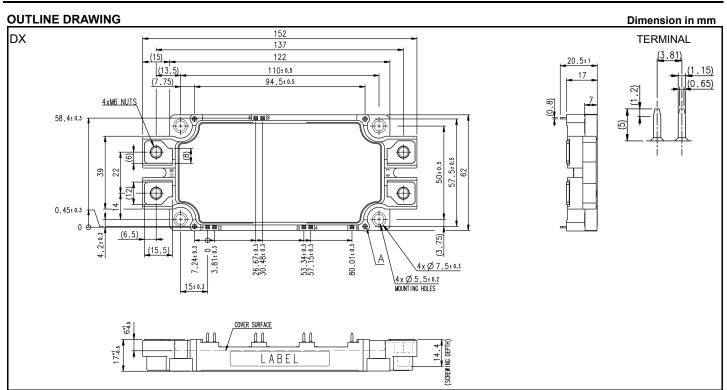
AC Motor Control, Motion/Servo Control, Power supply, etc.

OPTION (Below options are available.)

- •PC-TIM (Phase Change Thermal Interface Material) pre-apply
- •V_{CEsat} selection for parallel connection

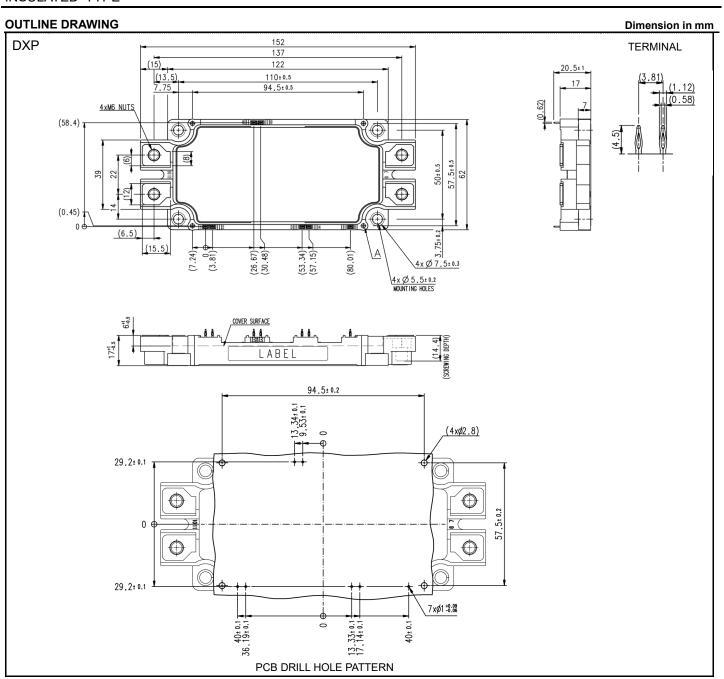
INTERNAL CONNECTION





Tolerance otherwise specified

Division	Tolerance			
0.5		to	3	±0.2
over 3		to	6	±0.3
over	6	to	30	±0.5
over 30		to 120		±0.8
over 120		to 4	400	±1.2



Tolerance otherwise specified

Divisio	n of I	Tolerance		
0.5		to	3	±0.2
over	3	to	6	±0.3
over	6	to	30	±0.5
over	30	to	120	±0.8
over 120		to 400		±1.2

MAXIMUM RATINGS (T $_{vj}$ =25 °C, unless otherwise specified) INVERTER PART IGBT/FWD

Symbol	Item	Conditions	Rating	Unit
V _{CES}	Collector-emitter voltage	G-E short-circuited	1700	V
V_{GES}	Gate-emitter voltage	C-E short-circuited	± 20	V
Ic	Collector current	DC, T _C =85 °C (Note2, 4)	300	^
I _{CRM}	Collector current	Pulse, Repetitive (Note3)	600	A
P _{tot}	Total power dissipation	T _C =25 °C (Note2, 4)	1515	W
IE (Note1)	Emitter current	DC (Note2)	300	^
IERM (Note1)	Emitter current	Pulse, Repetitive (Note3)	600	- A

MODULE

Symbol Item		Conditions	Rating	Unit
Visol	Isolation voltage	Terminals to base plate, RMS, f=60 Hz, AC 1 min	4000	V
T _{vjmax}	Maximum junction temperature	Instantaneous event (overload) (Note9)	175	°C
T _{Cmax}	Maximum case temperature	(Note4, 9)	125	
T _{vjop}	Operating junction temperature	Continuous operation (under switching) (Note9)	-40 ~ +150	°C
T _{stg}	Storage temperature	-	-40 ~ +125	

ELECTRICAL CHARACTERISTICS (Tvj=25 °C, unless otherwise specified) INVERTER PART IGBT/FWD

Symbol Item		Conditions			Unit		
Symbol	item	Conditions		Min.	Тур.	Max.	Offic
ICES	Collector-emitter cut-off current	V _{CE} =V _{CES} , G-E short-circuited		-	-	1.0	mA
I _{GES}	Gate-emitter leakage current	V _{GE} =V _{GES} , C-E short-circuited		-	-	0.5	μA
$V_{\text{GE(th)}}$	Gate-emitter threshold voltage	I _C =30 mA, V _{CE} =10 V		5.4	6.0	6.6	V
.,		I _C =300 A, V _{GE} =15 V,	T _{vj} =25 °C	-	2.05	2.45	
V _{CEsat} (Terminal)		Refer to the figure of test circuit	T _{vj} =125 °C	-	2.45	-	V
		(Note5)	T _{vj} =150 °C	-	2.55	-	
	Collector-emitter saturation voltage	I _C =300 A,	T _{vj} =25 °C	-	1.95	2.35	
V _{CEsat}		V _{GE} =15 V,	T _{vj} =125 °C	-	2.35	-	V
(Chip)		(Note5)	T _{vj} =150 °C	-	2.45	-	
Cies	Input capacitance			-	-	80	
C _{oes}	Output capacitance	V _{CE} =10 V, G-E short-circuited		-	-	2.2	nF
Cres	Reverse transfer capacitance	7			-	0.7	1
Q_{G}	Gate charge	V _{CC} =1000 V, I _C =300 A, V _{GE} =15 V		-	2.35	-	μC
t _{d(on)}	Turn-on delay time	- V _{cc} =1000 V, I _c =300 A, V _{GE} =±15 V,		-	-	800	- ns
tr	Rise time			-	-	200	
$t_{d(off)}$	Turn-off delay time			-	-	800	
t _f	Fall time	$-R_{\rm G}=0$ Ω, Inductive load		-	-	600	1 '
		I _E =300 A, G-E short-circuited,	T _{vi} =25 °C	-	2.75	3.35	
V _{EC} (Note1)		Refer to the figure of test circuit	T _{vj} =125 °C	-	2.95	-	V
(Terminal)		(Note5)	T _{vj} =150 °C	-	2.95	-	-
	Emitter-collector voltage	I _E =300 A,	T _{vi} =25 °C	-	2.65	3.25	
V _{EC} (Note1)		G-E short-circuited,	T _{vj} =125 °C	-	2.75	-	V
(Chip)		(Note5)	T _{vj} =150 °C	-	2.75	-	
t _{rr} (Note1)	Reverse recovery time	V _{CC} =1000 V, I _E =300 A, V _{GE} =±15 V,	,		-	300	ns
Qrr (Note1)	Reverse recovery charge	$R_{\rm G}=0$ Ω , Inductive load		-	12.5	-	μC
Eon	Turn-on switching energy per pulse	V _{cc} =1000 V, I _c =I _E =300 A,		-	74.5	-	
E _{off}	Turn-off switching energy per pulse	V _{GE} =±15 V, R _G =0 Ω, T _{vi} =150 °C,		-	65.7	-	mJ
Err (Note1)	Reverse recovery energy per pulse	Inductive load		-	36.8	-	mJ
$R_{CC'+EE'}$	Internal lead resistance	Main terminals-chip, per switch, T _c =25 °C	(Note4)	-	0.88	-	mΩ
r _g	Internal gate resistance	Per switch		-	2.5	-	Ω

ELECTRICAL CHARACTERISTICS (cont.; T_{vj} =25 °C, unless otherwise specified) NTC THERMISTOR PART

Symbol	Item	Conditions		Unit		
Symbol	item	Conduons	Min.	Тур.	Max.	Unit
R ₂₅	Zero-power resistance	T _C =25 °C ^(Note4)	4.85	5.00	5.15	kΩ
ΔR/R	Deviation of resistance	R_{100} =493 Ω, T_{C} =100 °C (Note4)	-7.3	-	+7.8	%
B _(25/50)	B-constant	Approximate by equation (Note6)	-	3375	-	К
P ₂₅	Power dissipation	T _C =25 °C ^(Note4)	-	-	10	mW

THERMAL RESISTANCE CHARACTERISTICS

Symbol	Item	Conditions	Limits			Unit
	nem	Conditions	Min.	Тур.	Max.	Unit
$R_{th(j-c)Q}$	Thermal resistance	Junction to case, per Inverter IGBT (Note4)	-	-	99	K/kW
$R_{th(j-c)D}$		Junction to case, per Inverter FWD (Note4)	-	-	149	N/KVV
$R_{th(c-s)}$	Contact thermal resistance	Case to heat sink, per 1 module Thermal grease applied ^(Note4, 7,9)	-	11.5	-	K/kW

MECHANICAL CHARACTERISTICS

Symbol	ltem	Conditions			Unit		
Symbol	Item	Col	Conditions		Тур.	Max.	Unit
Mt	Mounting torque	Main terminals	M 6 screw	3.5	4.0	4.5	N∙m
Ms	Mounting torque	Mounting to heat sink	M 5 screw	2.5	3.0	3.5	N∙m
		Coldennin ture (DV)	Terminal to terminal	17	-	-	
	Creepage distance	Solder pin type (DX)	Terminal to base plate	18.1	-	-	mm
ds		Pressfit pin type (DXP)	Terminal to terminal	17	-	-	
			Terminal to base plate	18.6	-	- IT	mm
		Solder pin type (DX)	Terminal to terminal	10	-	-	
			Terminal to base plate	16.2	-	-	mm
da	Clearance		Terminal to terminal	10	-	-	
		Pressfit pin type (DXP) Terminal to base plate		16.2	-	-	mm
ec	Flatness of base plate	On the centerline X, Y (Note8)		±0	-	+200	μm
m	mass	-	-	300	-	g	

*. This product is compliant with the Restriction of the Use of Certain Hazardous Substances in Electrical and Electronic Equipment (RoHS) directive 2011/65/EU and (EU) 2015/863.

Note1. Represent ratings and characteristics of the anti-parallel, emitter-collector free-wheeling diode (FWD).

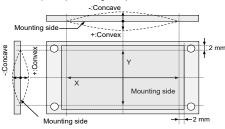
- 2. Junction temperature $(T_{\nu j})$ should not increase beyond $T_{\nu j\,m\,a\,x}$ rating.
- 3. Pulse width and repetition rate should be such that the device junction temperature (T_{vj}) dose not exceed T_{vjmax} rating.
- 4. Case temperature (T_c) and heat sink temperature (T_s) are defined on the each surface (mounting side) of base plate and heat sink just under the chips. Refer to the figure of chip location.

5. Pulse width and repetition rate should be such as to cause negligible temperature rise. Refer to the figure of test circuit.

6.
$$B_{(25/50)} = ln(\frac{R_{25}}{R_{50}})/(\frac{1}{T_{25}} - \frac{1}{T_{50}})$$

R₂₅: resistance at absolute temperature T₂₅ [K]; T₂₅=25 [°C]+273.15=298.15 [K]

- R_{50} : resistance at absolute temperature T_{50} [K]; $T_{50}\text{=}50$ [°C]+273.15=323.15 [K]
- 7. Reference value. Thermally conductive grease of thermal conductivity λ =0.9 W/(m·K) and thickness D_(C-S)=50 µm.
- 8. The base plate (mounting side) flatness measurement points (X, Y) are shown in the following figure.



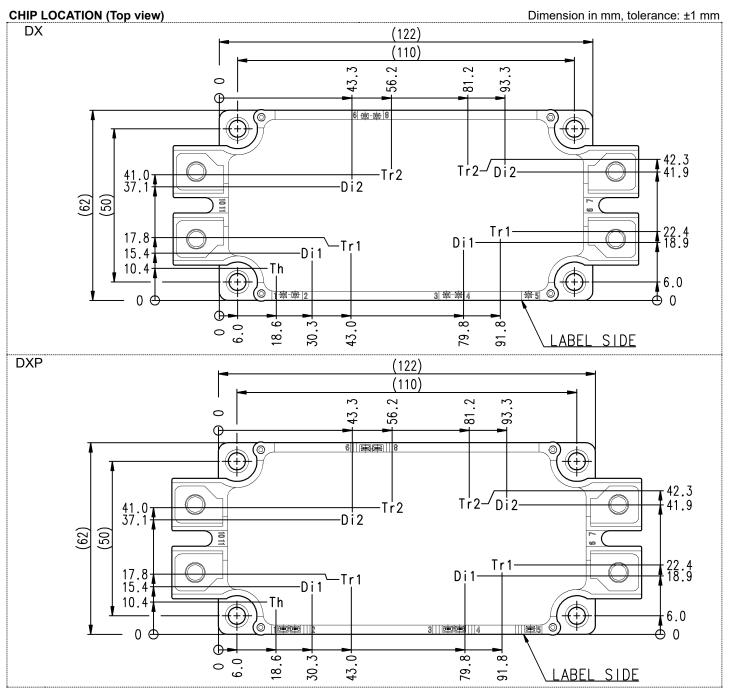
9. Long term performance related to thermal conductive grease (including but not limited to aspects such as the increase of thermal resistance due to pumping out, etc.) should be verified under user's specific application conditions. Each temperature condition (T_{vj max}, T_{vj op}, T_{C max}) must be maintained below the maximum rated temperature throughout consideration of the temperature rise even for long term usage.

Note10. Use the following screws when mounting the printed circuit board (PCB) on the standoffs. PCB thickness : t1.6

	Туре	Manufacturer	Size	Tightening torque (N•m)	Recommended tightening method
(1)	PT®	EJOT	K25×8	0.55 ± 0.055	
(2)	PT®		K25×10	0.75 ± 0.075 N∙m	by handwork (equivalent to 30 rpm
(3)	DELTA PT®		25×8	0.55 ± 0.055 N∙m	by mechanical screw driver)
(4)	DELTA PT®		25×10	0.75 ± 0.075 N∙m	~ 600 rpm (by mechanical screw driver)
(5)	B1	-	φ2.6×10	0.75 ± 0.075 N ⋅ m	
	tapping screw	pping screw φ2.6×12	φ2.6×12	0.75 ± 0.075 N°III	

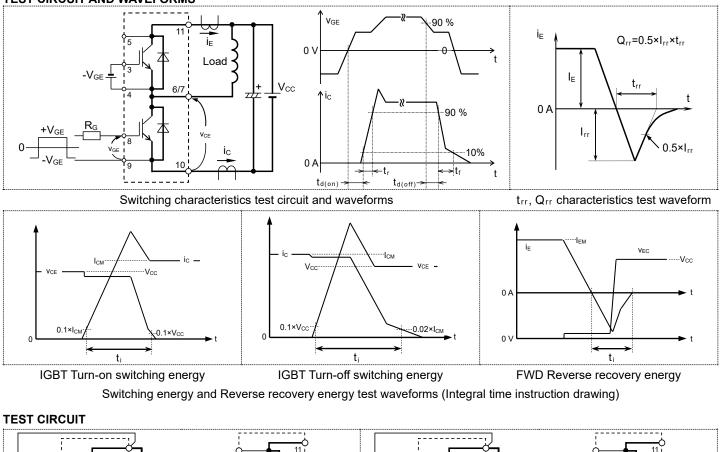
RECOMMENDED OPERATING CONDITIONS

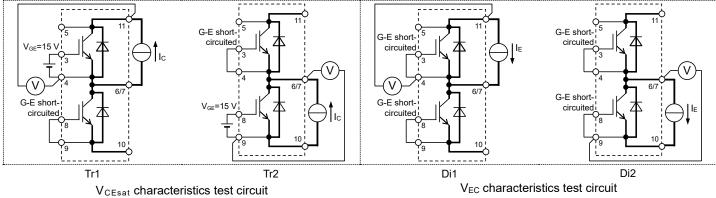
Svmbol	Item	Conditions		Unit		
Symbol	item	Conditions	Min.	Тур.	Max.	Onit
Vcc	DC) Supply voltage Applied across C1-E2 terminals		-	1000	1200	V
V_{GEon}	Gate (-emitter drive) voltage	Applied across G1-Es1/G2-Es2 terminals	13.5	15.0	16.5	V
R _G	External gate resistance	Per switch	0	-	16	Ω



Tr1/Tr2: IGBT, Di1/Di2: FWD, Th: NTC thermistor



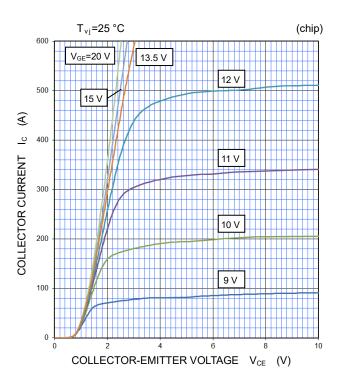




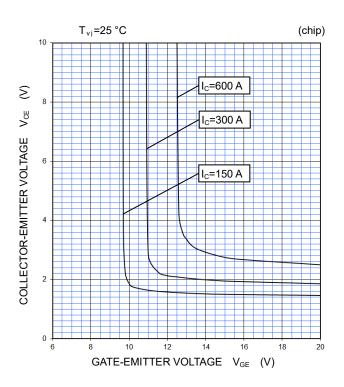
PERFORMANCE CURVES

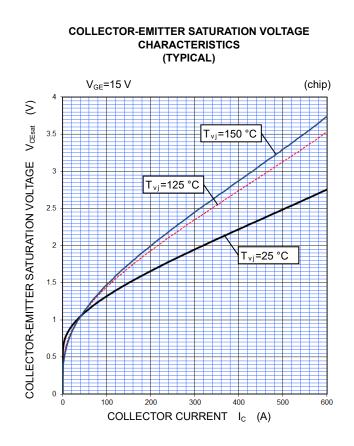
INVERTER PART



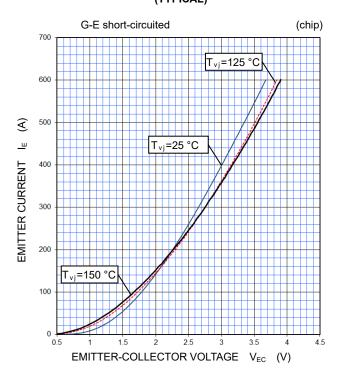


COLLECTOR-EMITTER VOLTAGE CHARACTERISTICS (TYPICAL)





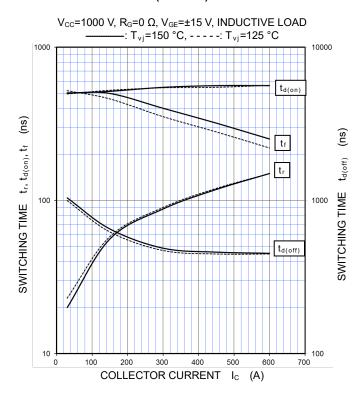
FREE WHEELING DIODE FORWARD CHARACTERISTICS (TYPICAL)



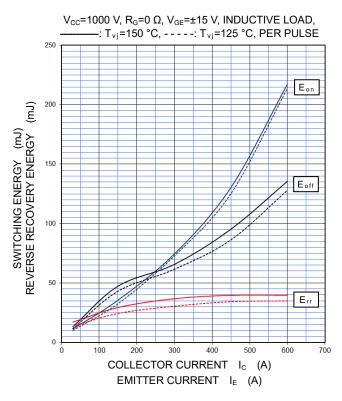
PERFORMANCE CURVES

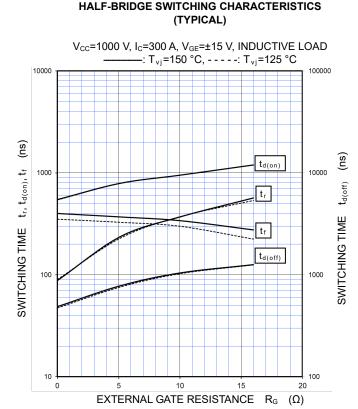
INVERTER PART



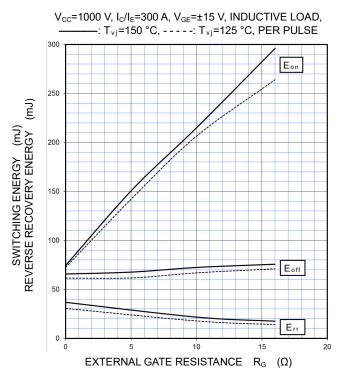


HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)





(TYPICAL)

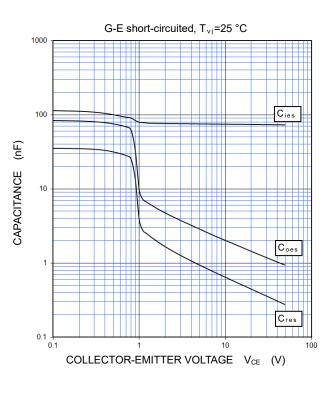


HALF-BRIDGE SWITCHING CHARACTERISTICS

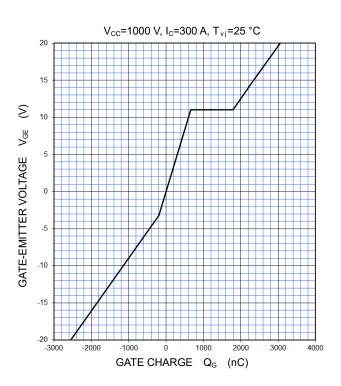
PERFORMANCE CURVES

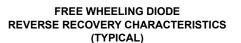
INVERTER PART

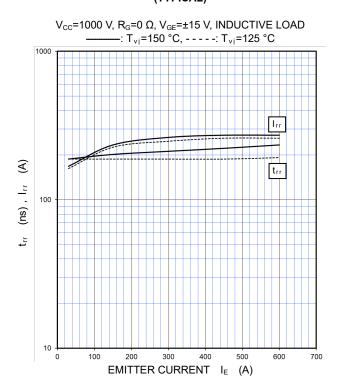
CAPACITANCE CHARACTERISTICS (TYPICAL)



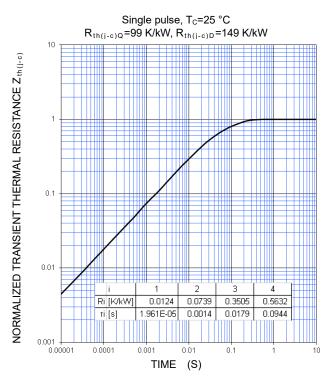
GATE CHARGE CHARACTERISTICS (TYPICAL)







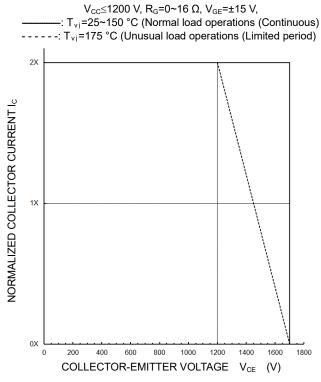
TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS (MAXIMUM)



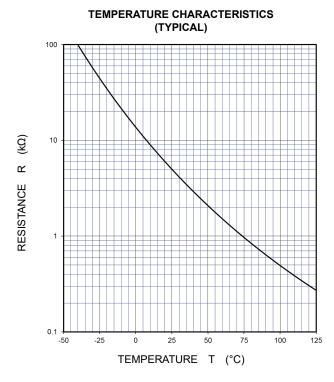
PERFORMANCE CURVES

INVERTER PART

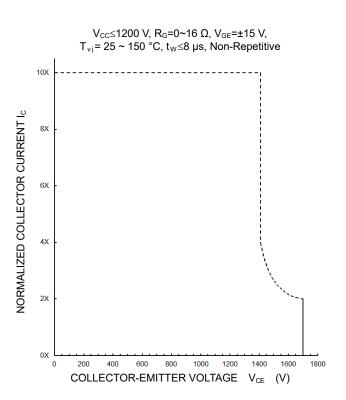
TURN-OFF SWITCHING SAFE OPERATIONG AREA (REVERSE BIAS SAFE OPERATING AREA) (MAXIMUM)



NTC thermistor part



SHORT-CIRCUIT SAFE OPERATING AREA (MAXIMUM)



Note: The characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

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