

<DIODE Modules>

RM800DY-34S

HIGH POWER SWITCHING USE INSULATED TYPE



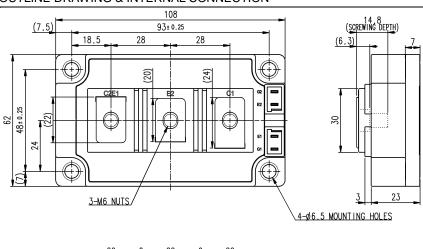
- Flat base Type
- Copper base plate
- •RoHS Directive compliant
- •UL Recognized under UL1557, File No. E323585

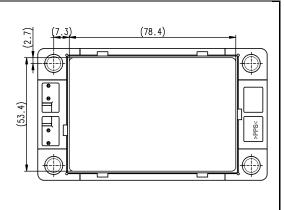
APPLICATION

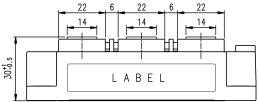
AC Motor Control, Motion/Servo Control, Power supply, Photovoltaic power, Wind power, etc.

OUTLINE DRAWING & INTERNAL CONNECTION

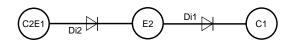
Dimension in mm







INTERNAL CONNECTION



Terminal code

C2E1 : A2 E2 : A1K2 C1 : K1

Tolerance	otherwise	specified

Division of Dimension		Tolerance
0.5	to 3	±0.2
over 3	to 6	±0.3
over 6	to 30	±0.5
over 30	to 120	±0.8
over 120	to 400	±1.2
over 400	to 1000	±2.0
over 1000	to 2000	±3.0
over 2000	to 4000	±4.0

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INSULATED TYPE

MAXIMUM RATINGS (Tvj=25 °C, unless otherwise specified)

Symbol	Item	Conditions	Rating	Unit
V_{RRM}	Repetitive peak reverse voltage	-	1700	V
V_{RSM}	Non-repetitive peak reverse voltage	-	1700	V
V _{R(DC)}	Reverse DC blocking voltage	-	1360	V
I _{DC}	Forward current	DC (Note1)	800	Α
I _{FSM}	Surge non-repetitive forward current	1 cycle of half wave at 60 Hz, peak value, T _{vj} =25 °C start, V _{RM} =0 V	4000	А
l ² t	Current square time for fusing	t _w =8.3 ms, T _{vj} =25 °C start, Value for one cycle of surge current	6.0×10^4	A ² s
Visol	Isolation voltage	Terminals to base plate, RMS, f=60 Hz, AC 1 min	4000	V
T_{vjmax}	Maximum junction temperature	Instantaneous event (overload)	175	°C
T _{Cmax}	Maximum case temperature			
T _{vjop}	Operating junction temperature	Continuous operation (under switching)	-40 ~ +150	°C
T _{stg}	Storage temperature	-	-40 ~ +125	

ELECTRICAL CHARACTERISTICS (Tvj=25 °C, unless otherwise specified)

Complete	Itam	Conditions		Limits			l lait
Symbol Item		Conditions		Min.	Тур.	Max.	Unit
I _{RRM}	Reverse current	V _R =V _{RRM} , T _{vj} =150 °C		-	-	50	mA
		I _F =800 A,	T _{vj} =25 °C	-	2.25	2.75	
V _F		t _w ≦1 ms,	T _{vj} =125 °C	-	2.35	-	V
(Terminal) Forward voltage	Forward voltage	Refer to the figure of test circuit	T _{vj} =150 °C	-	2.30	-	
V _F (Chip)		I _F =800 A, t _w ≦1 ms		-	2.00	2.50	V
trr	Reverse recovery time	V _{CC} =1000 V, I _F =800 A,		-	-	500	ns
Qrr	Reverse recovery charge	-diF/dt=4000 kA/µs,		=	160	-	μC
Err	Reverse recovery energy per pulse	Inductive load		-	104	-	mJ

THERMAL RESISTANCE CHARACTERISTICS

Symbol Item	Conditions	Limits			Unit	
	Conditions	Min.	Тур.	Max.	Unit	
R _{th(j-c)}	Thermal resistance	Junction to case (Note2)	-	-	20	K/kW
R _{th(c-s)}	Contact thermal resistance	Case to heat sink, Thermal grease applied (Note2, 4)	-	13.3	-	K/kW

MECHANICAL CHARACTERISTICS

Symbol	Item	Conditions		Limits			Unit
				Min.	Тур.	Max.	Unit
M _t	Mounting torque	Main terminals	M 6 screw	3.5	4.0	4.5	N∙m
Ms	Mounting torque	Mounting to heat sink	M 6 screw	3.5	4.0	4.5	N∙m
d _s Creepage distance	Craspage distance	Terminal to terminal		-	-	-	mm
	Creepage distance	Terminal to base plate		-	-	-	
٨	Clearance	Terminal to terminal		-	-	-	
d _a Clearand	Clearance	Terminal to base plate		-	-	-	mm
ес	Flatness of base plate	On the centerline X, Y (Note5)		0	-	+200	μm
m	mass	-		-	260	-	g

Publication Date : February 2018 TMH-1126-A Ver.1.1

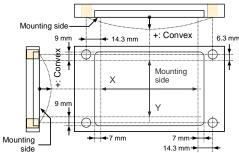
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HIGH POWER SWITCHING USE

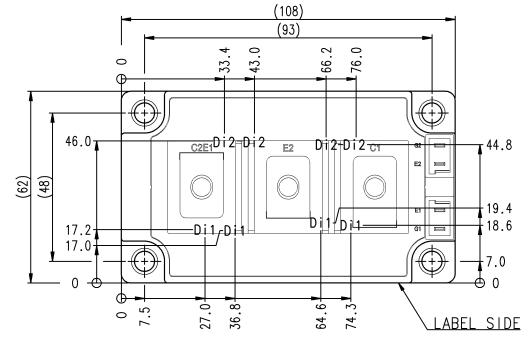
INSULATED TYPE

- *: This product is compliant with the Restriction of the Use of Certain Hazardous Substances in Electrical and Electronic Equipment (RoHS) directive 2011/65/EU.
- Note1. Junction temperature (T_{vj}) should not exceed T_{vjmax} rating.
 - 2. Case temperature (T_C) and heat sink temperature (T_S) are defined on the each surface (mounting side) of base plate and heat sink just under the chips. Refer to the figure of chip location.
 - 3. Pulse width and repetition rate should be such as to cause negligible temperature rise. Refer to the figure of test circuit.
 - 4. Typical value is measured by using thermally conductive grease of λ =3.0 W/(m·K)/D_(C-S)=50 µm.
 - 5. The base plate (mounting side) flatness measurement points (X, Y) are shown in the following figure.

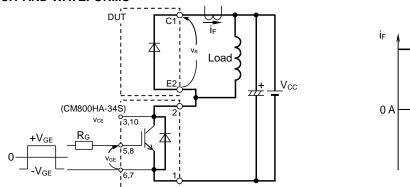


CHIP LOCATION (Top view)

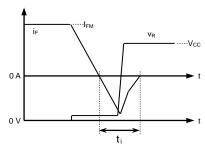
Dimension in mm, tolerance: ±1 mm



TEST CIRCUIT AND WAVEFORMS

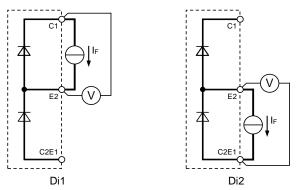


trr, Qrr characteristics test circuit and waveforms



Reverse recovery energy test waveforms (Integral time instruction drawing)

TEST CIRCUIT

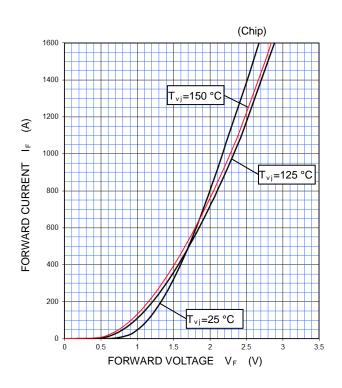


V_F characteristics test circuit

 $Q_{rr}=0.5\times I_{rr}\times t_{rr}$

PERFORMANCE CURVES

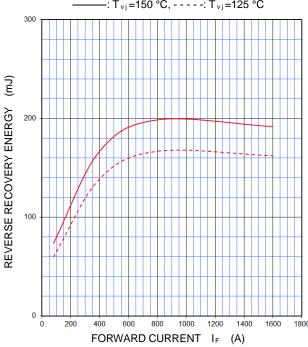
FORWARD CHARACTERISTICS (TYPICAL)



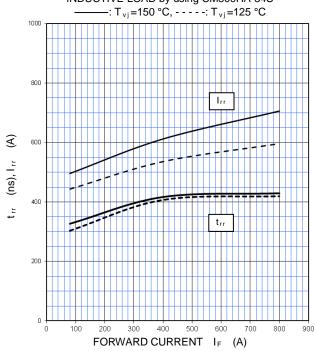
HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

 $V_{CC}=1000 \text{ V}, V_{GE}=\pm15 \text{ V}, R_G=0 \Omega,$ INDUCTIVE LOAD by using CM800HA-34S, PER PULSE

......: $T_{vi}=150 \, ^{\circ}\text{C}, ----: T_{vi}=125 \, ^{\circ}\text{C}$

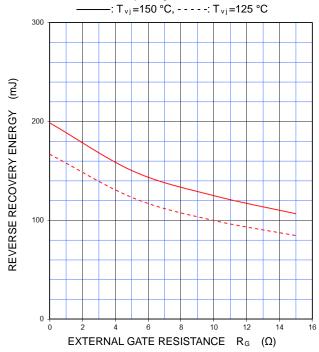


REVERSE RECOVERY CHARACTERISTICS (TYPICAL)



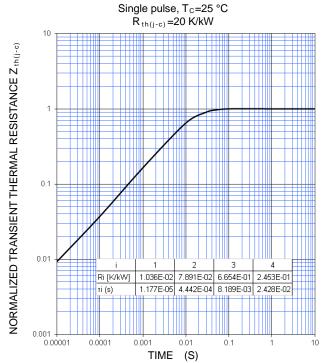
HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

 V_{CC} =1000 V, V_{GE} =±15 V, I_F =800 A, INDUCTIVE LOAD by using CM800HA-34S, PER PULSE



PERFORMANCE CURVES

TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS (MAXIMUM)



Note: The characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

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