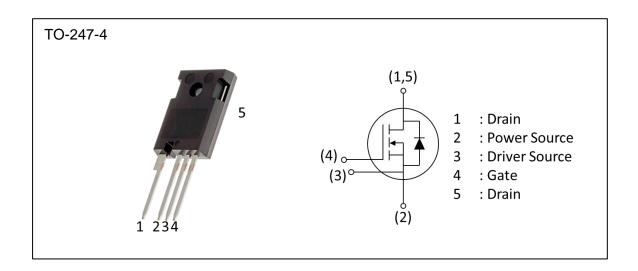




< SiC-MOSFET >

BM022N120K

N series 1200V TO-247-4



Features

- ✓ Low switching losses
- ✓ High tolerance for capacitive turn-on
- √ Fast reverse recovery of body diode
- ✓ Pb-free lead plating (RoHS compliant)

Applications

- ✓ Power factor correction
- ✓ Switch mode power supply
- ✓ Uninterruptible power supply
- ✓ Charging infrastructure
- ✓ Solar inverter

Key Performance

V_{DSS}	1200V
$I_D(T_C = 25^{\circ}C)$	107A
$R_{DS(on)}$ ($T_j = 25$ °C)	22mΩ

Packaging Specifications

Part Number	BM022N120K
Package	TO-247-4
Marking	BM022N120K

N series 1200V TO-247-4



Maximum ratings ($T_j = 25$ °C, unless otherwise noted)

Item	Symbol	Condition	Rating	Unit
Drain-source voltage	V _{DSS}	-	1200	V
Gate-source voltage	V _{GSS} *1	-	-10/+22	V
Out to the late of	1 *2	T _C = 25°C	107	А
Continuous drain current	l _D *²	T _C = 100°C	77	А
Pulsed drain current	I _{D,pulse} *3	Limited by T _{jmax}	284	А
Continuous body diode forward current	I _S *2	T _C = 25°C	89	А
Pulsed body diode forward current	I _{S,pulse} *3	Limited by T _{jmax}	213	А
Power dissipation	P _{TOT} *2	T _C = 25°C	468	W
Operating junction temperature	T _j	-	-55 to 175	°C
Storage temperature	T _{stg}	-	-55 to 150	°C
Soldering temperature	T _{sold}	1.6mm from case for 10s	260	°C
Mounting torque	М	-	0.8	N∙m

Thermal characteristics

Item	Symbol	Min.	Тур.	Max.	Unit
Thermal resistance, junction-case	R _{th(j-c)} *3	1	0.25	0.32	°C/W





Static characteristics ($T_j = 25$ °C, unless otherwise noted.)

Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Drain-source breakdown voltage	V _{(BR)DSS}	$V_{GS} = 0V$, $I_D = 10uA$	1200	-	-	V
Drain-source on-state resistance	R _{DS(on)}	$V_{GS} = 15V, I_{D} = 71A$ $T_{j} = 25^{\circ}C$ $T_{j} = 100^{\circ}C$ $T_{j} = 175^{\circ}C$	- - -	22 23 29	33 - -	mΩ
Body diode forward voltage	V _{SD}	$V_{GS} = -5V, I_{SD} = 71A, T_j = 25^{\circ}C$	-	4.1	-	V
Gate-source threshold voltage	V _{GS(th)} *4	$V_{DS} = 10V, I_{D} = 7.1mA$	1.7	2.3	2.9	V
Drain-source leakage current	I _{DSS}	V _{DS} = 1200V, V _{GS} = 0V	-	0.01	10	uA
Gate – Source leakage	I _{GSS}	$V_{GS} = 22V, V_{DS} = 0V$	-	-	100	nA
current		$V_{GS} = -10V, V_{DS} = 0V$	-	-	100	
Transconductance	g _{fs}	$V_{DS} = 10V, I_{D} = 71A$	-	30	-	S
Internal gate resistance	$R_{G,int}$	f = 500kHz	-	1	-	Ω
Input capacitance	C _{iss}	V _{DS} = 800V, V _{GS} = 0V, f = 500kHz	-	4680	-	
Output capacitance	C _{oss}		-	235	-	pF
Reverse capacitance	C _{rss}		-	8	-	
C _{oss} Stored Energy	E _{oss}		-	100	-	uJ





Dynamic characteristics (T_i = 25 °C, unless otherwise noted.)

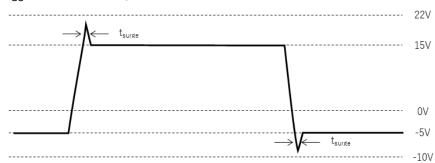
Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Total gate charge	Q_g	$V_{DD} = 800V, I_{D} = 71A,$ $V_{GS} = -5/15V$	-	163	-	
Gate to Drain charge	Q_{gd}		-	57	-	nC
Gate to Source charge	Q_{gs}		-	67	-	
Turn-on delay time	t _{d(on)}		-	21	-	
Rise time	t _r	V 000V I 74A	-	29	-	ns
Turn-off delay time	t _{d(off)}	$V_{DD} = 800V$, $I_{D} = 71A$, $V_{GS} = -5/15V$, $R_{G,ext} = 2.2\Omega$ FWD: same type device as D.U.T. at VGS = -5V Inductive load	-	36	-	
Fall time	t _f		-	9	-	
Turn-on switching loss	E _{on}		-	1093	-	
Turn-off switching loss	E _{off}		-	283	-	- uJ
Body diode reverse recovery charge	Q _{rr}	$V_{DD} = 800V$, $I_{S} = 71A$, $di/dt = 7300A/us$, $V_{GS} = -5V$	-	575	-	nC
Body diode reverse recovery time	t _{rr}		-	15	-	ns
Body diode reverse recovery current	I _{rr}		-	61		Α

*1 Recommended turn-off gate voltage V_{GS_off} is -5~0V.

Recommended turn-on gate voltage V_{GS on} is 15V.

Use with t_{surge} < 300ns. Do not use with V_{GS_on} < 13V.

V_{GS} Waveform Example



- *2 Limited by T_{jmax} and R_{th(j-c)max}
- *3 Designed value (not tested).
- *4 Tested after applying VGS = 20V for 200ms.

PRELIMINARY

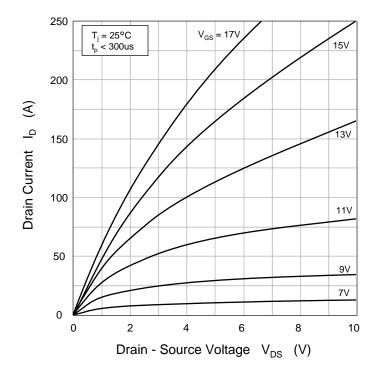


Figure 1 Typical Output Characteristics $(T_i = 25^{\circ}C)$

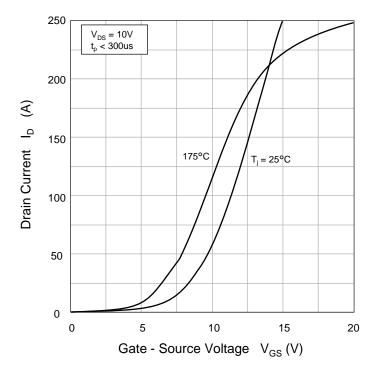


Figure 3 Typical Transfer Characteristics

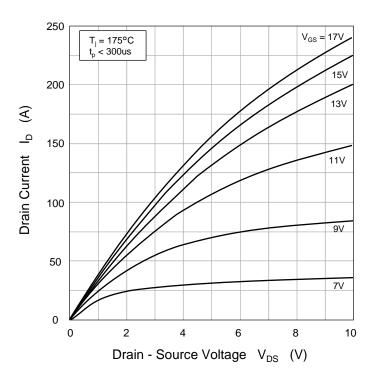


Figure 1 Typical Output Characteristics $(T_i = 175^{\circ}C)$

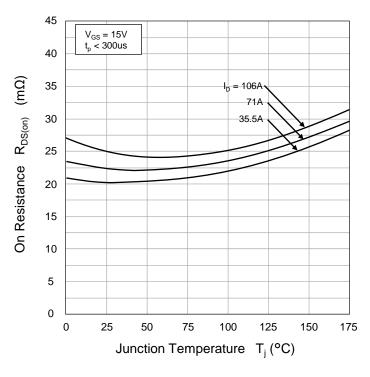


Figure 4 Typical On resistance vs.

Junction Temperature

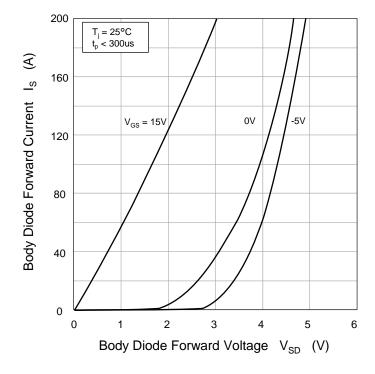


Figure 5 Typical Body Diode Forward current vs. Source-Drain Voltage ($T_i = 25$ °C)

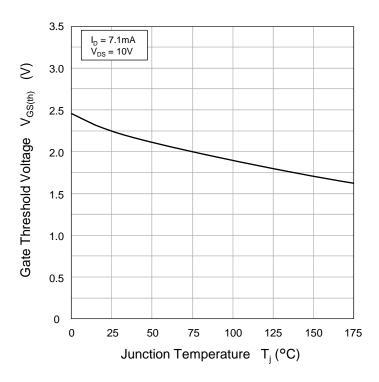


Figure 7 Typical Gate Threshold Voltage vs.
Junction Temperature

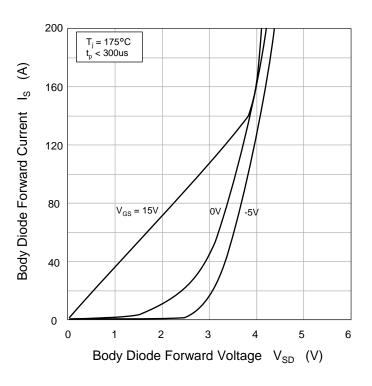


Figure 6 Typical Body Diode Forward current vs. Source-Drain Voltage (T_i = 175°C)

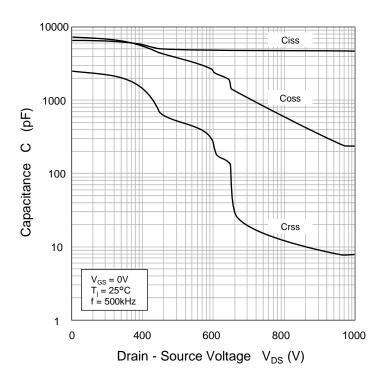


Figure 8 Typical Capacitance vs.

Drain-Source Voltage

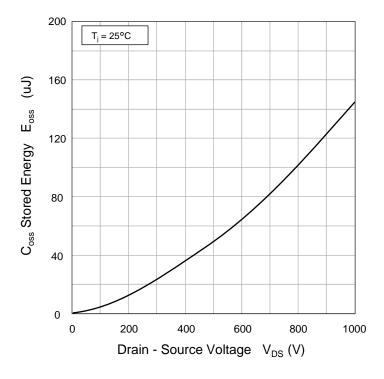


Figure 9 Typical C_{oss} Stored Energy

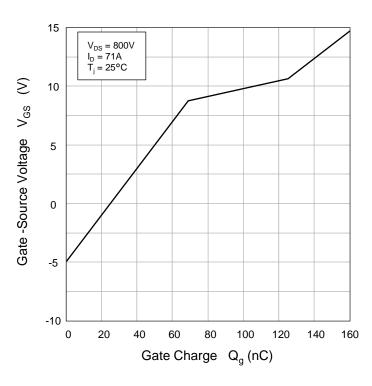


Figure 10 Typical Gate charge Characteristics

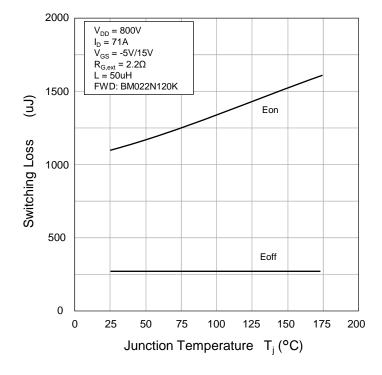


Figure 11 Typical Switching Loss vs.
Junction Temperature

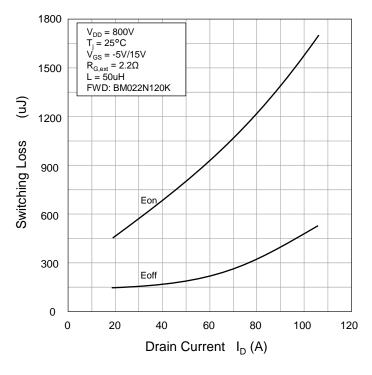


Figure 12 Typical Switching Loss vs. Drain Current ($T_i = 25$ °C)



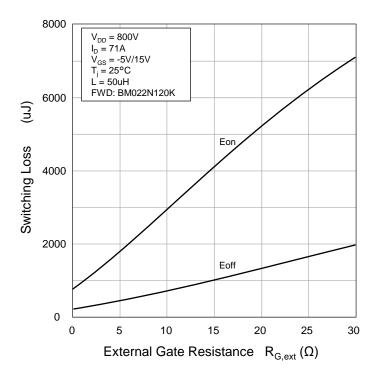


Figure 13 Typical Switching Loss vs. $R_{G,ext}$ $(T_i = 25^{\circ}C)$

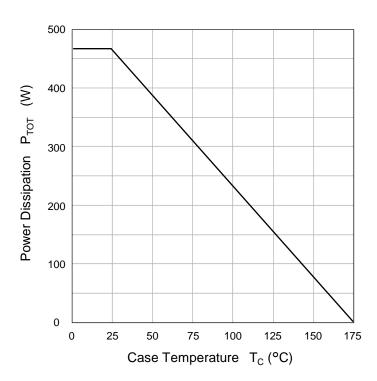


Figure 15 Maximum Power Dissipation Derating vs.

Case Temperature

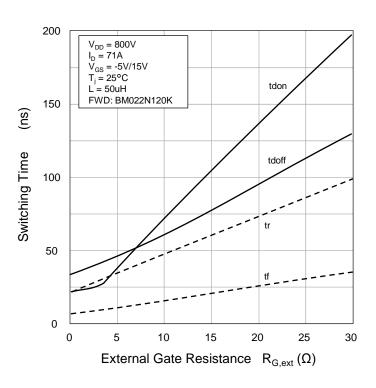


Figure 14 Typical Switching Times vs. $R_{G,ext}$ $(T_i = 25^{\circ}C)$

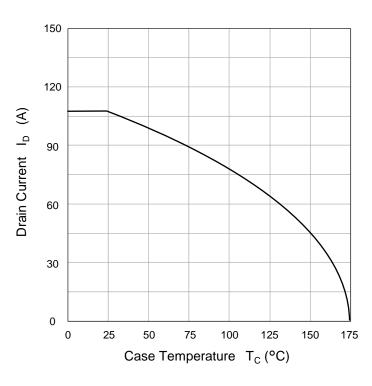


Figure 16 Maximum Continuous Drain Current vs.

Case Temperature

N series 1200V TO-247-4



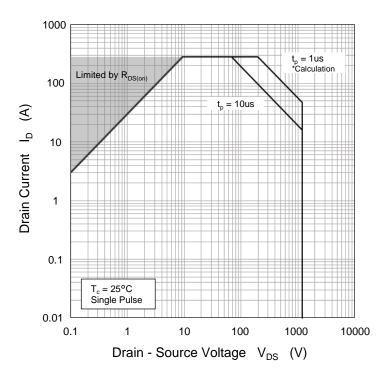


Figure 17 Maximum Safe Operating Area

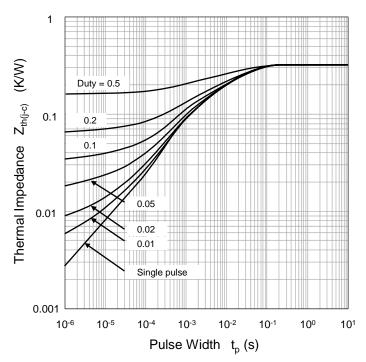
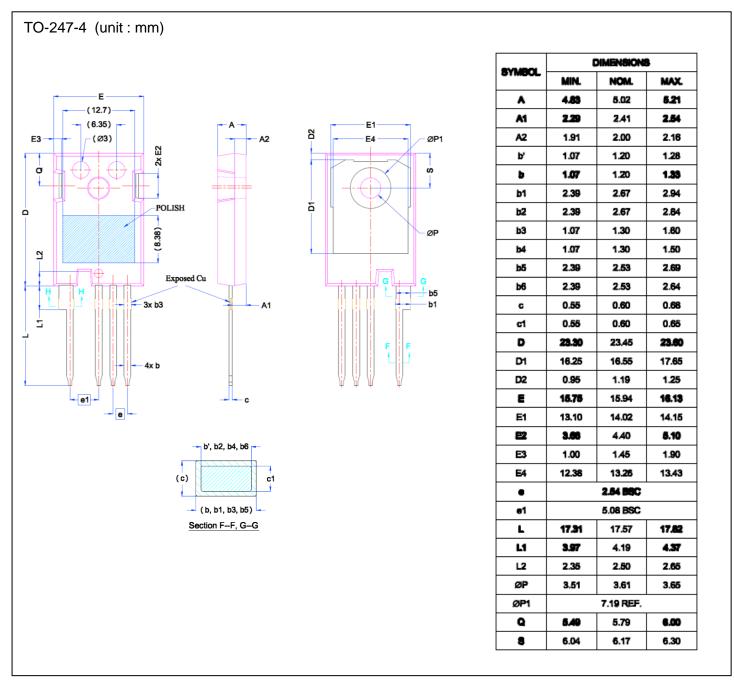


Figure 18 Maximum Transient Thermal Impedance vs. Pulse Width



Package Dimensions



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