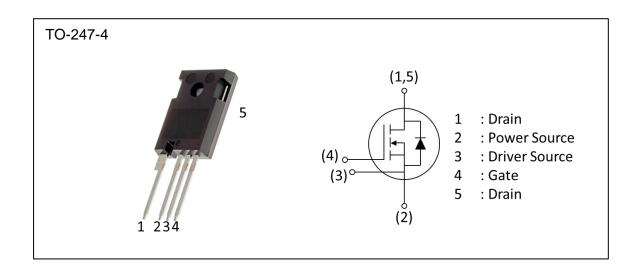




< SiC-MOSFET >

# BM080N120K

N series 1200V TO-247-4



#### **Features**

- ✓ Low switching losses
- ✓ High tolerance for capacitive turn-on
- ✓ Fast reverse recovery of body diode
- ✓ Pb-free lead plating (RoHS compliant)

### **Applications**

- ✓ Power factor correction
- ✓ Switch mode power supply
- ✓ Uninterruptible power supply
- ✓ Charging infrastructure
- ✓ Solar inverter

### **Key Performance**

V <sub>DSS</sub>	1200V
$I_D(T_C = 25^{\circ}C)$	36A
$R_{DS(on)}$ ( $T_i = 25$ °C)	80mΩ

### **Packaging Specifications**

Part Number	BM080N120K
Package	TO-247-4
Marking	BM080N120K

N series 1200V TO-247-4



## Maximum ratings ( $T_j = 25$ °C, unless otherwise noted)

Item	Symbol	Condition	Rating	Unit
Drain-source voltage	V <sub>DSS</sub>	-	1200	V
Gate-source voltage	V <sub>GSS</sub> *1	-	-10/+22	V
		T <sub>C</sub> = 25°C	36	А
Continuous drain current	l <sub>D</sub> *²	T <sub>C</sub> = 100°C	26	А
Pulsed drain current	I <sub>D,pulse</sub> *3	Limited by T <sub>jmax</sub>	80	А
Continuous body diode forward current	I <sub>S</sub> *2	T <sub>C</sub> = 25°C	36	А
Pulsed body diode forward current	I <sub>S,pulse</sub> *3	Limited by T <sub>jmax</sub>	60	А
Power dissipation	P <sub>TOT</sub> *2	T <sub>C</sub> = 25°C	202	W
Operating junction temperature	T <sub>j</sub>	-	-55 to 175	°C
Storage temperature	T <sub>stg</sub>	-	-55 to 150	°C
Soldering temperature	T <sub>sold</sub>	1.6mm from case for 10s	260	°C
Mounting torque	М	-	0.8	N∙m

### **Thermal characteristics**

Item	Symbol	Min.	Тур.	Max.	Unit
Thermal resistance, junction-case	R <sub>th(j-c)</sub> *3	-	0.59	0.74	°C/W





## Static characteristics ( $T_j = 25$ °C, unless otherwise noted.)

Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Drain-source breakdown voltage	V <sub>(BR)DSS</sub>	$V_{GS} = 0V$ , $I_D = 10uA$	1200	-	-	V
Drain-source on-state resistance	R <sub>DS(on)</sub>	$V_{GS} = 15V, I_{D} = 20A$ $T_{j} = 25^{\circ}C$ $T_{j} = 100^{\circ}C$ $T_{j} = 175^{\circ}C$	- - -	80 83 105	120 - -	mΩ
Body diode forward voltage	V <sub>SD</sub>	$V_{GS} = -5V, I_{SD} = 20A, T_j = 25^{\circ}C$	-	4.1	-	V
Gate-source threshold voltage	V <sub>GS(th)</sub> *4	$V_{DS} = 10V, I_{D} = 2.0mA$	1.7	2.3	2.9	V
Drain-source leakage current	I <sub>DSS</sub>	V <sub>DS</sub> = 1200V, V <sub>GS</sub> = 0V	-	0.01	10	uA
Gate – Source leakage current	I <sub>GSS</sub>	$V_{GS} = 22V, V_{DS} = 0V$	-	-	100	nA
		$V_{GS} = -10V, V_{DS} = 0V$	-	-	100	IIA
Transconductance	g <sub>fs</sub>	$V_{DS} = 10V, I_{D} = 20A$	-	9	ı	S
Internal gate resistance	$R_{G,int}$	f = 500kHz	-	2	ı	Ω
Input capacitance	C <sub>iss</sub>		-	1330	-	
Output capacitance	C <sub>oss</sub>	V <sub>DS</sub> = 800V, V <sub>GS</sub> = 0V, f = 500kHz	-	74	-	pF
Reverse capacitance	C <sub>rss</sub>		-	3	-	
C <sub>oss</sub> Stored Energy	E <sub>oss</sub>		-	31	-	uJ





### Dynamic characteristics (T<sub>i</sub> = 25 °C, unless otherwise noted.)

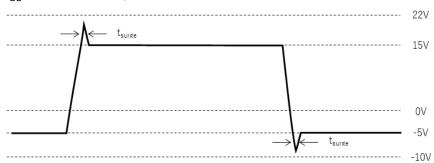
Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Total gate charge	$Q_g$	$V_{DD} = 800V, I_{D} = 20A,$ $V_{GS} = -5/15V$	-	50	-	
Gate to Drain charge	$Q_{gd}$		-	17	-	nC
Gate to Source charge	$Q_{gs}$		-	21	-	
Turn-on delay time	t <sub>d(on)</sub>		-	15	-	ns
Rise time	t <sub>r</sub>	) , , , , , , , , , , , , , , , , , , ,	-	16	-	
Turn-off delay time	t <sub>d(off)</sub>	$V_{DD} = 800V$ , $I_{D} = 20A$ , $V_{GS} = -5/15V$ , $R_{G,ext} = 2.2\Omega$ FWD: same type device as D.U.T. at VGS = -5V Inductive load	-	19	-	
Fall time	t <sub>f</sub>		-	10	-	
Turn-on switching loss	E <sub>on</sub>		-	243	-	
Turn-off switching loss	E <sub>off</sub>		-	61	-	- uJ
Body diode reverse recovery charge	Q <sub>rr</sub>	$V_{DD} = 800V, I_{S} = 20A,$ $di/dt = 4900A/us,$ $V_{GS} = -5V$	-	155	-	nC
Body diode reverse recovery time	t <sub>rr</sub>		-	10	-	ns
Body diode reverse recovery current	I <sub>rr</sub>		-	27		А

\*1 Recommended turn-off gate voltage  $V_{GS\_off}$  is -5~0V.

Recommended turn-on gate voltage V<sub>GS on</sub> is 15V.

Use with  $t_{surge}$  < 300ns. Do not use with  $V_{GS\_on}$  < 13V.

V<sub>GS</sub> Waveform Example



- \*2 Limited by T<sub>jmax</sub> and R<sub>th(j-c)max</sub>
- \*3 Designed value (not tested).
- \*4 Tested after applying VGS = 20V for 200ms.



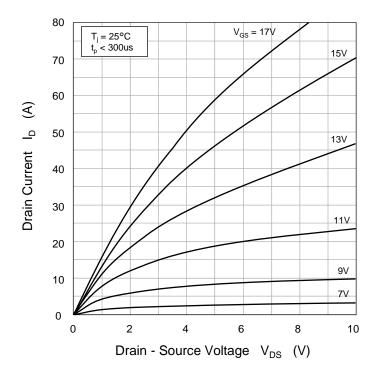


Figure 1 Typical Output Characteristics  $(T_i = 25^{\circ}C)$ 

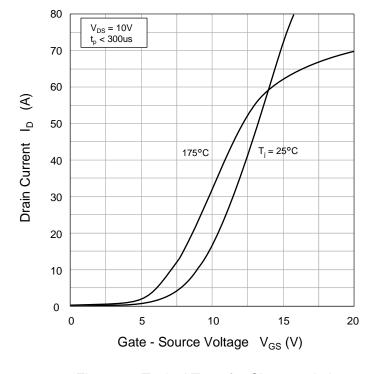


Figure 3 Typical Transfer Characteristics

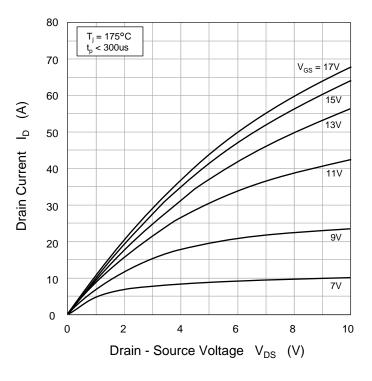


Figure 1 Typical Output Characteristics  $(T_i = 175^{\circ}C)$ 

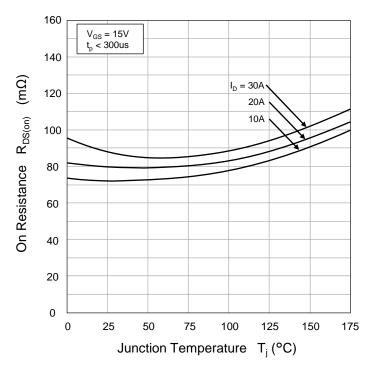


Figure 4 Typical On resistance vs.

Junction Temperature

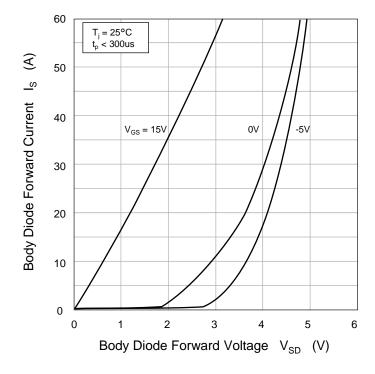


Figure 5 Typical Body Diode Forward current vs. Source-Drain Voltage ( $T_i = 25$ °C)

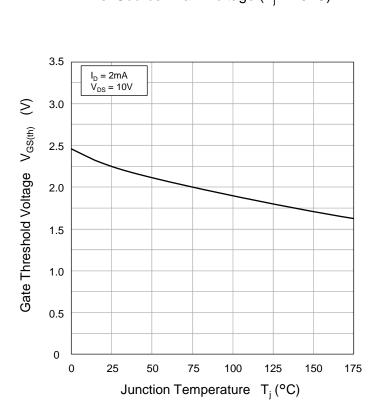


Figure 7 Typical Gate Threshold Voltage vs.
Junction Temperature

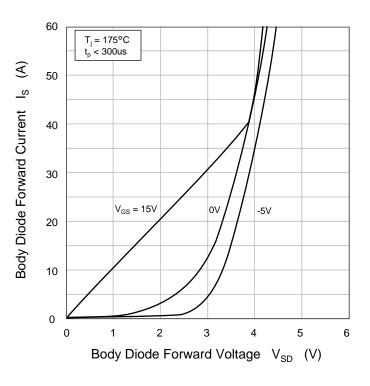


Figure 6 Typical Body Diode Forward current vs. Source-Drain Voltage (T<sub>i</sub> = 175°C)

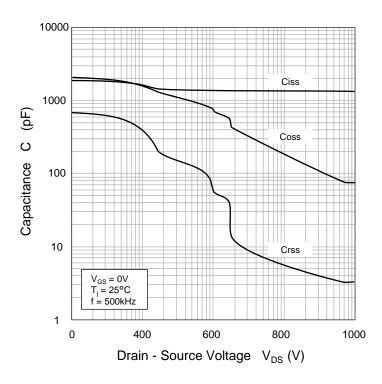


Figure 8 Typical Capacitance vs.
Drain-Source Voltage

## **PRELIMINARY**

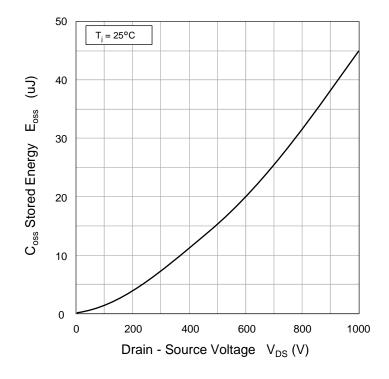


Figure 9 Typical  $C_{oss}$  Stored Energy

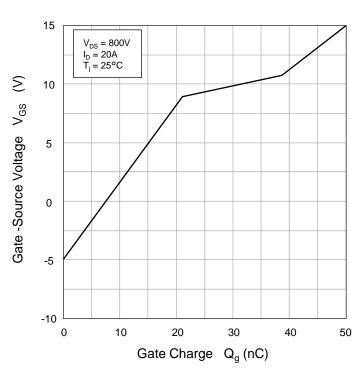


Figure 10 Typical Gate charge Characteristics

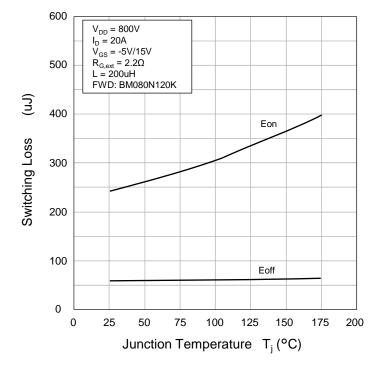


Figure 11 Typical Switching Loss vs.
Junction Temperature

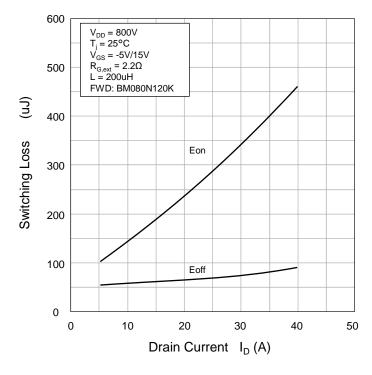


Figure 12 Typical Switching Loss vs.

Drain Current ( $T_i = 25$ °C)

## N series 1200V TO-247-4

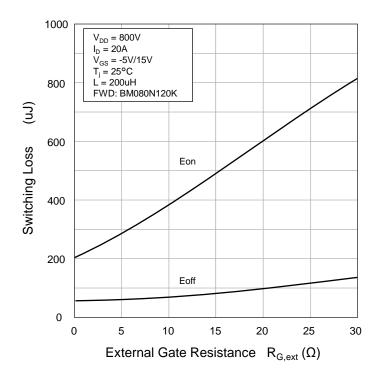


Figure 13 Typical Switching Loss vs.  $R_{G,ext}$   $(T_i = 25^{\circ}C)$ 

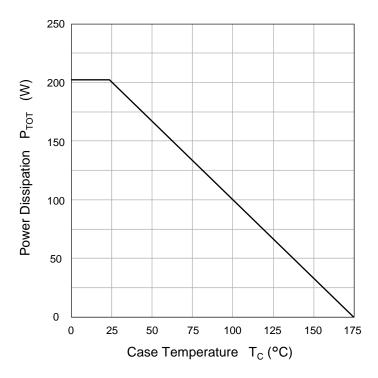


Figure 15 Maximum Power Dissipation Derating vs.

Case Temperature

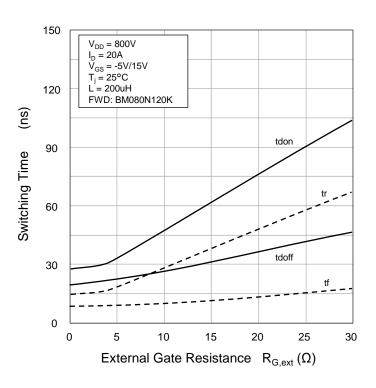


Figure 14 Typical Switching Times vs.  $R_{G,ext}$  ( $T_i = 25$ °C)

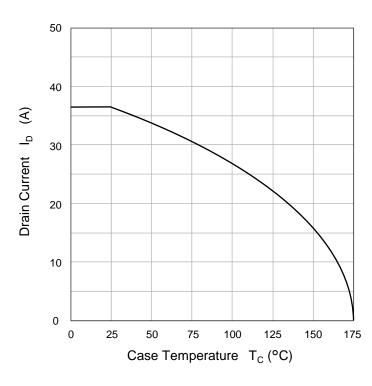


Figure 16 Maximum Continuous Drain Current vs.

Case Temperature

### N series 1200V TO-247-4



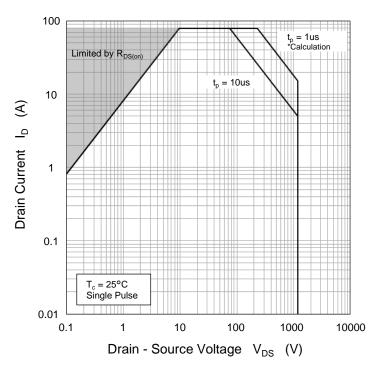


Figure 17 Maximum Safe Operating Area

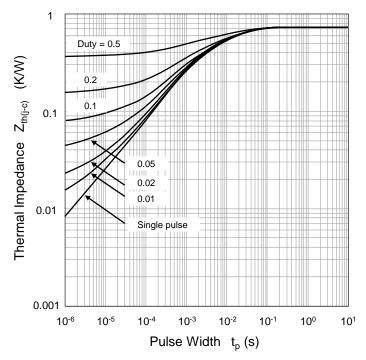
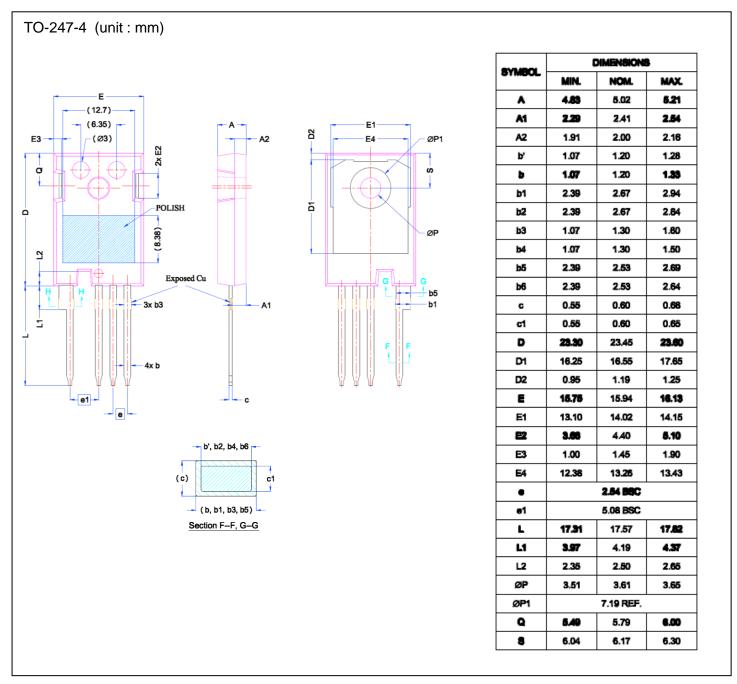


Figure 18 Maximum Transient Thermal Impedance vs. Pulse Width



### **Package Dimensions**



N series 1200V TO-247-4



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N series 1200V TO-247-4



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