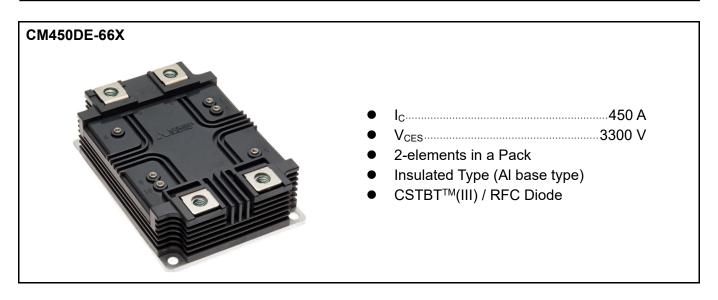


### **CM450DE-66X**

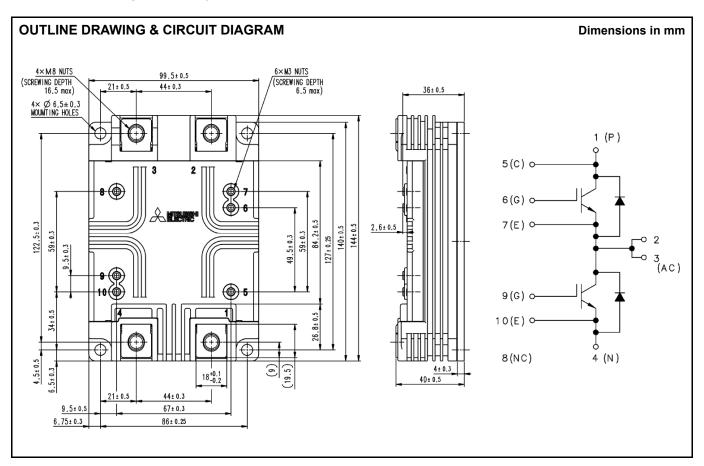
HIGH POWER SWITCHING USE INSULATED TYPE

5th-Version HVIGBT (High Voltage Insulated Gate Bipolar Transistor) Modules



#### **APPLICATION**

Traction drives, High Reliability Converters / Inverters, DC choppers



### CM450DE-66X

HIGH POWER SWITCHING USE

**INSULATED TYPE** 

5th-Version HVIGBT (High Voltage Insulated Gate Bipolar Transistor) Modules

### **MAXIMUM RATINGS**

Symbol	Item	Conditions	Ratings	Unit	
Vces	Collector-emitter voltage	$V_{GE} = 0V, T_j = -50 \text{ °C}$ $V_{GE} = 0V, T_j = -40+150 \text{ °C}$	3200 3300	V	
V <sub>GES</sub>	Gate-emitter voltage	V <sub>CE</sub> = 0V, T <sub>j</sub> = 25 °C	± 20	V	
Ic	Collector current	DC, T <sub>c</sub> = 114 °C Pulse (Note 1)	450	Α	
ICRM			900		
I <sub>E</sub>	Emitter current (Note 2)	DC, T <sub>c</sub> = 98 °C Pulse (Note 1)	450 900	A	
P <sub>tot</sub>	Maximum power dissipation	T <sub>c</sub> = 25 °C, IGBT part (Note 3)	5000	W	
Viso	Isolation voltage	Charged part to the base-plate RMS sinusoidal, 60 Hz 1 min., T <sub>c</sub> = 25 °C	10200	V	
Q <sub>PD</sub>	Partial discharge	Charged part to the base-plate V1 = 6900 Vrms, V2 = 5100 Vrms AC 60 Hz, T <sub>c</sub> = 25 °C (acc. to IEC 61287-1)	10	рС	
Tj	Junction temperature	_	<b>−50 ~ +150</b>	°C	
T <sub>jop</sub>	Operating junction temperature	_	<b>−50 ~ +150</b>	°C	
T <sub>stg</sub>	Storage temperature	_	−55 ~ <b>+150</b>	°C	
t <sub>psc</sub>	Short circuit pulse width	$V_{CC} \le 2400 \text{ V}, V_{GE} = \pm 15 \text{ V}$ $R_{G(on)} = 2.7 \Omega, R_{G(off)} = 62 \Omega$ $T_j = T_{jop}, C_{GE} = 33 \text{ nF}, L_S = 85 \text{ nH}$	10	μs	

### **ELECTRICAL CHARACTERISTICS**

Symbol	ltem	Conditions			Limits		Unit
Syllibol	item			Min.	Тур.	Max.	Oill
			T <sub>j</sub> = 25 °C	_	_	1.5	
I <sub>CES</sub>	Collector cutoff current	V <sub>CE</sub> = V <sub>CES</sub> V <sub>GF</sub> = 0V	T <sub>i</sub> = 125 °C	_	1.5		mΑ
		VGE - UV	T <sub>j</sub> = 150 °C		15.0	_	
V <sub>GE(th)</sub>	Gate-emitter threshold voltage	$V_{CE} = 10 \text{ V}, I_{C} = 45 \text{ mA}, T_{j} = 25 \text{ mA}$	5 °C	6.5	7.0	7.5	V
I <sub>GES</sub>	Gate leakage current	V <sub>GE</sub> = V <sub>GES</sub> , V <sub>CE</sub> = 0 V, T <sub>j</sub> = 25	°C	-0.5	_	0.5	μΑ
		L = 450 A	T <sub>j</sub> = 25 °C		2.20	_	
$V_{CEsat}$	Collector-emitter saturation voltage	I <sub>C</sub> = 450 A V <sub>GE</sub> = 15 V <sup>(Note 4)</sup>	T <sub>j</sub> = 125 °C	_	2.65	_	V
		VGE = 13 V \ /	T <sub>j</sub> = 150 °C	_	2.75	3.15	
Cies	Input capacitance			_	44.5	_	
Coes	Output capacitance	$V_{CE} = 10 \text{ V}, V_{GE} = 0 \text{ V}$ f = 100 kHz, T <sub>i</sub> = 25 °C		_	3.1	_	nF
Cres	Reverse transfer capacitance	1 - 100 KHZ, 1j - 25 C		_	0.4	_	
$Q_{\mathrm{G}}$	Total gate charge	V <sub>CC</sub> = 1800 V, I <sub>C</sub> = 450 A V <sub>GE</sub> = ±15 V, T <sub>i</sub> = 25 °C		_	3.0	_	μC
t <sub>d(on)</sub>	Turn-on delay time	V <sub>CC</sub> = 1800 V	T <sub>j</sub> = 150 °C	_		1.25	μs
tr	Rise time	Ic = 450 A	T <sub>j</sub> = 150 °C			0.50	μs
	Turn-on switching energy per pulse (Note 5)	$V_{GE} = \pm 15 \text{ V}$ $R_{G(on)} = 2.7 \Omega$ $C_{GE} = 33 \text{ nF}$	T <sub>j</sub> = 25 °C	_	0.74	_	J
E <sub>on(10%)</sub>			T <sub>j</sub> = 125 °C	_	0.89	_	
			T <sub>j</sub> = 150 °C	_	0.90	_	
	Turn-on switching energy per pulse	L <sub>S</sub> = 85 nH	T <sub>j</sub> = 25 °C	_	0.79	_	
Eon		Inductive load	T <sub>j</sub> = 125 °C		0.95	_	J
			T <sub>j</sub> = 150 °C		0.96	_	
			T <sub>j</sub> = 25 °C	_	3.40	_	
$t_{d(off)}$	Turn-off delay time		T <sub>j</sub> = 125 °C	_	3.60	_	μs
		Vcc = 1800 V	T <sub>j</sub> = 150 °C	_	3.65	5.00	
	Fall time	Ic = 450 A	T <sub>j</sub> = 25 °C	_	0.24	_	
t <sub>f</sub>		V <sub>GE</sub> = ±15 V	T <sub>j</sub> = 125 °C	_	0.35	— μs	μs
		$R_{G(off)} = 62 \Omega$	T <sub>j</sub> = 150 °C	_	0.37	1.00	
	Turn-off switching energy per pulse (Note 5)	C <sub>GE</sub> = 33 nF	T <sub>j</sub> = 25 °C	_	0.55		
E <sub>off(10%)</sub>		L <sub>S</sub> = 85 nH	T <sub>j</sub> = 125 °C	_	0.74	_	J
		1	T <sub>j</sub> = 150 °C	_	0.75	_	
	Turn-off switching energy per pulse	Inductive load	T <sub>j</sub> = 25 °C	_	0.62	_	
$E_{off}$			T <sub>j</sub> = 125 °C	_	0.84	_	J
			T <sub>i</sub> = 150 °C		0.85		

### CM450DE-66X

**HIGH POWER SWITCHING USE** 

**INSULATED TYPE** 

5th-Version HVIGBT (High Voltage Insulated Gate Bipolar Transistor) Modules

### **ELECTRICAL CHARACTERISTICS**

Symbol	Item	Conditions		Limits			Unit	
Symbol	iteiii			Min.	Тур.	Max.	Offic	
	Emitter-collector voltage (Note 2)		T <sub>j</sub> = 25°C	_	2.00	_		
VEC		$I_E = 450 \text{ A}$ $V_{GE} = 0 \text{ V}^{\text{(Note 4)}}$	T <sub>j</sub> = 125°C	_	2.20	_	V	
		VGE - U V \ /	T <sub>j</sub> = 150°C	_	2.30	2.80		
			T <sub>j</sub> = 25°C	_	0.65	_		
$t_{rr}$	Reverse recovery time (Note 2)		$T_j = 125^{\circ}C$	_	0.80		μs	
			T <sub>j</sub> = 150°C	_	0.85	_		
	Reverse recovery current (Note 2)		$T_j = 25^{\circ}C$	_	720	_	А	
Irr			$T_j = 125^{\circ}C$	_	690	_		
		$\begin{array}{l} V_{CC} = 1800 \text{ V} \\ I_{C} = 450 \text{ A} \\ V_{GE} = \pm 15 \text{ V} \\ R_{G(on)} = 2.7 \Omega \\ C_{GE} = 33 \text{ nF} \\ L_{s} = 85 \text{ nH} \end{array}$	$T_j = 150^{\circ}C$	_	680	_		
	Reverse recovery charge (Note 2, 6)		$T_j = 25^{\circ}C$	_	450	_	μC	
Qrr(10%)			$T_j = 125^{\circ}C$	_	555	_		
			T <sub>j</sub> = 150°C	_	585	_		
	Reverse recovery charge (Note 2)		$T_j = 25^{\circ}C$	_	490	_		
Qrr			T <sub>j</sub> = 125°C	_	605	_		
		Industive lead	$T_j = 150^{\circ}C$	_	635	_		
	Daviere receiver and	Inductive load	T <sub>j</sub> = 25°C	_	0.46	_		
Erec(10%)	Reverse recovery energy per pulse (Note 2, 5)	Nor pulse (Note 2, 5)		T <sub>j</sub> = 125°C	_	0.62		J
			T <sub>j</sub> = 150°C	_	0.64	_		
	Reverse recovery energy per pulse (Note 2)		T <sub>j</sub> = 25°C		0.53	_		
Erec			T <sub>j</sub> = 125°C	_	0.71	_	J	
			T <sub>j</sub> = 150°C		0.73			

#### THERMAL CHARACTERISTICS

Cumbal	ymbol Item	Conditions		Limits		
Symbol		Conditions	Min.	Тур.	Max.	Unit
R <sub>th(j-c)Q</sub>	Thermal resistance	Junction to Case, IGBT part, 1/2 module	_	_	25.0	K/kW
R <sub>th(j-c)D</sub>	Thermal resistance	Junction to Case, FWDi part, 1/2 module	_	_	41.0	K/kW
R <sub>th(c-s)</sub>	Contact thermal resistance	Case to heat sink, 1/2 module		16.0		K/kW
i Viri(C-S)	Contact thermal resistance	$\lambda_{grease} = 1 \text{ W/m} \cdot \text{K}, D_{(c-s)} = 70 \mu\text{m}$	_	10.0		1 1/1/1/ /

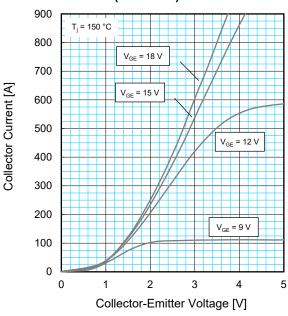
#### **MECHANICAL CHARACTERISTICS**

Symbol	Item	Conditions		Limits		
Symbol				Тур.	Max.	Unit
Mt		Main terminals screw: M8		_	14.0	N·m
Ms	Mounting torque	Mounting screw: M6		_	6.0	N⋅m
Mt		Auxiliary terminals screw: M3		_	8.0	N⋅m
m	Mass	_	_	0.75		kg
CTI	Comparative tracking index	_	600	_	_	_
da	Clearance	_	26.0	_	_	mm
ds	Creepage distance	_	56.0	_		mm
L <sub>P P-N</sub>	Parasitic stray inductance	Between P-side terminal and N-side terminal	_	40		nΗ
Rcc'+EE'	Internal lead resistance	$T_c$ = 25 °C, 1/2 module	_	0.59	_	mΩ

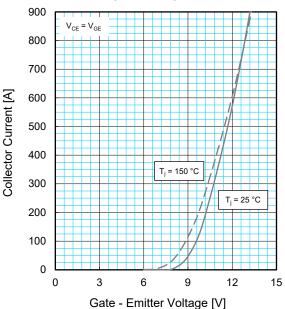
Note1. Pulse width and repetition rate should be such that junction temperature  $(T_j)$  does not exceed maximum  $T_{jop}$  rating  $(150^{\circ}\text{C})$ .

Note2. The symbols represent characteristics of the anti-parallel, emitter to collector free-wheel diode (FWDi). Note3. Junction temperature (Tj) should not exceed T<sub>jmax</sub> rating (150°C). Note4. Pulse width and repetition rate should be such as to cause negligible temperature rise. Note5. The integration range of switching energies is from 10%V<sub>CE</sub> to 10%lc(I<sub>E</sub>). Note6. The integration range of reverse recovery charge is from I<sub>E</sub>=0A to 10%I<sub>E</sub>

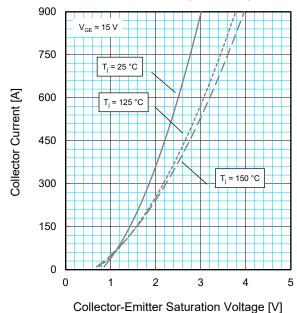
### **OUTPUT CHARACTERISTICS** (TYPICAL) 900 T<sub>i</sub> = 150 °C



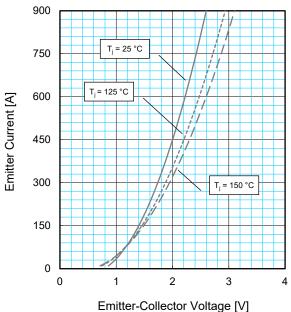
### TRANSFER CHARACTERISTICS (TYPICAL)



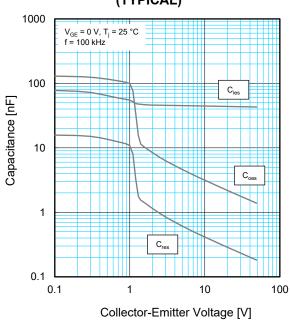
### **COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)**



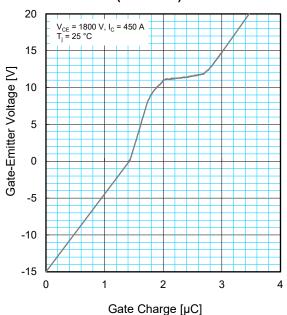
### FREE-WHEEL DIODE FORWARD **CHARACTERISTICS (TYPICAL)**



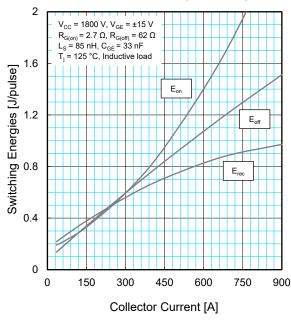
# CAPACITANCE CHARACTERISTICS (TYPICAL)



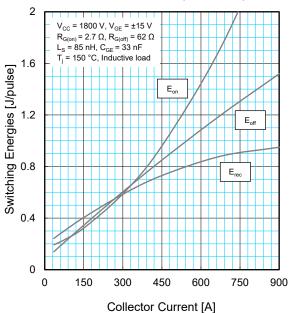
## GATE CHARGE CHARACTERISTICS (TYPICAL)



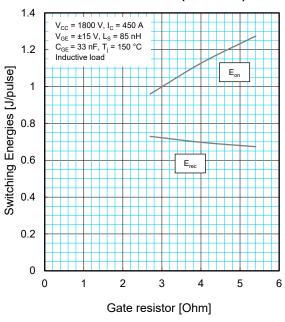
# HALF-BRIDGE SWITCHING ENERGY CHARACTERISTICS (TYPICAL)



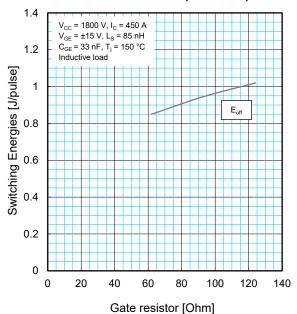
# HALF-BRIDGE SWITCHING ENERGY CHARACTERISTICS (TYPICAL)



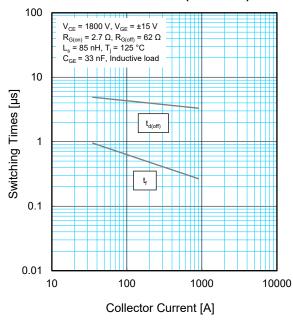
# HALF-BRIDGE SWITCHING ENERGY CHARACTERISTICS (TYPICAL)



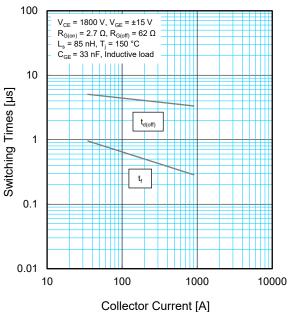
## HALF-BRIDGE SWITCHING ENERGY CHARACTERISTICS (TYPICAL)



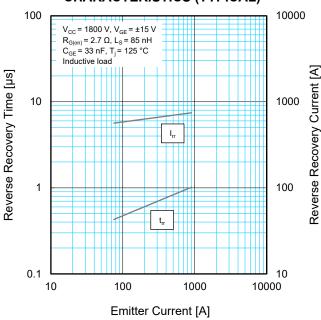
## HALF-BRIDGE SWITCHING TIME CHARACTERISTICS (TYPICAL)



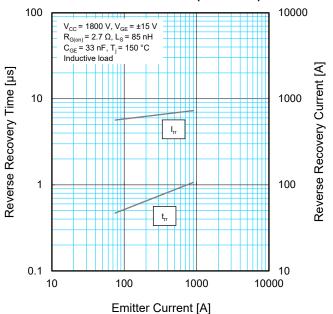
# HALF-BRIDGE SWITCHING TIME CHARACTERISTICS (TYPICAL)



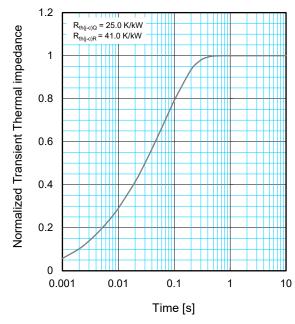
## FREE-WHEEL DIODE REVERSE RECOVERY CHARACTERISTICS (TYPICAL)



## FREE-WHEEL DIODE REVERSE RECOVERY CHARACTERISTICS (TYPICAL)



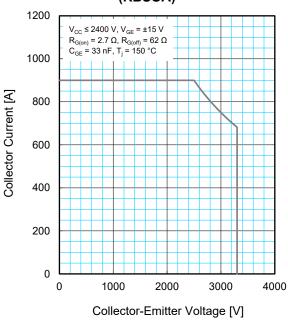
## TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS



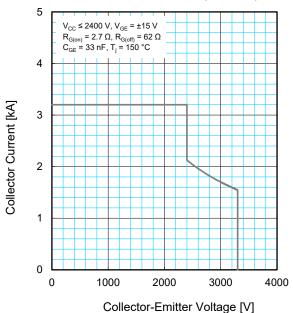
$$Z_{th(j-c)}(t) = \sum_{i=1}^{n} R_i \left\{ 1 - exp^{\left(-\frac{t}{\tau_i}\right)} \right\}$$

	1	2	3	4
R <sub>i</sub> / R <sub>th(j-c)</sub>	0.0292	0.0832	0.2277	0.6599
τ i [s]	0.0025	0.0027	0.0155	0.0865

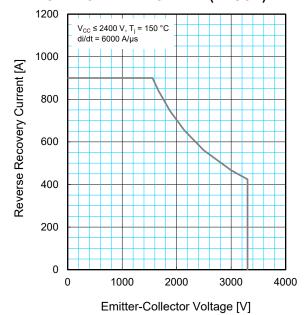
# REVERSE BIAS SAFE OPERATING AREA (RBSOA)



## SHORT CIRCUIT SAFE OPERATING AREA (SCSOA)



## FREE-WHEEL DIODE REVERSE RECOVERY SAFE OPERATING AREA (RRSOA)



CM450DE-66X

HIGH POWER SWITCHING USE

INSULATED TYPE 5th-Version HVIGBT (High Voltage Insulated Gate Bipolar Transistor) Modules

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**INSULATED TYPE** 

5th-Version HVIGBT (High Voltage Insulated Gate Bipolar Transistor) Modules

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