

<IGBT Modules>

CM450DX-24T1/CM450DXP-24T1

HIGH POWER SWITCHING USE

INSULATED TYPE



- •Flat base type
- Copper base plate (Nickel-plating)
- •RoHS Directive compliant
- •Tin-plating pin terminals



- Flat base type
- Copper base plate (Nickel-plating)
- •RoHS Directive compliant
- Tin-plating pressfit terminals
- •UL Recognized under UL1557, File No. E323585

APPLICATION

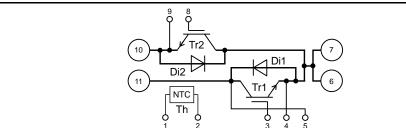
AC Motor Control, Motion/Servo Control, Power supply, etc.

dual switch (half-bridge)

OPTION (Below options are available.)

- •PC-TIM (Phase Change Thermal Interface Material) pre-apply
- •V_{CEsat} selection for parallel connection

INTERNAL CONNECTION



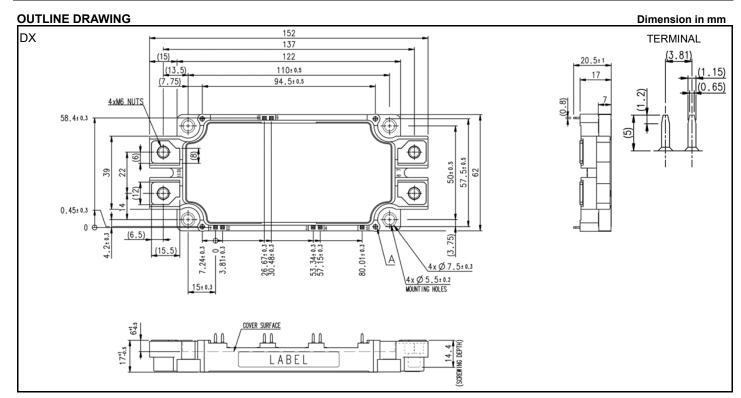
TERMINAL CODE

1. TH1 6. C2E1 2. TH2 7. C2E1 3. G1 8. G2 4. Es1 9. Es2 5. Cs1 10. E2 11. C1

OUTLINE DRAWING COM. (6.5) (97) MOUNTING HOLES SECTION A (Ø2.6) (Ø2.32)

HIGH POWER SWITCHING USE

INSULATED TYPE

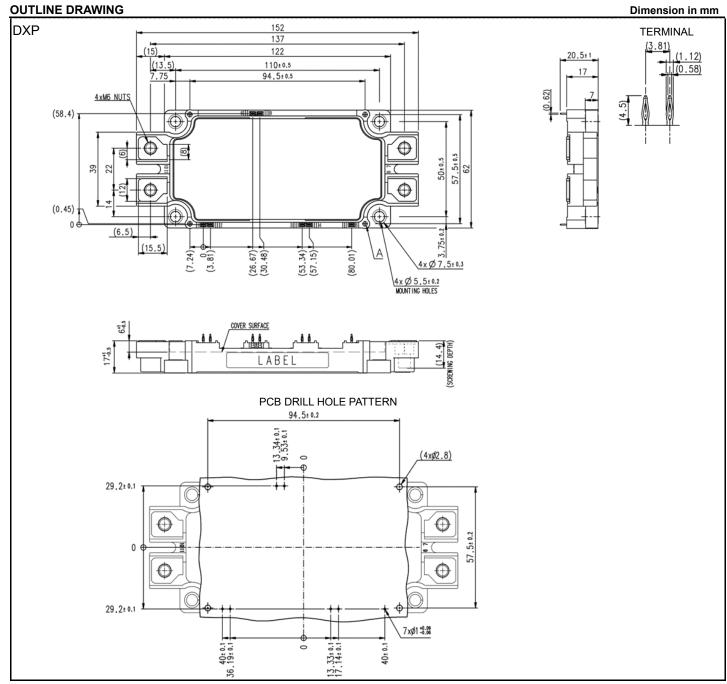


Tolerance otherwise specified

Division	n of I	Tolerance					
	0.5	to	3	±0.2			
over 3		to	6	±0.3			
over	6	to	30	±0.5			
over	30	to '	120	±0.8			
over 120		to 4	400	±1.2			

HIGH POWER SWITCHING USE

INSULATED TYPE



Tolerance otherwise specified

	Divisio	n of I	Tolerance		
		0.5	to	3	±0.2
	over 3		to	6	±0.3
	over	6	to	30	±0.5
	over 30 over 120		to 120		±0.8
			to 4	400	±1.2

HIGH POWER SWITCHING USE

INSULATED TYPE

MAXIMUM RATINGS (Tvj=25 °C, unless otherwise specified)

INVERTER PART IGBT/FWD

Symbol	Item	Conditions	Rating	Unit	
V _{CES}	Collector-emitter voltage	G-E short-circuited	1200	V	
V_{GES}	Gate-emitter voltage	C-E short-circuited	± 20	V	
Ic	Callagton augment	DC, T _C =70 °C (Note2, 4)		^	
I _{CRM}	Collector current	Pulse, Repetitive (Note3)	900	A	
P _{tot}	Total power dissipation	Tc=25 °C (Note2, 4)	1700	W	
I _E (Note1)	Fuelthan assument	DC (Note2)	450	^	
I _{ERM} (Note1)	Emitter current	Pulse, Repetitive (Note3)	900	Α	

MODULE

Symbol	Item	Conditions	Rating	Unit
Visol	Isolation voltage	Terminals to base plate, RMS, f=60 Hz, AC 1 min	2500	V
T _{vjmax}	Maximum junction temperature	Instantaneous event (overload) (Note9)	175	°C
T _{Cmax}	Maximum case temperature	(Note4, 9)	125	C
T _{vjop}	Operating junction temperature	Continuous operation (under switching) (Note9)	-40 ~ +150	°C
T _{sta}	Storage temperature	-	-40 ~ +125	C

ELECTRICAL CHARACTERISTICS (T $_{vj}$ =25 °C, unless otherwise specified)

INVERTER PART IGBT/FWD

Cumbal	Itam	Conditions			Limits		Unit
Symbol	Item			Min.	Тур.	Max.	
I _{CES}	Collector-emitter cut-off current	V _{CE} =V _{CES} , G-E short-circuited	V _{CE} =V _{CES} , G-E short-circuited		-	1.0	mA
I _{GES}	Gate-emitter leakage current	V _{GE} =V _{GES} , C-E short-circuited		-	-	0.5	μΑ
$V_{GE(th)}$	Gate-emitter threshold voltage	I _C =45 mA, V _{CE} =10 V		5.4	6.0	6.6	V
.,		I _C =450 A, V _{GE} =15 V,	T _{vj} =25 °C	-	1.9	2.3	
V _{CEsat} (Terminal)		Refer to the figure of test circuit	T _{vj} =125 °C	-	2.2	-	V
(Terminal)	0-11	(Note5)	T _{vj} =150 °C	-	2.3	-	
	Collector-emitter saturation voltage	I _C =450 A,	T _{vj} =25 °C	-	1.8	2.1	
V _{CEsat}		V _{GE} =15 V,	T _{vj} =125 °C	-	2.1	-	V
(Chip)		(Note5)	T _{vj} =150 °C	-	2.2	-	
Cies	Input capacitance			-	-	72.8	
Coes	Output capacitance	V _{CE} =10 V, G-E short-circuited	V _{CE} =10 V, G-E short-circuited		-	2.1	nF
Cres	Reverse transfer capacitance			-	-	0.9	
Q _G	Gate charge	V _{CC} =600 V, I _C =450 A, V _{GE} =15 V		-	2.26	-	μC
t _{d(on)}	Turn-on delay time	V _{cc} =600 V, I _c =450 A, V _{GE} =±15 V, R _G =1.3 Ω, Inductive load		-	-	600	ns
tr	Rise time			-	-	200	
t _{d(off)}	Turn-off delay time			-	-	800	
t _f	Fall time			-	-	400	
(NI=4=4)		I _E =450 A, G-E short-circuited,	T _{vj} =25 °C	-	1.90	2.35	V
V _{EC} (Note1)		Refer to the figure of test circuit	T _{vj} =125 °C	-	2.00	-	
(Terminal)	Fusitten celle stem velte ne	(Note5)	T _{vj} =150 °C	-	2.05	-	
No. (Noted)	- Emitter-collector voltage	I _E =450 A,	T _{vj} =25 °C	-	1.80	2.15	
V _{EC} (Note1) (Chip)		G-E short-circuited,	T _{vj} =125 °C	-	1.85	-	
(Criip)		(Note5)	T _{vj} =150 °C	-	1.85	-	
t _{rr} (Note1)	Reverse recovery time	V _{CC} =600 V, I _E =450 A, V _{GE} =±15 V,	V _{CC} =600 V, I _E =450 A, V _{GE} =±15 V,		-	400	ns
Q _{rr} (Note1)	Reverse recovery charge	R_G =1.3 Ω , Inductive load	R_G =1.3 $Ω$, Inductive load		35.1	-	μC
Eon	Turn-on switching energy per pulse	V _{CC} =600 V, I _C =I _E =450 A,		-	56.5	-	
E _{off}	Turn-off switching energy per pulse	V _{GE} =±15 V, R _G =1.3 Ω, T _{vj} =150 °C,		-	42.0	-	mJ
E _{rr} (Note1)	Reverse recovery energy per pulse	Inductive load		-	23.0	-	mJ
R _{CC'+EE'}	Internal lead resistance	Main terminals-chip, per switch, T _C =2	5 °C (Note4)	-	0.88	-	mΩ
r _g	Internal gate resistance	Per switch		-	1.0	-	Ω

HIGH POWER SWITCHING USE

INSULATED TYPE

ELECTRICAL CHARACTERISTICS (cont.; Tvj=25 °C, unless otherwise specified)

NTC THERMISTOR PART

Symbol	Item	Conditions		Unit		
	item	Conditions	Min.	Тур.	Max.	Offic
R ₂₅	Zero-power resistance	T _C =25 °C (Note4)	4.85	5.00	5.15	kΩ
ΔR/R	Deviation of resistance	R ₁₀₀ =493 Ω, T _C =100 °C (Note4)	-7.3	-	+7.8	%
B _(25/50)	B-constant	Approximate by equation (Note6)	-	3375	-	K
P ₂₅	Power dissipation	T _C =25 °C (Note4)	-	-	10	mW

THERMAL RESISTANCE CHARACTERISTICS

Symbol	Itom	Conditions		Unit		
Symbol	Item	Conditions		Тур.	Max.	Onit
$R_{th(j-c)Q}$	Thermal resistance	Junction to case, per Inverter IGBT (Note4)	-	-	88	K/kW
$R_{th(j-c)D}$	Thermal resistance	Junction to case, per Inverter FWD (Note4)	-	1	115	N/KVV
R _{th(c-s)}	Contact thermal resistance	Case to heat sink, per 1 module, Thermal grease applied (Note4, 7, 9)	-	11.5	-	K/kW

MECHANICAL CHARACTERISTICS

Cumbal	Item	Con	Limits			Unit	
Symbol	item	Con	ditions	Min.	Тур.	Max.	Offic
M _t	Mounting torque	Main terminals	M 6 screw	3.5	4.0	4.5	N·m
Ms	Mounting torque	Mounting to heat sink	M 5 screw	2.5	3.0	3.5	N·m
		Caldennia tura (DV)	Terminal to terminal	17	-	-	mm
al .	Creepage distance	Solder pin type (DX)	Terminal to base plate	16.4	-	-	
ds		Duran Stanio America (DVD)	Terminal to terminal	17	-	-	mm
		Pressfit pin type (DXP)	Terminal to base plate	16.8	-	-	
		Caldennia tura (DV)	Terminal to terminal	10	-	-	mm
al .	Classes	Solder pin type (DX)	Terminal to base plate	16.2	-	-	
d _a	Clearance	Donas fit win town (DVD)	Terminal to terminal	10	-	-	
		Pressfit pin type (DXP) Terminal to base plate		16.2		-	mm
ec	Flatness of base plate	On the centerline X, Y	Note8)	±0	-	+200	μm
m	mass	-		-	300	-	g

^{*.} This product is compliant with the Restriction of the Use of Certain Hazardous Substances in Electrical and Electronic Equipment (RoHS) directive 2011/65/EU and (EU) 2015/863.

Note1. Represent ratings and characteristics of the anti-parallel, emitter-collector free-wheeling diode (FWD).

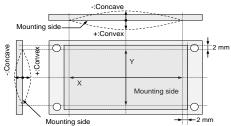
- 2. Junction temperature (T $_{\nu\,j}$) should not increase beyond T $_{\nu\,j\,m\,a\,x}$ rating.
- 3. Pulse width and repetition rate should be such that the device junction temperature (Tvj) dose not exceed Tvjmax rating.
- 4. Case temperature (T_C) and heat sink temperature (T_S) are defined on the each surface (mounting side) of base plate and heat sink just under the chips. Refer to the figure of chip location.
- 5. Pulse width and repetition rate should be such as to cause negligible temperature rise. Refer to the figure of test circuit.

6.
$$B_{(25/50)} = ln(\frac{R_{25}}{R_{50}})/(\frac{1}{T_{25}} - \frac{1}{T_{50}})$$

 $R_{25}\!:$ resistance at absolute temperature T_{25} [K]; $T_{25}\!=\!25$ [°C]+273.15=298.15 [K]

 R_{50} : resistance at absolute temperature T_{50} [K]; T_{50} =50 [°C]+273.15=323.15 [K]

- 7. Reference value. Thermally conductive grease of thermal conductivity λ =0.9 W/(m·K) and thickness D_(C-S)=50 μ m.
- 8. The base plate (mounting side) flatness measurement points (X, Y) are shown in the following figure.



9. Long term performance related to thermal conductive grease (including but not limited to aspects such as the increase of thermal resistance due to pumping out, etc.) should be verified under user's specific application conditions. Each temperature condition (T_{vj max}, T_{vj op}, T_{C max}) must be maintained below the maximum rated temperature throughout consideration of the temperature rise even for long term usage.

HIGH POWER SWITCHING USE

INSULATED TYPE

Note10. Use the following screws when mounting the printed circuit board (PCB) on the standoffs.

PCB thickness: t1.6

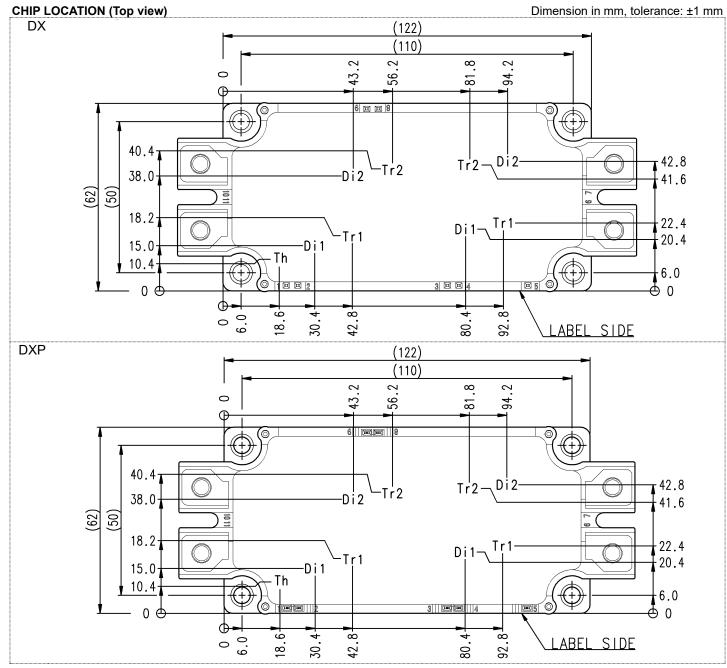
	Туре	Manufacturer	Size	Tightening torque (N•m)	Recommended tightening method
(1)	PT®	EJOT	K25×8	0.55 ± 0.055	
(2)	PT®		K25×10	0.75 ± 0.075 N·m	by handwork (equivalent to 30 r/min
(3)	DELTA PT®		25×8	0.55 ± 0.055 N·m	by mechanical screw driver)
(4)	DELTA PT®		25×10	0.75 ± 0.075 N·m	~ 600 r/min (by mechanical screw driver)
(5)	B1	-	φ2.6×10	0.75 ± 0.075 N·m	
	tapping screw		φ2.6×12	0.75 ± 0.075 N•III	

RECOMMENDED OPERATING CONDITIONS

Symbol	Item	Conditions		Unit		
	item	Conditions	Min.	Тур.	Max.	Offic
V _{cc}	(DC) Supply voltage	Applied across C1-E2 terminals	-	600	850	V
V_{GEon}	Gate (-emitter drive) voltage	Applied across G1-E1s/G2-E2s terminals	13.5	15.0	16.5	V
R_G	External gate resistance	Per switch	1.3	-	10	Ω

HIGH POWER SWITCHING USE

INSULATED TYPE

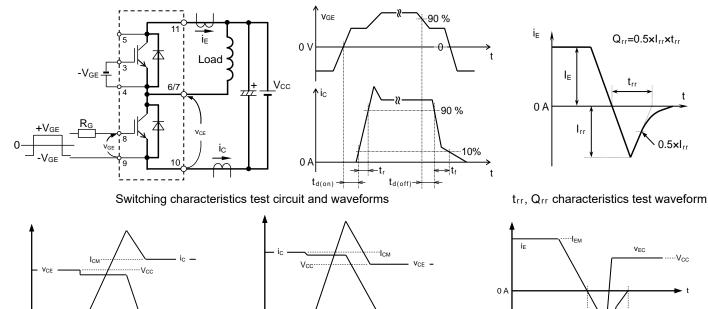


Tr1/Tr2: IGBT, Di1/Di2: FWD, Th: NTC thermistor

HIGH POWER SWITCHING USE

INSULATED TYPE

TEST CIRCUIT AND WAVEFORMS



IGBT Turn-on switching energy

IGBT Turn-off switching energy

0.02×I_{CM}

0 V

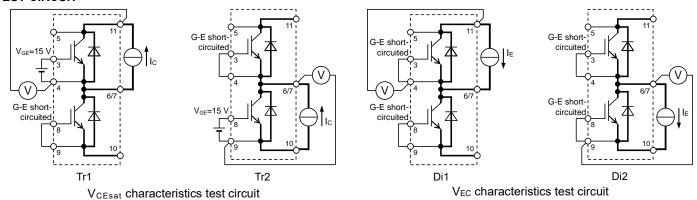
0.1×Vcc

t_i
FWD Reverse recovery energy

Switching energy and Reverse recovery energy test waveforms (Integral time instruction drawing)

TEST CIRCUIT

0.1×Ich



HIGH POWER SWITCHING USE

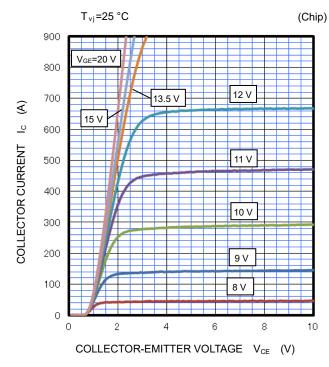
INSULATED TYPE

PERFORMANCE CURVES

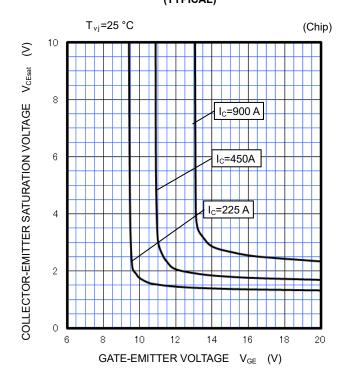
INVERTER PART

OUTPUT CHARACTERISTICS

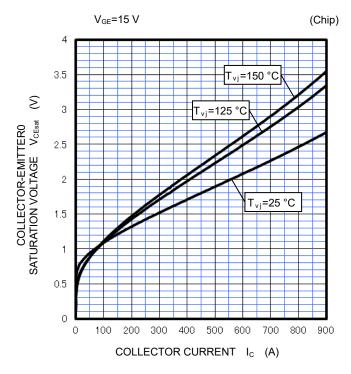
(TYPICAL)



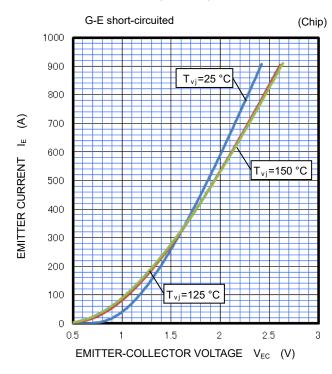
COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)



COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)



FREE WHEELING DIODE FORWARD CHARACTERISTICS (TYPICAL)



HIGH POWER SWITCHING USE

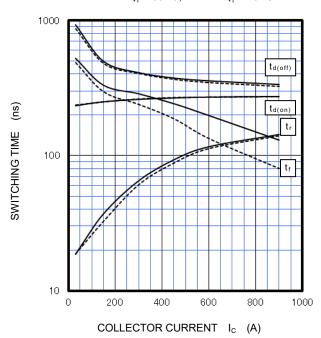
INSULATED TYPE

PERFORMANCE CURVES

INVERTER PART

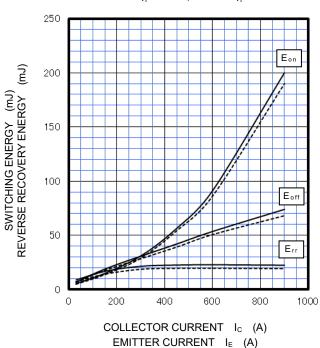
HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

 V_{CC} =600 V, V_{GE} =±15 V, R_{G} =1.3 Ω , INDUCTIVE LOAD: T_{vi} =150 °C, - - - - : T_{vi} =125 °C



HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

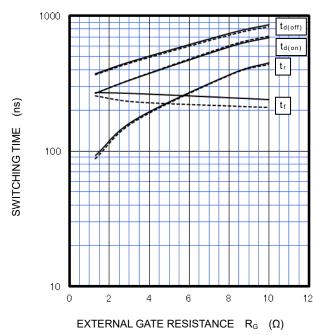
 V_{CC} =600 V, V_{GE} =±15 V, R_{G} =1.3 Ω , INDUCTIVE LOAD, PER PULSE: T_{v_1} =150 °C, - - - - : T_{v_1} =125 °C



HALF-BRIDGE SWITCHING CHARACTERISTICS

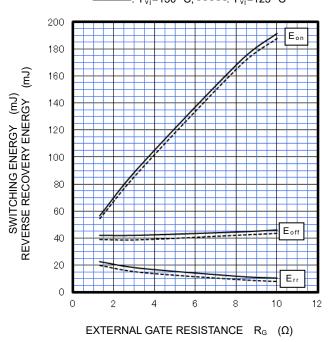
(TYPICAL)

 $\begin{array}{lll} V_{\text{CC}}\text{=}600 \text{ V}, \, V_{\text{GE}}\text{=}\pm15 \text{ V}, \, I_{\text{C}}\text{=}450 \text{ A}, \, \text{INDUCTIVE LOAD} \\ & -----: T_{v_{\text{I}}}\text{=}155 \, ^{\circ}\text{C}, \, ----: T_{v_{\text{I}}}\text{=}125 \, ^{\circ}\text{C} \end{array}$



HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

 V_{CC} =600 V, V_{GE} =±15 V, I_{C}/I_{E} =450 A, INDUCTIVE LOAD, PER PULSE -----: T_{vi} =150 °C, - - - - : T_{vi} =125 °C



HIGH POWER SWITCHING USE

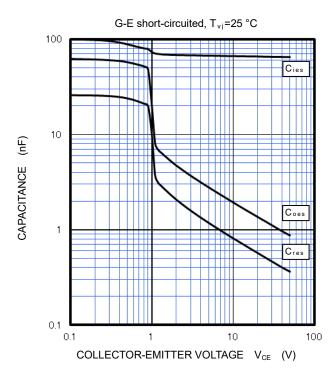
INSULATED TYPE

PERFORMANCE CURVES

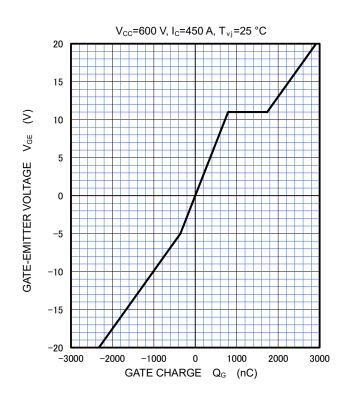
INVERTER PART

CAPACITANCE CHARACTERISTICS

(TYPICAL)

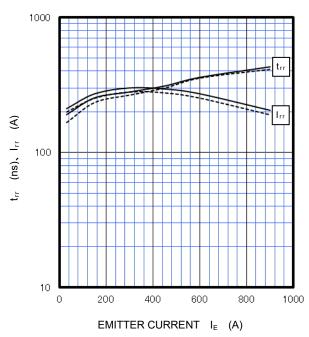


GATE CHARGE CHARACTERISTICS (TYPICAL)



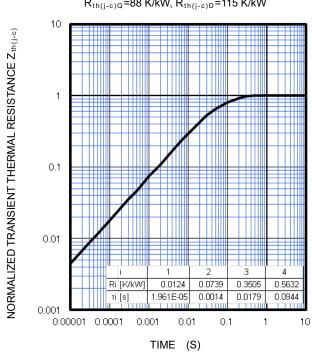
FREE WHEELING DIODE REVERSE RECOVERY CHARACTERISTICS (TYPICAL)

V_{CC}=600 V, V_{GE}=±15 V, R_G=1.3 Ω, INDUCTIVE LOAD
-----: T_{vj}=150 °C, - - - - : T_{vj}=125 °C



TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS (MAXIMUM)

Single pulse, T_C=25 °C $R_{th(j-c)Q}$ =88 K/kW, $R_{th(j-c)D}$ =115 K/kW



HIGH POWER SWITCHING USE

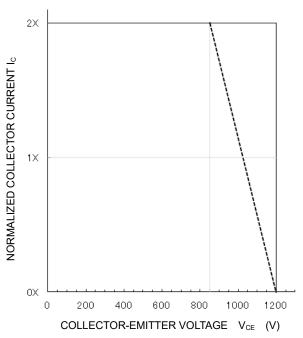
INSULATED TYPE

PERFORMANCE CURVES

INVERTER PART

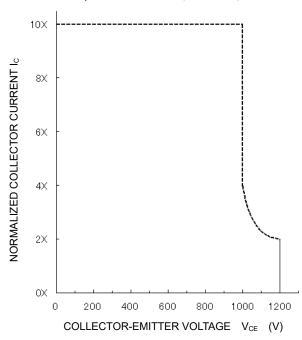
TURN-OFF SWITCHING SAFE OPERATING AREA (REVERSE BIAS SAFE OPERATING AREA) (MAXIMUM)

 $V_{\text{CC}} \le 850 \text{ V}$, $R_{\text{G}} = 1.3 \sim 10 \Omega$, $V_{\text{GE}} = \pm 15 \text{ V}$,: $T_{\text{V}_{\text{I}}} = 25 \sim 150 \,^{\circ}\text{C}$ (Normal load operations (Continuous): $T_{\text{V}_{\text{J}}} = 175 \,^{\circ}\text{C}$ (Unusual load operations (Limited period)



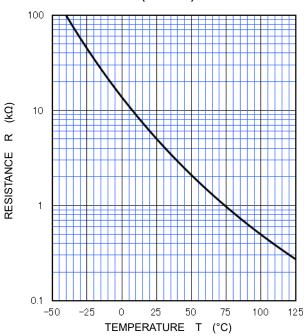
SHORT-CIRCUIT SAFE OPERATING AREA (MAXIMUM)

 $V_{CC} \le 800 \text{ V}$, $R_G = 1.3 \sim 10 \Omega$, $V_{GE} = \pm 15 \text{ V}$, $T_{vj} = 25 \sim 150 \text{ °C}$, $t_W \le 8 \mu \text{s}$, Non-Repetitive



NTC thermistor part

TEMPERATURE CHARACTERISTICS (TYPICAL)



Note: The characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

HIGH POWER SWITCHING USE INSULATED TYPE

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HIGH POWER SWITCHING USE INSULATED TYPE

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