

<IGBT Modules>

CM200TX-13T/CM200TXP-13T

HIGH POWER SWITCHING USE

INSULATED TYPE



- Flat base type
- •Copper base plate (Nickel-plating)
- •RoHS Directive compliant
- Tin-plating pin terminals



- •Flat base type
- Copper base plate (Nickel-plating)
- •RoHS Directive compliant
- Tin-plating pressfit terminals
- •UL Recognized under UL1557, File No. E323585

APPLICATION

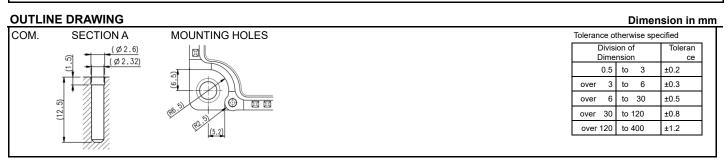
AC Motor Control, Motion/Servo Control, Power supply, etc.

sixpack (three-phase bridge)

OPTION (Below options are available.)

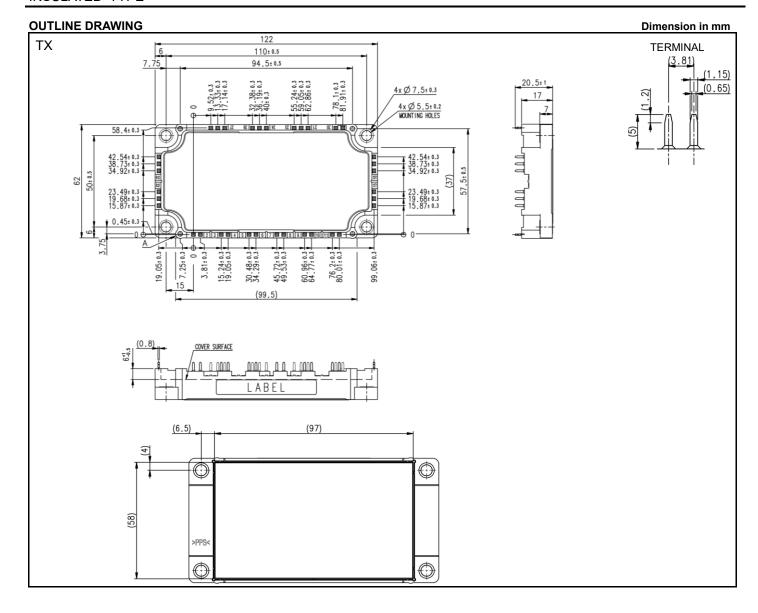
•PC-TIM (Phase Change Thermal Interface Material) pre-apply

INTERNAL CONNECTION **Terminal code** 1 GUP 13 N1 24 V 30~32 2 EUP 14 N1 25 V 16~18 3 GUN 15 N1 26 V 4 EUN 16 P1 27 U 5 GVP 17 P1 28 U 29 U 6 EVP 18 P1 7 GVN 19 TH1 30 P 8 EVN 20 TH2 31 P 9 GWP 21 W 32 P 10 EWP 33 N 22 W 11 GWN 34 N 12 EWN 35 N 13~15 33~35



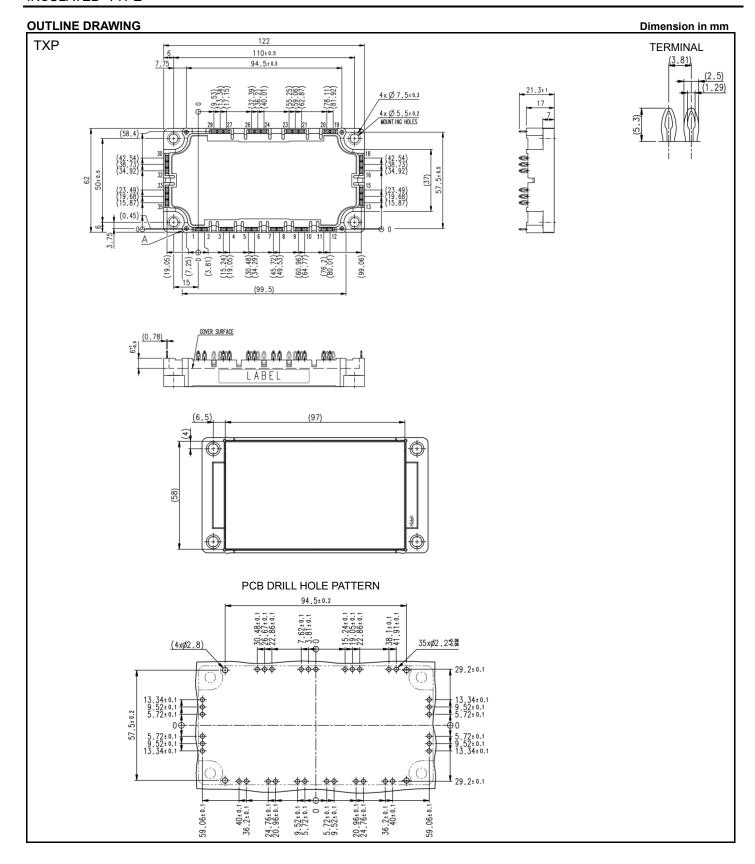
HIGH POWER SWITCHING USE

INSULATED TYPE



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HIGH POWER SWITCHING USE

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MAXIMUM RATINGS (Tvj=25 °C, unless otherwise specified)

INVERTER PART IGBT/FWD

Symbol	Item	Conditions	Rating	Unit	
V _{CES}	Collector-emitter voltage	G-E short-circuited	650	V	
V _{GES}	Gate-emitter voltage	C-E short-circuited	± 20	V	
Ic	Callagton accompant	DC, T _C =101 °C (Note2, 4)	200		
I _{CRM}	Collector current	Pulse, Repetitive (Note3)	400	A	
P _{tot}	Total power dissipation	Tc=25 °C (Note2, 4)	690	W	
I _E (Note1)	Fueither exposed	DC (Note2)	200	^	
I _{ERM} (Note1)	Emitter current	Pulse, Repetitive (Note3)	400	A	

MODULE

Symbol	Item	Conditions	Rating	Unit
Visol	Isolation voltage	Terminals to base plate, RMS, f=60 Hz, AC 1 min	2500	V
T _{vjmax}	Maximum junction temperature	Instantaneous event (overload) (Note9)	175	°C
T _{Cmax}	Maximum case temperature	(Note4, 9)	125	C
T _{vjop}	Operating junction temperature	Continuous operation (under switching) (Note9)	-40 ~ +150	°C
T _{sta}	Storage temperature	-	-40 ~ +125	C

ELECTRICAL CHARACTERISTICS (T $_{vj}$ =25 °C, unless otherwise specified)

INVERTER PART IGBT/FWD

Symbol Itam		Conditions			Limits		Unit
Symbol	Item	Conditions		Min.	Тур.	Max.	Unit
I _{CES}	Collector-emitter cut-off current	V _{CE} =V _{CES} , G-E short-circuited	-	-	1.0	mA	
I _{GES}	Gate-emitter leakage current	V _{GE} =V _{GES} , C-E short-circuited		-	-	0.5	μΑ
$V_{GE(th)}$	Gate-emitter threshold voltage	I _C =20 mA, V _{CE} =10 V		5.4	6.0	6.6	V
		I _C =200 A, V _{GE} =15 V,	T _{vj} =25 °C	-	1.40	1.75	V
V _{CEsat} (Terminal)		Refer to the figure of test circuit	T _{vj} =125 °C	-	1.50	-	
(Terminal)	0-11	(Note5)	T _{vj} =150 °C	-	1.55	-	
	Collector-emitter saturation voltage	I _C =200 A,	T _{vj} =25 °C	-	1.30	1.55	
V _{CEsat}		V _{GE} =15 V,	T _{vj} =125 °C	-	1.35	-	V
(Chip)		(Note5)	T _{vj} =150 °C	-	1.35	-	
Cies	Input capacitance		•	-	-	26.7	
Coes	Output capacitance	V _{CE} =10 V, G-E short-circuited		-	-	1.1	nF
Cres	Reverse transfer capacitance			-	-	0.5	1
Q _G	Gate charge	V _{CC} =300 V, I _C =200 A, V _{GE} =15 V		-	0.83	-	μC
t _{d(on)}	Turn-on delay time	V _{cc} =300 V, I _c =200 A, V _{GE} =±15 V, R _G =0.47 Ω, Inductive load		-	-	400	
tr	Rise time			-	-	200	ns
t _{d(off)}	Turn-off delay time			-	-	400	
t _f	Fall time			-	-	600	
		I _E =200 A, G-E short-circuited,	T _{vj} =25 °C	-	1.50	2.05	
V _{EC} (Note1)		Refer to the figure of test circuit	T _{vj} =125 °C	-	1.55	-	V
(Terminal)	For itten and the other continues	(Note5)	T _{vj} =150 °C	-	1.55	-	
(Noted)	Emitter-collector voltage	I _E =200 A,	T _{vj} =25 °C	-	1.45	1.85	
V _{EC} (Note1)		G-E short-circuited,	T _{vj} =125 °C	-	1.50	-	V
(Chip)		(Note5)	T _{vj} =150 °C	-	1.50	-	
t _{rr} (Note1)	Reverse recovery time	V _{CC} =300 V, I _E =200 A, V _{GE} =±15 V,		-	-	400	ns
Q _{rr} (Note1)	Reverse recovery charge	R_G =0.47 Ω , Inductive load		-	14	-	μC
Eon	Turn-on switching energy per pulse	V _{CC} =300 V, I _C =I _E =200 A,			3.9	-	ma I
E _{off}	Turn-off switching energy per pulse	V_{GE} =±15 V, R _G =0.47 Ω , T _{vj} =150 °C,		-	11.6	-	mJ
E _{rr} (Note1)	Reverse recovery energy per pulse	Inductive load			10.1	-	mJ
R _{CC'+EE'}	Internal lead resistance	Main terminals-chip, per switch, T _C =25	o °C (Note4)	-	1.4	-	mΩ
r _g	Internal gate resistance	Per switch		-	3.0	-	Ω

HIGH POWER SWITCHING USE

INSULATED TYPE

ELECTRICAL CHARACTERISTICS (cont.; Tvj=25 °C, unless otherwise specified)

NTC THERMISTOR PART

Symbol	Itama	Conditions		Limits		Unit
	Item	Conditions	Min.	Тур.	Max.	Onit
R ₂₅	Zero-power resistance	T _C =25 °C (Note4)	4.85	5.00	5.15	kΩ
ΔR/R	Deviation of resistance R ₁₀₀ =493 Ω, T _C =100 °C (Note4)		-7.3	-	+7.8	%
B _(25/50)	B-constant Approximate by equation (Note6)		-	3375	-	K
P ₂₅	Power dissipation	T _C =25 °C (Note4)	-	-	10	mW

THERMAL RESISTANCE CHARACTERISTICS

Symbol Item	Itama	Conditions		Limits		Unit
	item	Conditions		Тур.	Max.	Offic
$R_{th(j-c)Q}$	Thermal resistance	Junction to case, per Inverter IGBT (Note4)	-	-	216	K/kW
$R_{th(j-c)D}$	Thermal resistance	Junction to case, per Inverter FWD (Note4)	-	-	333	r/KVV
R _{th(c-s)}	Contact thermal resistance	Case to heat sink, per 1 module, Thermal grease applied (Note4, 7, 10)	-	11.5	-	K/kW

MECHANICAL CHARACTERISTICS

Cumbal	Itom	Con	ditions		Limits		Unit
Symbol	Item	Con	uitions	Min.	Тур.	Max.	Unit
Ms	Mounting torque	Mounting to heat sink	M 5 screw	2.5	3.0	3.5	N·m
ds		Colder pip type (TV)	Terminal to terminal	16.4	-	-	mm
	Consequent distance	Solder pin type (TX)	Terminal to base plate	18.5	-	-	mm
	Creepage distance	Dragofit nin tuna (TVD)	Terminal to terminal	19	-	-	mm
		Pressfit pin type (TXP)	Terminal to base plate	18.6	-	-	
		Colder pip type (TV)	Terminal to terminal	10.2	-	-	
	Classes	Solder pin type (TX)	Terminal to base plate	9.0	-	-	mm
da	Clearance	Descrit win town (TVD)	Terminal to terminal	8.9	-	-	
		Pressfit pin type (TXP)	Terminal to base plate	9.0	-	-	mm
ec	Flatness of base plate	On the centerline X, Y (Note8)		±0	-	+200	μm
m	mass	-		-	270	-	g

^{*.} This product is compliant with the Restriction of the Use of Certain Hazardous Substances in Electrical and Electronic Equipment (RoHS) directive 2011/65/EU and (EU) 2015/863.

Note1. Represent ratings and characteristics of the anti-parallel, emitter-collector free-wheeling diode (FWD).

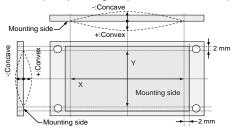
- 2. Junction temperature $(T_{\nu j})$ should not increase beyond $T_{\nu j\,m\,a\,x}$ rating.
- 3. Pulse width and repetition rate should be such that the device junction temperature (Tvj) dose not exceed Tvjmax rating.
- 4. Case temperature (T_C) and heat sink temperature (T_S) are defined on the each surface (mounting side) of base plate and heat sink just under the chips. Refer to the figure of chip location.
- 5. Pulse width and repetition rate should be such as to cause negligible temperature rise. Refer to the figure of test circuit.

6.
$$B_{(25/50)} = ln(\frac{R_{25}}{R_{50}})/(\frac{1}{T_{25}} - \frac{1}{T_{50}})$$

R₂₅: resistance at absolute temperature T₂₅ [K]; T₂₅=25 [°C]+273.15=298.15 [K]

 R_{50} : resistance at absolute temperature T_{50} [K]; T_{50} =50 [°C]+273.15=323.15 [K]

- 7. Reference value. Thermally conductive grease of thermal conductivity λ =0.9 W/(m·K) and thickness D_(C-S)=50 μ m.
- 8. The base plate (mounting side) flatness measurement points (X, Y) are shown in the following figure.



9. Long term performance related to thermal conductive grease (including but not limited to aspects such as the increase of thermal resistance due to pumping out, etc.) should be verified under user's specific application conditions. Each temperature condition (T_{vj max}, T_{vj op}, T_{C max}) must be maintained below the maximum rated temperature throughout consideration of the temperature rise even for long term usage.

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Note10. Use the following screws when mounting the printed circuit board (PCB) on the standoffs.

PCB thickness : t=1.6

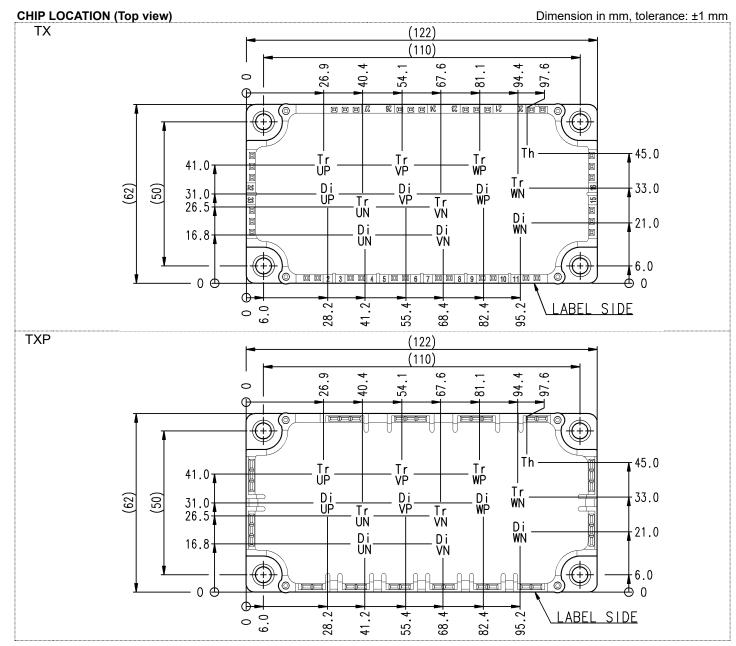
	Туре	Manufacturer	Size	Tightening torque (N·m)	Recommended tightening method
(1)	PT®	EJOT	K25×8	0.55 ± 0.055	
(2)	PT®		K25×10	0.75 ± 0.075 N·m	by handwork (equivalent to 30 rpm
(3)	DELTA PT®		25×8	0.55 ± 0.055 N·m	by mechanical screw driver)
(4)	DELTA PT®		25×10	0.75 ± 0.075 N·m	~ 600 rpm (by mechanical screw driver)
(5)	B1	-	φ2.6×10	0.75 ± 0.075 N·m	
	tapping screw		φ2.6×12	0.73 ± 0.073 N•III	

RECOMMENDED OPERATING CONDITIONS

Symbol	Itama	Conditions Limits Min. Typ. M			Unit	
	Item			Max.	Offic	
V _{CC}	(DC) Supply voltage	Applied across P-N terminals		300	450	V
V_{GEon}	Gate (-emitter drive) voltage	Applied across G*P-E*P/G*N-E*N terminals (*=U,V,W)		15.0	16.5	V
R _G	External gate resistance	Per switch	0.47	-	30	Ω

HIGH POWER SWITCHING USE

INSULATED TYPE



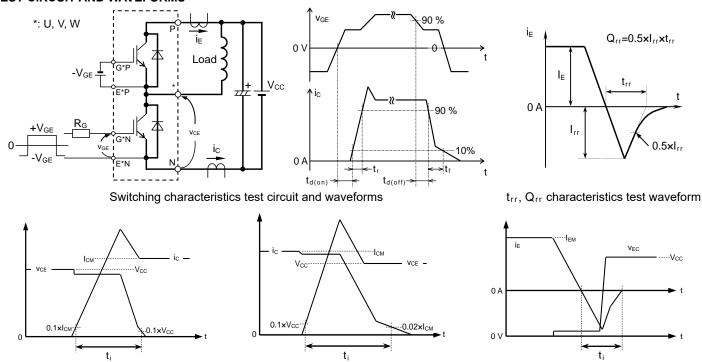
Tr*P/Tr*N: IGBT, Di*P/Di*N: FWD (*=U,V,W), Th: NTC thermistor

HIGH POWER SWITCHING USE

INSULATED TYPE



IGBT Turn-on switching energy



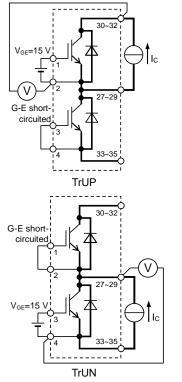
IGBT Turn-off switching energy Switching energy and Reverse recovery energy test waveforms (Integral time instruction drawing)

FWD Reverse recovery energy

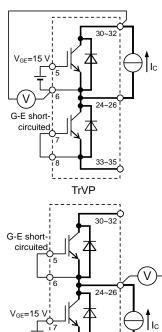
HIGH POWER SWITCHING USE

INSULATED TYPE

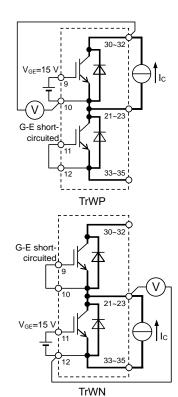
TEST CIRCUIT



Gate-emitter GVP-EVP, GVN-EVN, short-circuited GWP-EWP, GWN-EWN

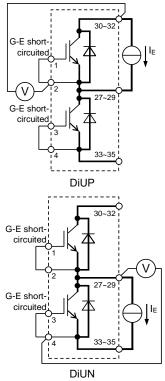


TrVN
Gate-emitter GUP-EUP, GUN-EUN, short-circuited GWP-EWP, GWN-EWN

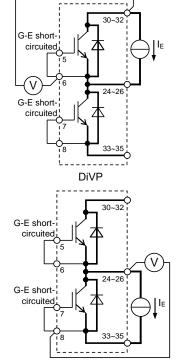


Gate-emitter GUP-EUP, GUN-EUN, short-circuited GVP-EVP, GVN-EVN

V_{CEsat} characteristics test circuit

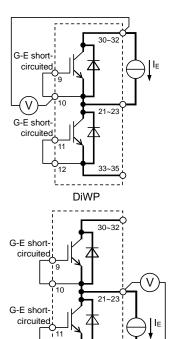


Gate-emitter GVP-EVP, GVN-EVN, short-circuited GWP-EWP, GWN-EWN



DiVN
Gate-emitter GUP-EUP, GUN-EUN, short-circuited GWP-EWP, GWN-EWN

 V_{EC} characteristics test circuit



DiWNGate-emitter GUP-EUP, GUN-EUN, short-circuited GVP-EVP, GVN-EVN

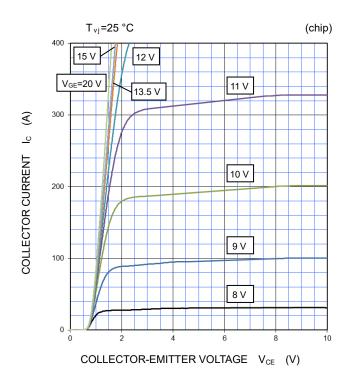
HIGH POWER SWITCHING USE

INSULATED TYPE

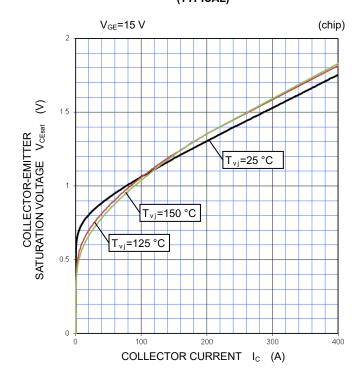
PERFORMANCE CURVES

INVERTER PART

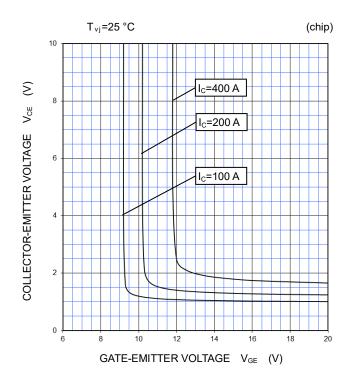
OUTPUT CHARACTERISTICS (TYPICAL)



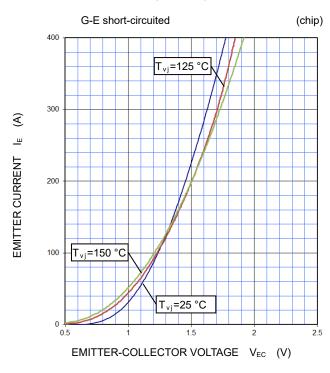
COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)



COLLECTOR-EMITTER VOLTAGE CHARACTERISTICS (TYPICAL)



FREE WHEELING DIODE FORWARD CHARACTERISTICS (TYPICAL)



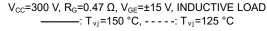
HIGH POWER SWITCHING USE

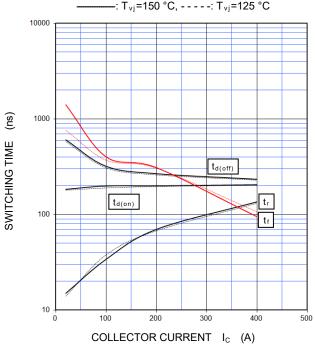
INSULATED TYPE

PERFORMANCE CURVES

INVERTER PART

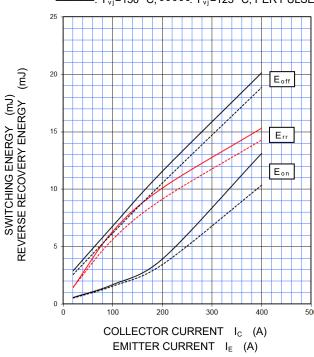
HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)





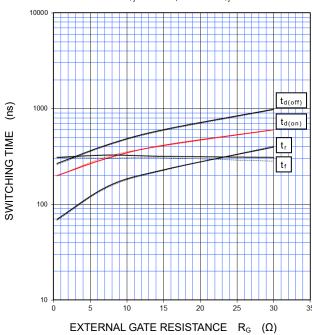
HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

 V_{CC} =300 V, R_G =0.47 Ω , V_{GE} =±15 V, INDUCTIVE LOAD, -: T_{vj}=150 °C, - - - - -: T_{vj}=125 °C, PER PULSE



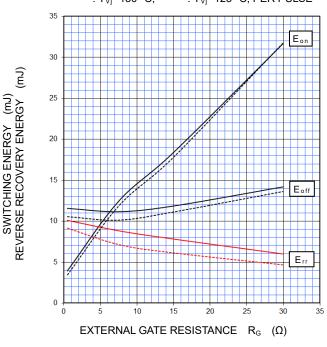
HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

 V_{CC} =300 V, I_{C} =200 A, V_{GE} =±15 V, INDUCTIVE LOAD -: T_{vj}=150 °C, - - - -: T_{vj}=125 °C



HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

 V_{CC} =300 V, I_C/I_E =200 A, V_{GE} =±15 V, INDUCTIVE LOAD, -: T_{vj}=150 °C, - - - - -: T_{vj}=125 °C, PER PULSE



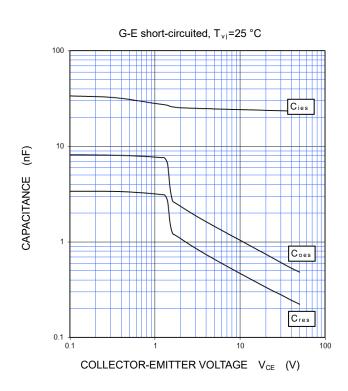
HIGH POWER SWITCHING USE

INSULATED TYPE

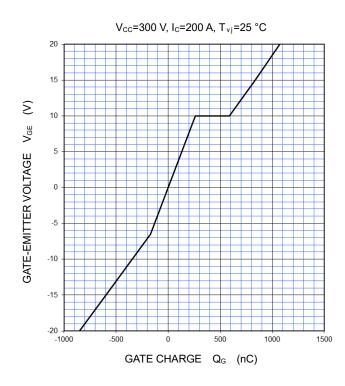
PERFORMANCE CURVES

INVERTER PART

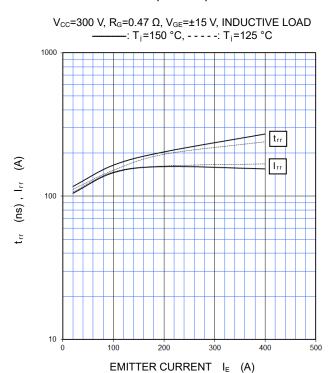
CAPACITANCE CHARACTERISTICS (TYPICAL)



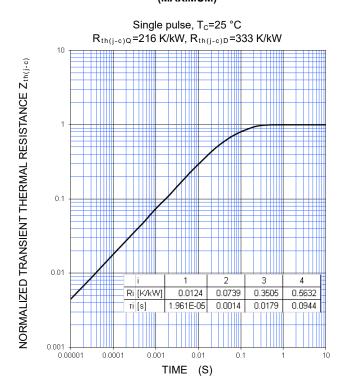
GATE CHARGE CHARACTERISTICS (TYPICAL)



FREE WHEELING DIODE REVERSE RECOVERY CHARACTERISTICS (TYPICAL)



TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS (MAXIMUM)



HIGH POWER SWITCHING USE

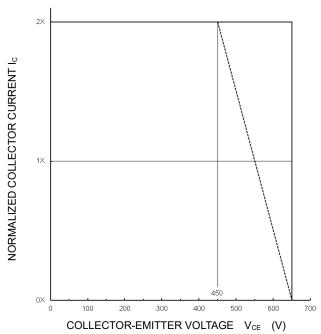
INSULATED TYPE

PERFORMANCE CURVES

INVERTER PART

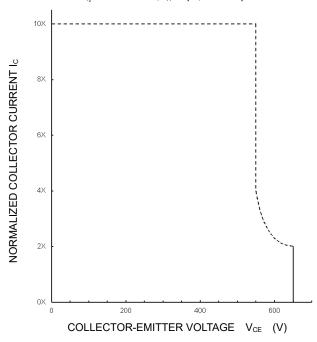
TURN-OFF SWITCHING SAFE OPERATIONG AREA (REVERSE BIAS SAFE OPERATING AREA) (MAXIMUM)

 $V_{\text{CC}} \!\! \leq \!\! 450 \text{ V, } R_{\text{G}} \!\! = \!\! 0.47 \!\! \sim \!\! 30 \text{ } \Omega, V_{\text{GE}} \!\! = \!\! \pm \!\! 15 \text{ V,} \\ ------: T_{\text{V}_{\text{J}}} \!\! = \!\! 25 \!\! \sim \!\! 150 \, ^{\circ}\text{C (Normal load operations (Continuous)} \\ -----: T_{\text{V}_{\text{J}}} \!\! = \!\! 175 \, ^{\circ}\text{C (Unusual load operations (Limited period)}$

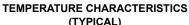


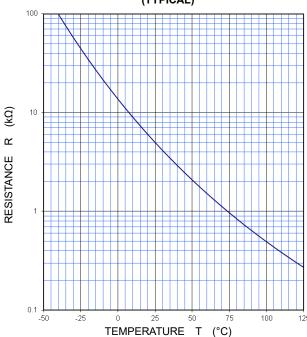
SHORT-CIRCUIT SAFE OPERATING AREA (MAXIMUM)

 $V_{CC} \le 400 \text{ V}$, $R_G = 0.47 \sim 30 \Omega$, $V_{GE} = \pm 15 \text{ V}$, $T_{vj} = 25 \sim 150 \text{ °C}$, $t_W \le 8 \mu \text{s}$, Non-Repetitive



NTC thermistor part





Note: The characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

HIGH POWER SWITCHING USE INSULATED TYPE

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HIGH POWER SWITCHING USE INSULATED TYPE

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