

<IGBT Modules>

CM300DY-34T

HIGH POWER SWITCHING USE INSULATED TYPE



dual switch (half-bridge)

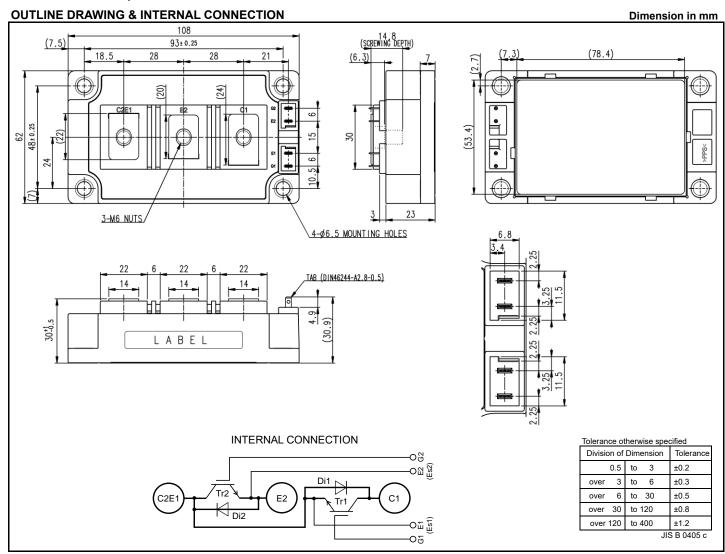
- ●Flat base type
- Nickel-plating tab terminals
- •RoHS Directive compliant
- •UL Recognized under UL1557, File No.E323585

APPLICATION

AC Motor Control, Motion/Servo Control, Power supply, etc.

OPTION (Below options are available.)

- ●PC-TIM (Phase Change Thermal Interface Material) pre-apply (Note8)
- •VcEsat selection for parallel connection



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MAXIMUM RATINGS (T_{vj} =25 °C, unless otherwise specified)

Symbol	Item	Conditions	Rating	Unit
V _{CES}	Collector-emitter voltage	G-E short-circuited	1700	V
V_{GES}	Gate-emitter voltage	C-E short-circuited	± 20	V
Ic	Callantan assument	DC, T _C =134 °C* (Note2, 4)	300	^
I _{CRM}	Collector current	Pulse, Repetitive (Note3)	600	_ A
P _{tot}	Total power dissipation	T _C =25 °C (Note2, 4)	3405	W
I _E (Note1)	Fitt	DC (Note2)	300	^
I _{ERM} (Note1)	Emitter current	Pulse, Repetitive (Note3)	600	_ A
V _{isol}	Isolation voltage	Terminals to base plate, RMS, f=60 Hz, AC 1 min	4000	V
T _{vjmax}	Maximum junction temperature	Instantaneous event (overload) (Note8)	175	°C
T _{Cmax}	Maximum case temperature	(Note4,8)	150*	°C
T _{vjop}	Operating junction temperature	Continuous operation (under switching) (Note8)	-40 ~ +150	- °C
T _{stg}	Storage temperature	-	-40 ~ +150*	

ELECTRICAL CHARACTERISTICS (Tvj=25 °C, unless otherwise specified)

Symbol	Item	Conditions		Limits			Unit
Syllibol	item	Conditions		Min.	Min. Typ. Max.	Max.	Unit
I _{CES}	Collector-emitter cut-off current	V _{CE} =V _{CES} , G-E short-circuited		-	-	1.0	mA
I _{GES}	Gate-emitter leakage current	V _{GE} =V _{GES} , C-E short-circuited		-	-	0.5	μA
$V_{GE(th)}$	Gate-emitter threshold voltage	I _C =30 mA, V _{CE} =10 V		5.4	6.0	6.6	V
		I _C =300 A, V _{GE} =15 V,	T _{vj} =25 °C	-	2.05	2.50	V
V _{CEsat}		Refer to the figure of test circuit	T _{vj} =125 °C	-	2.45	-	
(Terminal)		(Note5)	T _{vi} =150 °C	-	2.55	-	
	Collector-emitter saturation voltage	I _C =300 A,	T _{vi} =25 °C	-	1.95	2.35	
V _{CEsat}		V _{GE} =15 V,	T _{vi} =125 °C	-	2.35	-	V
(Chip)		(Note5)	T _{vj} =150 °C	-	2.45	-	
Cies	Input capacitance		,	-	-	82.5	nF
Coes	Output capacitance	V _{CE} =10 V, G-E short-circuited		-	-	2.2	
Cres	Reverse transfer capacitance	-		-	-	0.7	1
Q _G	Gate charge	V _{CC} =1000 V, I _C =300 A, V _{GE} =15 V		-	2.35	-	μC
t _{d(on)}	Turn-on delay time	V_{CC} =1000 V, I _C =300 A, V_{GE} =±15 V, R _G =0 Ω, Inductive load		-	-	800	- ns
t _r	Rise time			-	-	200	
t _{d(off)}	Turn-off delay time			-	-	800	
t _f	Fall time			-	-	600	1
	/EC (Note.1) Terminal) Emitter-collector voltage Chip)	I _E =300 A, G-E short-circuited,	T _{vj} =25 °C	-	2.75	3.35	V
		Refer to the figure of test circuit	T _{vj} =125 °C	-	3.00	-	
(Terminal)		(Note5)	T _{vj} =150 °C	-	3.00	-	
		I _E =300 A,	T _{vj} =25 °C	-	2.65	3.20	
		G-E short-circuited,	T _{vj} =125 °C	-	2.75	-	V
(Cnip)		(Note5)	T _{vj} =150 °C	-	2.75	-	
t _{rr} (Note1)	Reverse recovery time	V _{CC} =1000 V, I _E =300 A, V _{GE} =±15 V,		-	-	300	ns
Q _{rr} (Note1)	Reverse recovery charge	$R_G=0 \Omega$, Inductive load		-	15	-	μC
Eon	Turn-on switching energy per pulse	V _{CC} =1000 V, I _C =I _E =300 A, V _{GE} =±15 V, R _G =0 Ω, T _{vj} =150 °C,		-	58.5	-	1
E _{off}	Turn-off switching energy per pulse			-	72.8	-	mJ
E _{rr} (Note1)	Reverse recovery energy per pulse	Inductive load		-	40.6	-	mJ
R _{CC'+EE'}	Internal lead resistance	Main terminals-chip, per switch, T _C =25 °C (Note4)		-	0.3	-	mΩ
r _g	Internal gate resistance	Per switch		-	2.5	-	Ω

^{*:} PC-TIM applied module is restricted by the heat resistant temperature of PC-TIM.

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THERMAL RESISTANCE CHARACTERISTICS

Symbol	Itam	Conditions	Limits			Unit
	Item	Conditions	Min.	Тур.	Max.	Offic
$R_{th(j-c)Q}$	Thermal resistance	Junction to case, per Inverter IGBT (Note4)	-	-	44.0	K/kW
$R_{th(j-c)D}$	Thermal resistance	Junction to case, per Inverter FWD (Note4)	-	-	67.7	N/KVV
R _{th(c-s)}	Contact thermal resistance	Case to heat sink, per 1 module Thermal grease applied (Note4,6,8)	-	13.3	-	K/kW

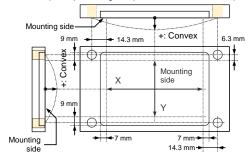
MECHANICAL CHARACTERISTICS

Symbol	lá a ma	Limits			l lmit		
	Item	Conditions		Min.	Тур.	Max.	Unit
Mt	Mounting torque	Main terminals	M 6 screw	3.5	4.0	4.5	N·m
Ms	Mounting torque	Mounting to heat sink	M 6 screw	3.5	4.0	4.5	N·m
۵	Creepage distance	Terminal to terminal		17.3	-	-	mm
ds		Terminal to base plate		25.3	-	-	
۵	Clearance	Terminal to terminal		12.6	-	-	mana
d _a	Clearance	Terminal to base plate		21.8	-	-	mm
ec	Flatness of base plate	On the centerline X, Y (Note7)		±0	-	+200	μm
m	mass	-		-	260	-	g

^{*.} This product is compliant with the Restriction of the Use of Certain Hazardous Substances in Electrical and Electronic Equipment (RoHS) directive 2011/65/EU and (EU) 2015/863.

Note1. Represent ratings and characteristics of the anti-parallel, emitter-collector free-wheeling diode (FWD).

- 2. Junction temperature (T_{vj}) should not increase beyond T_{vjmax} rating.
- 3. Pulse width and repetition rate should be such that the device junction temperature (T_{vi}) dose not exceed T_{vimax} rating.
- 4. Case temperature (T_C) and heat sink temperature (T_S) are defined on the each surface (mounting side) of base plate and heat sink just under the chips. Refer to the figure of chip location.
- 5. Pulse width and repetition rate should be such as to cause negligible temperature rise. Refer to the figure of test circuit.
- 6. Typical value is measured by using thermally conductive grease of λ =3.0 W/(m·K)/D_(C-S)=50 μ m.
- 7. The base plate (mounting side) flatness measurement points (X, Y) are shown in the following figure.



8. Long term performance related to thermal conductive grease and PC-TIM (including but not limited to aspects such as the increase of thermal resistance due to pumping out, etc.) should be verified under your specific application conditions. Each temperature condition (T_{vj max}, T_{vj op}, T_{C max}) must be maintained below the maximum rated temperature throughout consideration of the temperature rise even for long term usage.

HIGH POWER SWITCHING USE

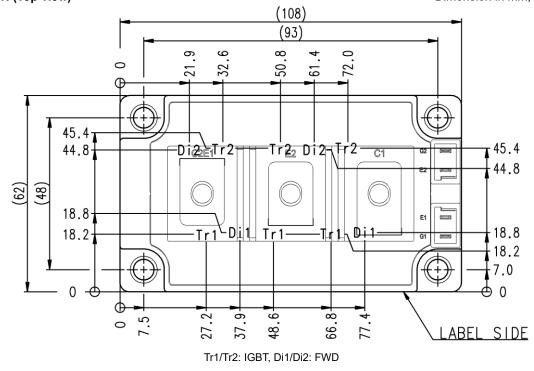
INSULATED TYPE

RECMENDED OPERATING CONDITIONS

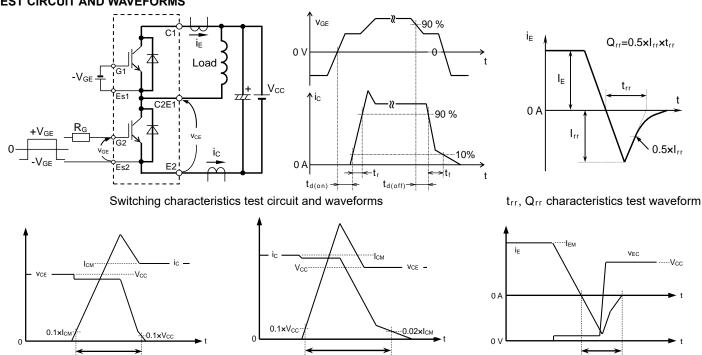
Symbol	Item Condition	Conditions	Limits			Unit
	item	Conditions	Min.	Тур.	Max.	Offic
V _{CC}	(DC) Supply voltage	Applied across C1-E2 terminals	-	1000	1200	V
V_{GEon}	Gate (-emitter drive) voltage	Applied across G1-Es1/G2-Es2 terminals	13.5	15.0	16.5	V
R _G	External gate resistance	Per switch	0	-	16	Ω

CHIP LOCATION (Top view)

Dimension in mm, tolerance: ±1 mm



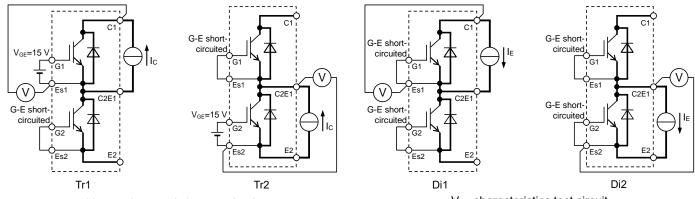
TEST CIRCUIT AND WAVEFORMS



IGBT Turn-off switching energy Turn-on / Turn-off switching energy and Reverse recovery energy test waveforms (Integral time instruction drawing)

TEST CIRCUIT

IGBT Turn-on switching energy

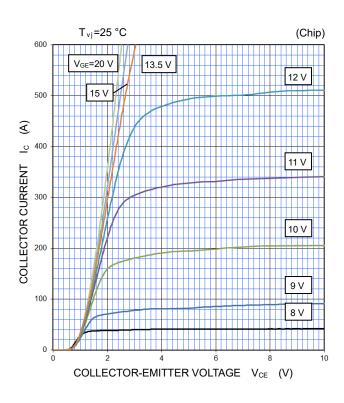


V_{CEsat} characteristics test circuit

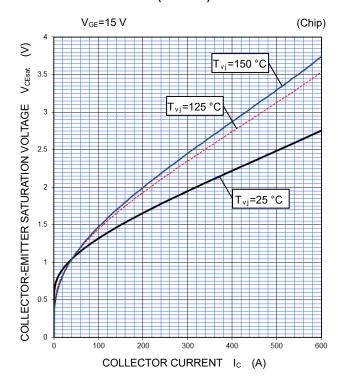
V_{EC} characteristics test circuit

FWD Reverse recovery energy

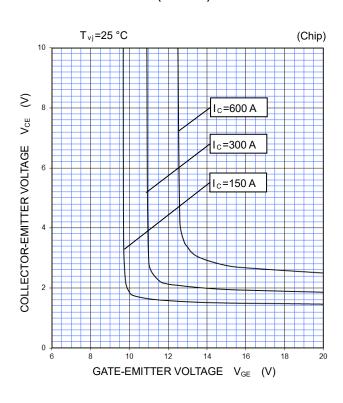
OUTPUT CHARACTERISTICS (TYPICAL)



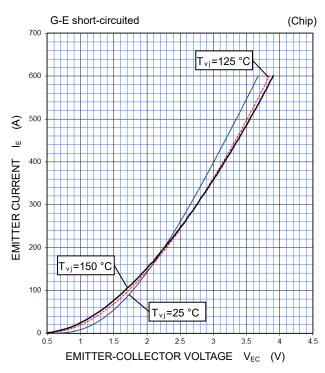
COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)



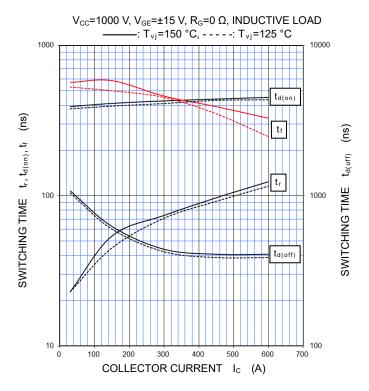
COLLECTOR-EMITTER VOLTAGE CHARACTERISTICS (TYPICAL)



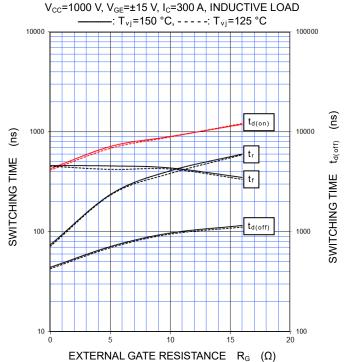
FREE WHEELING DIODE FORWARD CHARACTERISTICS (TYPICAL)



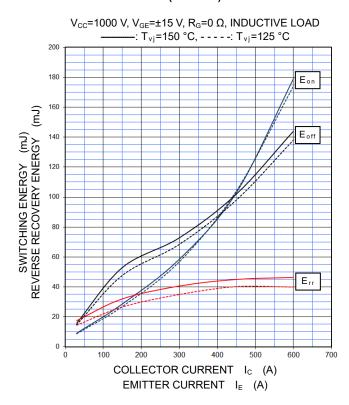
HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)



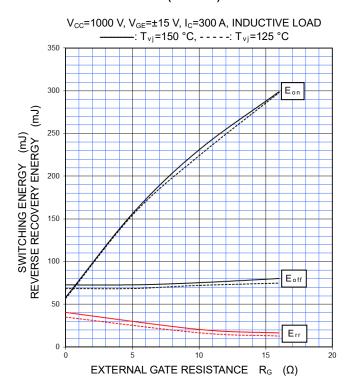
HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)



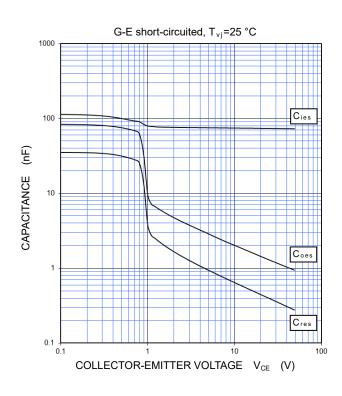
HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)



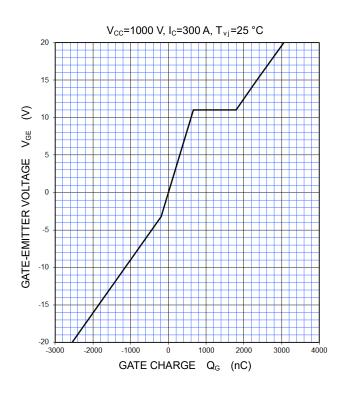
HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)



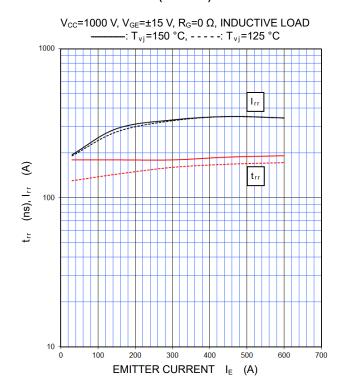
CAPACITANCE CHARACTERISTICS (TYPICAL)



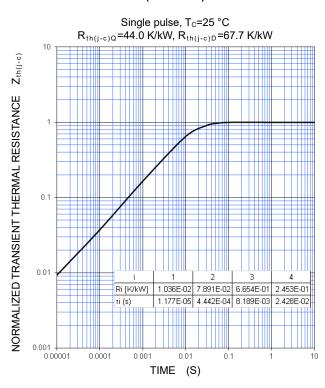
GATE CHARGE CHARACTERISTICS (TYPICAL)



FREE WHEELING DIODE REVERSE RECOVERY CHARACTERISTICS (TYPICAL)

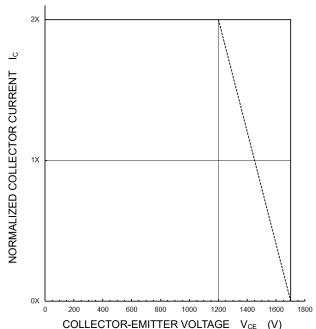


TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS (MAXIMUM)



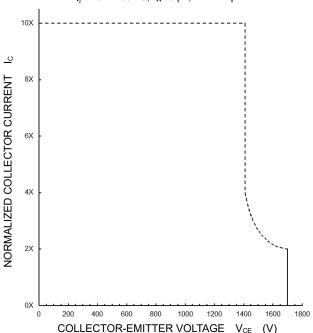
TURN-OFF SWITCHING SAFE OPERATING AREA (REVERSE BIAS SAFE OPERATING AREA) (MAXIMUM)

 V_{CC} ≤1200 V, V_{GE} =±15 V, R_{G} =0~16 Ω, ———: $T_{v_{i}}$ =25~150 °C (Normal load operations (Continuous) -----: $T_{v_{j}}$ =175 °C (Unusual load operations (Limited period)



SHORT-CIRCUIT SAFE OPERATING AREA (MAXIMUM)

 V_{CC} ≤1200 V, V_{GE} =±15 V, R_{G} =0~16 Ω, T_{vj} = 25 ~ 150 °C, t_{W} ≤8 μs, Non-Repetitive



Note: The characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

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