

<IGBT Modules>

CM600DY-24T

HIGH POWER SWITCHING USE **INSULATED TYPE**



dual switch (half-bridge)

Collector current I_C Maximum junction temperature T_{vjmax}

Flat base type

14.8 (<u>screwing depth</u>)

(6.3)

- Nickel-plating tab terminals
- •RoHS Directive compliant
- •UL Recognized under UL1557, File No.E323585

APPLICATION

(7.5)

AC Motor Control, Motion/Servo Control, Power supply, etc.

OPTION (Below options are available.)

●PC-TIM (Phase Change Thermal Interface Material) pre-apply (Note8)

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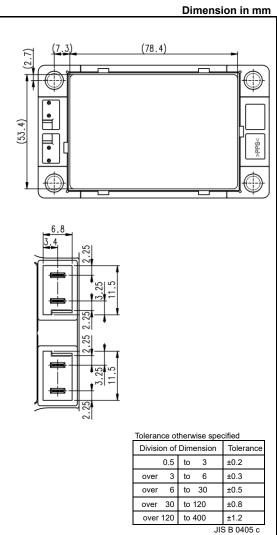
93±0.25

•Vcesat selection for parallel connection

18.5



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MAXIMUM RATINGS (Tvj=25 °C, unless otherwise specified)

Symbol	Item	Conditions	Rating	Unit
V _{CES}	Collector-emitter voltage	G-E short-circuited	1200	V
V _{GES}	Gate-emitter voltage	C-E short-circuited	± 20	V
Ic	Callacter assument	DC, T _C =144 °C* (Note2, 4)	600	^
I _{CRM}	Collector current	Pulse, Repetitive (Note3)	1200	Α
P _{tot}	Total power dissipation	T _C =25 °C (Note2, 4)	6250	W
I _E (Note1)	Fitt	DC (Note2)	600	^
I _{ERM} (Note1)	Emitter current	Pulse, Repetitive (Note3)	1200	Α
V _{isol}	Isolation voltage	Terminals to base plate, RMS, f=60 Hz, AC 1 min	4000	V
T _{jmax}	Maximum junction temperature	Instantaneous event (overload) (Note8)	175	°C
T _{Cmax}	Maximum case temperature	(Note4,8)	150*	°C
T _{jop}	Operating junction temperature	Continuous operation (under switching) (Note8)	-40 ~ +150	°C
T _{stg}	Storage temperature	-	-40 ~ +150*	°C

ELECTRICAL CHARACTERISTICS (Tvj=25 °C, unless otherwise specified)

Symbol	Item	Conditions			Limits		Unit
Symbol	item	Conditions		Min.	Тур.	Max.	Unit
I _{CES}	Collector-emitter cut-off current	V _{CE} =V _{CES} , G-E short-circuited		-	-	1.0	mA
I _{GES}	Gate-emitter leakage current	V _{GE} =V _{GES} , C-E short-circuited		-	-	0.5	μA
$V_{GE(th)}$	Gate-emitter threshold voltage	I _C =60 mA, V _{CE} =10 V		5.4	6.0	6.6	V
		I _C =600 A, V _{GE} =15 V,	T _{vj} =25 °C	-	1.75	2.05	V
V _{CEsat}		Refer to the figure of test circuit	T _{vj} =125 °C	-	2.00	-	
(Terminal)	Callegter emitter esturation valtage	(Note5)	T _{vj} =150 °C	-	2.10	-	
.,	Collector-emitter saturation voltage	I _C =600 A,	T _{vj} =25 °C	-	1.55	1.80	
V _{CEsat}		V _{GE} =15 V,	T _{vj} =125 °C	-	1.75	-	V
(Chip)		(Note5)	T _{vj} =150 °C	-	1.80	-	
Cies	Input capacitance					123	
Coes	Output capacitance	V _{CE} =10 V, G-E short-circuited		-	-	3.6	nF
Cres	Reverse transfer capacitance		-	-	1.5		
Q _G	Gate charge	Vcc=600 V, Ic=600 A, VgE=15 V		-	3.7	-	μC
t _{d(on)}	Turn-on delay time	V_{CC} =600 V, I _C =600 A, V_{GE} =±15 V, R_{G} =1.0 Ω, Inductive load		-	-	500	- ns
t _r	Rise time			-	-	200	
t _{d(off)}	Turn-off delay time			-	-	600	
t _f	Fall time			-	-	300	1
		I _E =600 A, G-E short-circuited,	T _{vj} =25 °C	-	1.85	2.25	V
V _{EC} (Note.1)		Refer to the figure of test circuit	T _{vj} =125 °C	-	2.00	-	
(Terminal)	Forth and the state of the stat	(Note5)	T _{vj} =150 °C	-	2.00	-	1
	Emitter-collector voltage	I _E =600 A,	T _{vj} =25 °C	-	1.65	2.00	
V _{EC} (Note.1)	.1)	G-E short-circuited,	T _{vj} =125 °C	-	1.65	-	V
(Chip)		(Note5)	T _{vj} =150 °C	-	1.65	-	1
t _{rr} (Note1)	Reverse recovery time	V _{CC} =600 V, I _E =600 A, V _{GE} =±15 V,	1	-	-	400	ns
Q _{rr} (Note1)	Reverse recovery charge	R_G =1.0 Ω, Inductive load		-	60	-	μC
Eon	Turn-on switching energy per pulse	V _{CC} =600 V, I _C =I _E =600 A, V _{GE} =±15 V, R _G =1.0 Ω, T _{Vj} =150 °C,		-	56.6	-	
E _{off}	Turn-off switching energy per pulse			-	64.3	-	mJ
E _{rr} (Note1)	Reverse recovery energy per pulse	Inductive load		-	38.2	-	mJ
R _{CC'+EE'}	Internal lead resistance	Main terminals-chip, per switch, T _C =25 °C (Note4)		-	0.3	-	mΩ
r _g	Internal gate resistance	Per switch		-	0.67	-	Ω

^{*:} The value of PC-TIM applied module is limited by the heat resistant temperature of PC-TIM.

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THERMAL RESISTANCE CHARACTERISTICS

Symbol	Itama	Conditions	Limits			Unit
	Item	Conditions	Min.	Тур.	Max.	Offic
$R_{th(j-c)Q}$	Thermal resistance	Junction to case, per Inverter IGBT (Note4)	-	-	24	K/kW
$R_{th(j-c)D}$	THEITIALTESISTATICE	Junction to case, per Inverter FWD (Note4)	1	1	42	N/KVV
R _{th(c-s)}	Contact thermal resistance	Case to heat sink, per 1 module Thermal grease applied (Note4,6,8)	-	13.3	-	K/kW

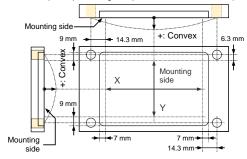
MECHANICAL CHARACTERISTICS

Symbol	lá a ma	Limits			l lmi4		
	Item	Conditions		Min.	Тур.	Max.	Unit
M _t	Mounting torque	Main terminals	M 6 screw	3.5	4.0	4.5	N·m
Ms	Mounting torque	Mounting to heat sink	M 6 screw	3.5	4.0	4.5	N·m
ds	Cracan and distance	Terminal to terminal		17.3	-	-	m.m.
	Creepage distance	Terminal to base plate		25.3	-	-	mm
da	Clearance	Terminal to terminal		12.6	-	-	mm
	Clearance	Terminal to base plate		21.8	-	-	
ec	Flatness of base plate	On the centerline X, Y (Note7)		±0	-	+200	μm
m	mass	-		-	260	-	g

^{*.} This product is compliant with the Restriction of the Use of Certain Hazardous Substances in Electrical and Electronic Equipment (RoHS) directive 2011/65/EU and (EU) 2015/863.EU.

Note1. Represent ratings and characteristics of the anti-parallel, emitter-collector free-wheeling diode (FWD).

- 2. Junction temperature (T_{vj}) should not increase beyond T_{vjmax} rating.
- 3. Pulse width and repetition rate should be such that the device junction temperature (T_{vi}) dose not exceed T_{vimax} rating.
- 4. Case temperature (T_C) and heat sink temperature (T_S) are defined on the each surface (mounting side) of base plate and heat sink just under the chips. Refer to the figure of chip location.
- 5. Pulse width and repetition rate should be such as to cause negligible temperature rise. Refer to the figure of test circuit.
- 6. Typical value is measured by using thermally conductive grease of λ =3.0 W/(m·K)/D_(C-S)=50 μ m.
- 7. The base plate (mounting side) flatness measurement points (X, Y) are shown in the following figure.



8. Long term performance related to thermal conductive grease and PC-TIM (including but not limited to aspects such as the increase of thermal resistance due to pumping out, etc.) should be verified under your specific application conditions. Each temperature condition (T_{vj max}, T_{vj op}, T_{C max}) must be maintained below the maximum rated temperature throughout consideration of the temperature rise even for long term usage.

HIGH POWER SWITCHING USE

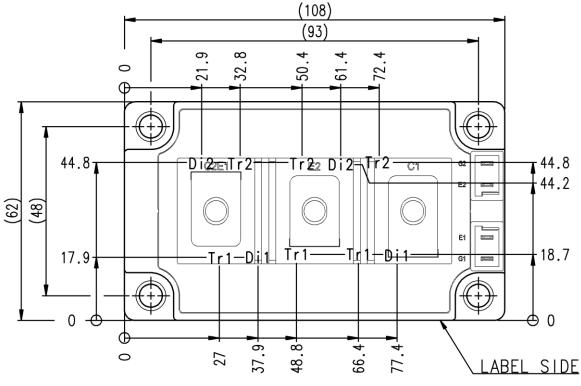
INSULATED TYPE

RECMENDED OPERATING CONDITIONS

Symbol	Itam	Conditions Min.	Limits	Unit		
	Item		Min.	Тур.	Max.	Unit
V _{cc}	(DC) Supply voltage	Applied across C1-E2 terminals	-	600	850	V
V_{GEon}	Gate (-emitter drive) voltage	Applied across G1-Es1/G2-Es2 terminals	13.5	15.0	16.5	V
R _G	External gate resistance	Per switch	1.0	-	10	Ω

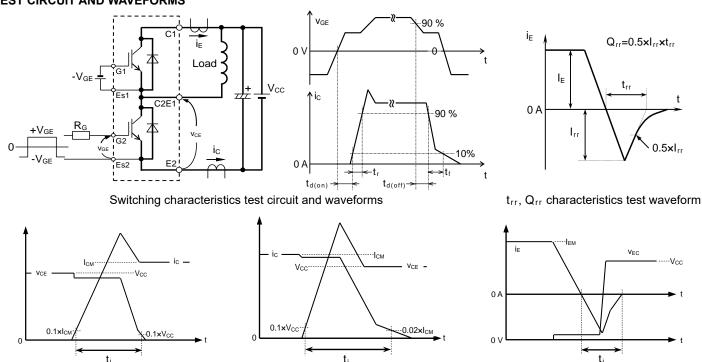
CHIP LOCATION (Top view)

Dimension in mm, tolerance: ±1 mm



Tr1/Tr2: IGBT, Di1/Di2: FWD

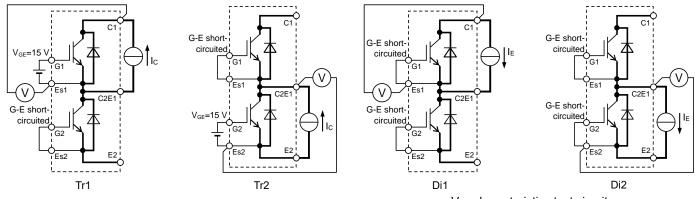
TEST CIRCUIT AND WAVEFORMS



IGBT Turn-off switching energy Turn-on / Turn-off switching energy and Reverse recovery energy test waveforms (Integral time instruction drawing)

TEST CIRCUIT

IGBT Turn-on switching energy

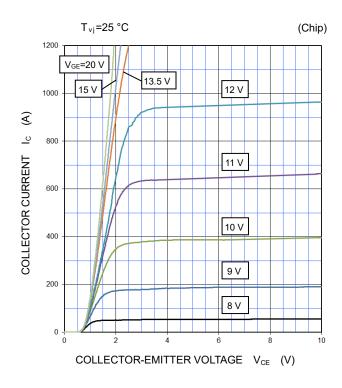


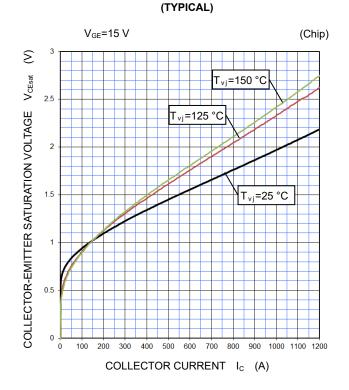
V_{CEsat} characteristics test circuit

V_{EC} characteristics test circuit

FWD Reverse recovery energy

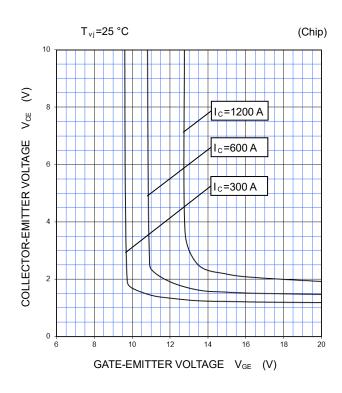
OUTPUT CHARACTERISTICS (TYPICAL)



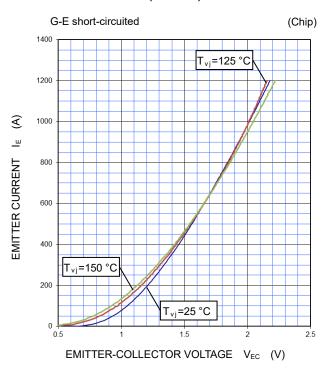


COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS

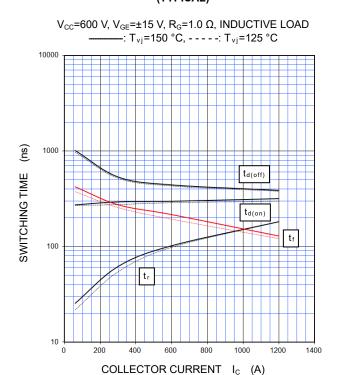
COLLECTOR-EMITTER VOLTAGE CHARACTERISTICS (TYPICAL)



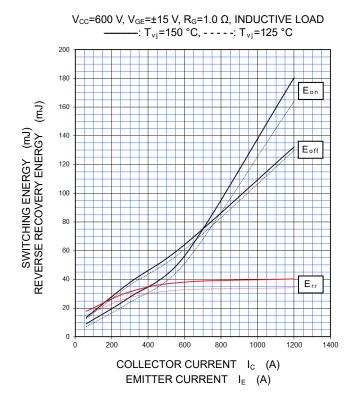
FREE WHEELING DIODE FORWARD CHARACTERISTICS (TYPICAL)



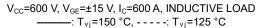
HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

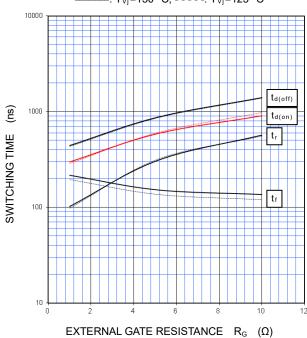


HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

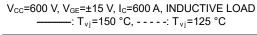


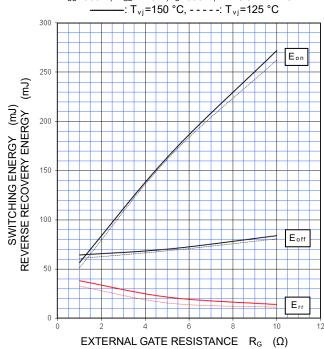
HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)



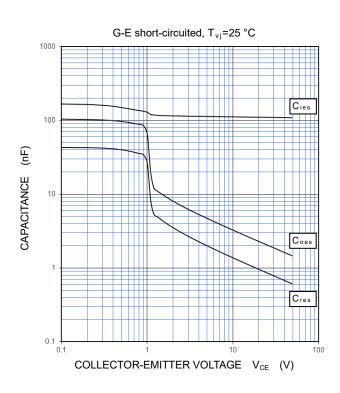


HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

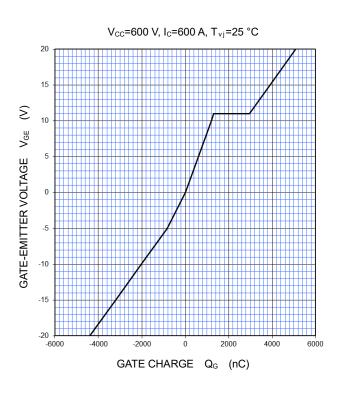




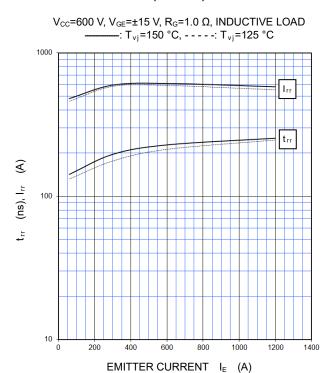
CAPACITANCE CHARACTERISTICS (TYPICAL)



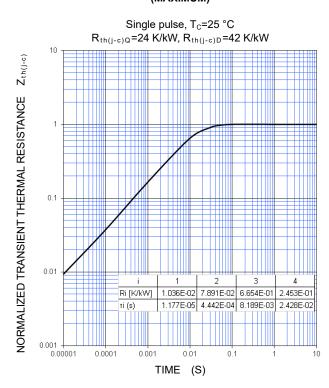
GATE CHARGE CHARACTERISTICS (TYPICAL)



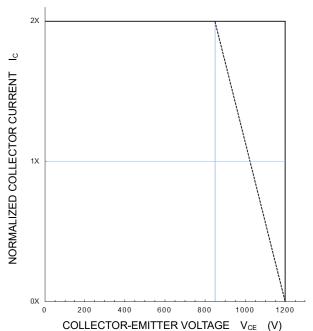
FREE WHEELING DIODE REVERSE RECOVERY CHARACTERISTICS (TYPICAL)



TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS (MAXIMUM)

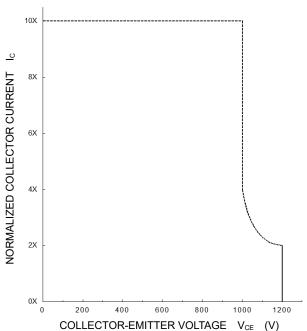


TURN-OFF SWITCHING SAFE OPERATING AREA (REVERSE BIAS SAFE OPERATING AREA) (MAXIMUM)



SHORT-CIRCUIT SAFE OPERATING AREA (MAXIMUM)

 $V_{\text{CC}} \leq 800 \text{ V}$, $V_{\text{GE}} = \pm 15 \text{ V}$, $R_{\text{G}} = 1.0 \sim 10 \Omega$, $T_{\text{vj}} = 25 \sim 150 \, ^{\circ}\text{C}$, $t_{\text{W}} \leq 8 \, \mu\text{s}$, Non-Repetitive



Note: The characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

HIGH POWER SWITCHING USE INSULATED TYPE

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HIGH POWER SWITCHING USE INSULATED TYPE

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