

<Hybrid-SiC Modules>

CMH300DX-24NFH

HIGH POWER SWITCHING USE INSULATED TYPE



dual switch (Half-Bridge)

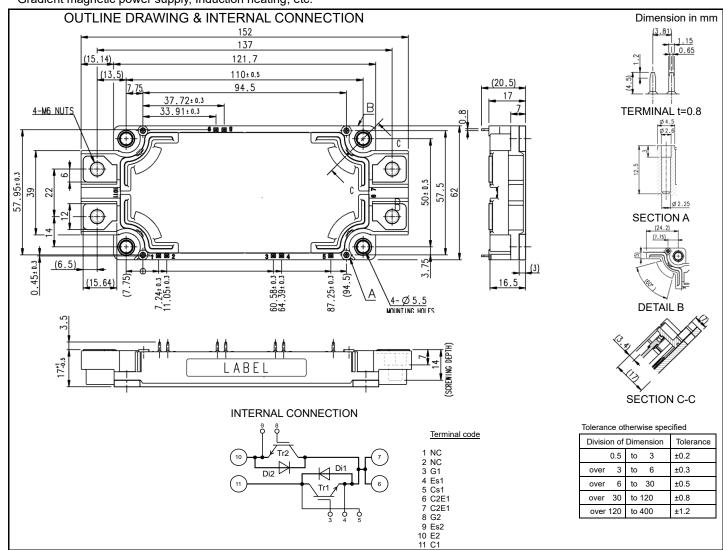
- •Silicon IGBT + Silicon Carbide Schottky Barrier Diode
- •Flat base Type
- Copper base plate
- RoHS Directive compliant
- •Recognized under UL1557, File E323585

APPLICATION

High frequency switching use(30kHz to 60kHz)

Publication Date: December 2020

Gradient magnetic power supply, Induction heating, etc.



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INSULATED TYPE

MAXIMUM RATINGS (T_j =25 °C, unless otherwise specified, per 1/2 module)

Symbol	Item	Conditions	Rating	Unit	
V _{CES}	Collector-emitter voltage	G-E short-circuited	1200	V	
V_{GES}	Gate-emitter voltage	C-E short-circuited	± 20	V	
Ic	Collector current	DC, T _C =25 °C (Note2, 4)	300	^	
I _{CRM}	Collector current	Pulse, Repetitive (Note3)	600	Α	
P _{tot}	Total power dissipation	T _C =25 °C (Note2, 4)	1890	W	
I _E (Note1)	F	DC, T _C =25 °C (Note2, 4)	300	Δ.	
I _{ERM} (Note1)	Emitter current	Pulse, Repetitive (Note3)	600	Α	
V _{isol}	Isolation voltage	Terminals to base plate, RMS, f=60 Hz, AC 1 min	4000	V	
T _j	Junction temperature	_ (Note9)	-40 ~ +150	°C	
T _{stg}	Storage temperature	-	-40 ~ +125	C	

ELECTRICAL CHARACTERISTICS (T_j=25 °C, unless otherwise specified, per 1/2 module)

Cumbal	Item	Conditions -		Limits			Unit
Symbol	Item			Min.	Тур.	Max.	Offic
I _{CES}	Collector-emitter cut-off current	V _{CE} =V _{CES} , G-E short-circuited		-	-	11.0	mA
I _{GES}	Gate-emitter leakage current	V _{GE} =V _{GES} , C-E short-circuited		-	-	1.0	μA
$V_{GE(th)}$	Gate-emitter threshold voltage	I _C =30 mA, V _{CE} =10 V		4.5	6.0	7.5	V
	0-11-4	I _C =300 A, V _{GE} =15 V (Note5)	T _j =25 °C	-	5.0	6.5	
V _{CEsat}	Collector-emitter saturation voltage	Refer to the figure of test circuit	T _j =125 °C	-	5.0	-	V
Cies	Input capacitance		•	-	-	47	nF
Coes	Output capacitance	V _{CE} =10 V, G-E short-circuited		-	-	5.6	
Cres	Reverse transfer capacitance			-	-	1.1	
Q _G	Gate charge	V _{CC} =600 V, I _C =300 A, V _{GE} =15 V		-	1360	-	nC
t _{d(on)}	Turn-on delay time	- V _{CC} =600 V, I _C =300 A, V _{GE} =±15 V,			-	300	
t _r	Rise time			-	-	80]
$t_{d(off)}$	Turn-off delay time	B 400 Industrial lead		-	-	500	ns
t _f	Fall time	R_G =1.0 Ω, Inductive load		-	-	150	
V (Note1)	Foreitte and a life of a month of	I _E =300 A, G-E short-circuited (Note5)	T _j =25 °C		2.2	2.7	V
V _{EC} (Note1)	Emitter-collector voltage	Refer to the figure of test circuit	T _j =125 °C		2.9	-	V
Q _C (Note1)	Collector - emitter charge	V_{CC} =600 V, I_E =300 A, V_{GE} =±15 V, R_G =1.0 Ω , Inductive load		-	2.8	-	μC
Eon	Turn-on switching energy per pulse	V _{CC} =600 V, I _C /I _E =300 A,		-	5.0	-	
E _{off}	Turn-off switching energy per pulse	V _{GE} =±15 V, R _G =1.0 Ω,		-	10.0	-	mJ
E _{rec} (Note1)	Reverse energy per pulse	T _j =125 °C, Inductive load		-	1.0	-	mJ
r _g	Internal gate resistance	Per switch		-	0.8	-	Ω

THERMAL RESISTANCE CHARACTERISTICS (per 1/2 module)

Symbol	Item	Conditions	Limits			Unit
	item	Conditions	Min.	Тур.	Max.	Offic
$R_{th(j-c)Q}$	Thermal resistance	Junction to case (Note4)	-	-	0.066	K/W
$R_{th(j-c)D}$		Junction to case (Note4)	-	-	0.245	
$R_{th(c-s)}$	Contact thermal resistance	Case to heat sink, Thermal grease applied (Note4, 6, 9)	-	0.015	-	K/W

Caution; No short-circuit capability is designed.

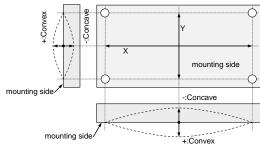
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MECHANICAL CHARACTERISTICS

Symbol	Item	Conditions	Conditions		Limits		
		Conditions			Тур.	Max.	Unit
Mt	Mounting torque	Main terminals	M 6 screw	3.5	4.0	4.5	N·m
Ms	Mounting torque	Mounting to heat sink	M 5 screw	2.5	3.0	3.5	N·m
ds	Creepage distance	Terminal to terminal		17.0	-	-	mm
		Terminal to base plate		18.5	-	-	
da	01	Terminal to terminal		10.0	-	-	
	Clearance	Terminal to base plate		16.3	-	-	mm
m	mass	-		-	350	-	g
e _c	EL	On the centerline X (Note7)		0	-	100	
	Flatness of base plate	On the centerline Y (Note7)		0	-	100	μm

- *: This product is compliant with the Restriction of the Use of Certain Hazardous Substances in Electrical and Electronic Equipment (RoHS) directive 2011/65/EU and (EU) 2015/863.
- Note1. Represent ratings and characteristics of the anti-parallel, emitter-collector free-wheeling diode (DIODE).
 - 2. Junction temperature (T_j) should not increase beyond T_{jmax} rating.
 - 3. Pulse width and repetition rate should be such that the device junction temperature (T_j) dose not exceed $T_{j\,m\,a\,x}$ rating.
 - 4. Case temperature (T_C) and heat sink temperature (T_s) are defined on the each surface (mounting side) of base plate and heat sink just under the chips. Refer to the figure of chip location.
 - 5. Pulse width and repetition rate should be such as to cause negligible temperature rise.
 - 6. $B_{(25/50)} = ln(\frac{R_{25}}{R_{50}})/(\frac{1}{T_{25}} \frac{1}{T_{50}})$ R_{25} : resistance at absolute temperature T_{25} [K]; T_{25} =25 [°C]+273.15=298.15 [K] R_{50} : resistance at absolute temperature T_{50} [K]; T_{50} =50 [°C]+273.15=323.15 [K]
 - 7. Typical value is measured by using thermally conductive grease of λ =0.9 W/(m·K).
 - 8. The base plate (mounting side) flatness measurement points (X, Y) are as follows of the following figure.



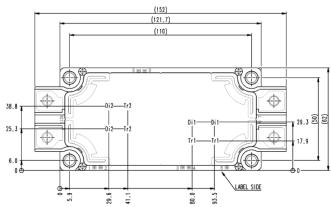
9. Long term performance related to thermal conductive material such as thermal grease (including but not limited to aspects such as the increase of thermal resistance due to pumping out, etc.) should be verified under your specific application conditions. Temperature condition (Tj) must be maintained below the maximum rated temperature throughout consideration of the temperature rise even for long term usage.

RECOMMENDED OPERATING CONDITIONS

TEGOMMENTED OF ENATING CONDITIONS							
Symbol	Item	Conditions	Limits			Unit	
	Symbol	item	Conditions	Min.	Тур.	Max.	Unit
V _{cc}	(DC) Supply voltage	Applied across C1-E2 terminals	-	600	800	V	
V_{GEon}	Gate (-emitter drive) voltage	Applied across G1-Es1/G2-Es2 terminals	13.5	15.0	16.5	V	
R_G	External gate resistance	Per switch	1.0	-	10	Ω	

CHIP LOCATION (Top view)

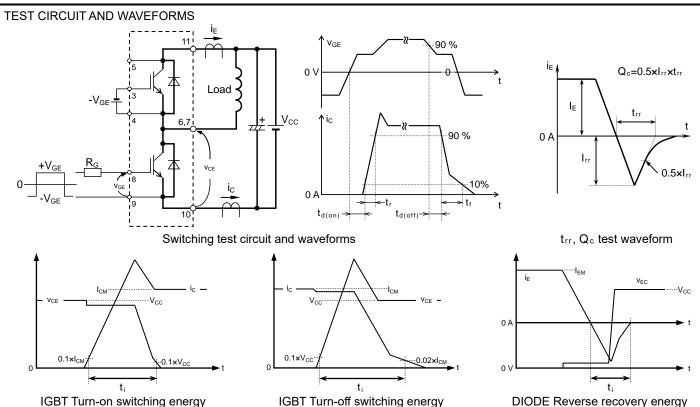
Dimension in mm, tolerance: ±1 mm



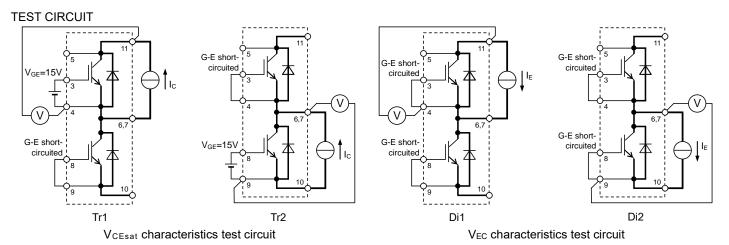
Tr1/Tr2: IGBT, Di1/Di2: DIODE

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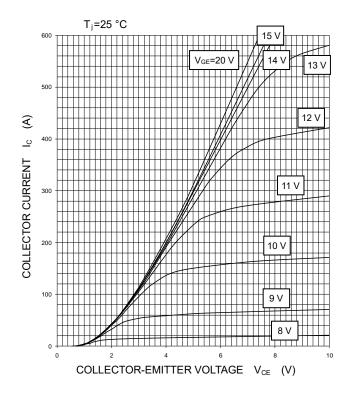
Turn-on / Turn-off switching energy and Reverse recovery energy test waveforms (Integral time instruction drawing)



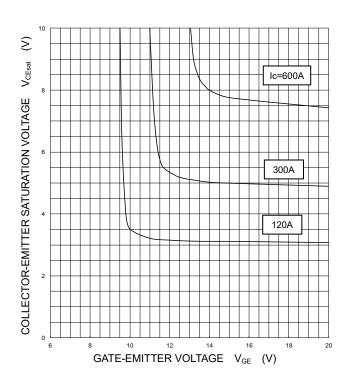
HIGH POWER SWITCHING USE INSULATED TYPE

PERFORMANCE CURVES

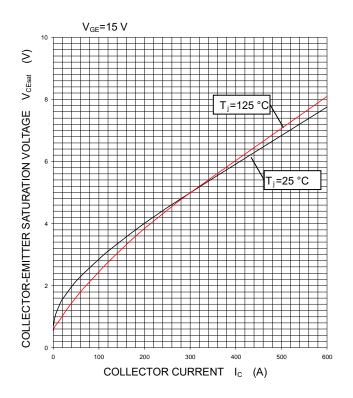
OUTPUT CHARACTERISTICS (TYPICAL)



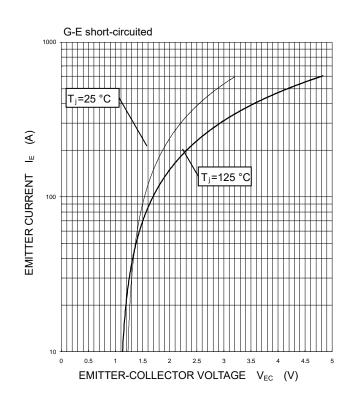
COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)



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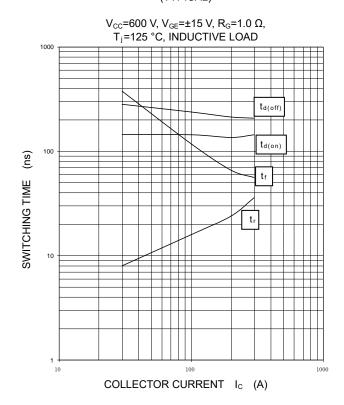
FREE WHEELING DIODE FORWARD CHARACTERISTICS (TYPICAL)



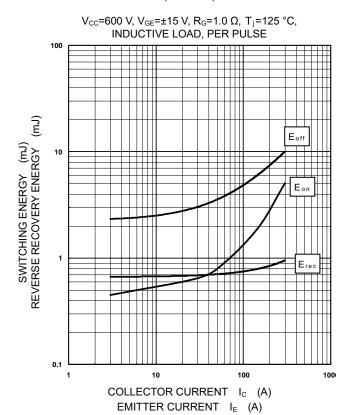
HIGH POWER SWITCHING USE INSULATED TYPE

PERFORMANCE CURVES

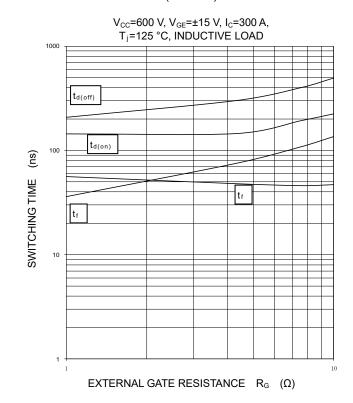
HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)



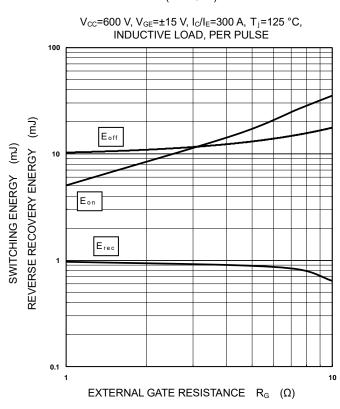
HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)



HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)



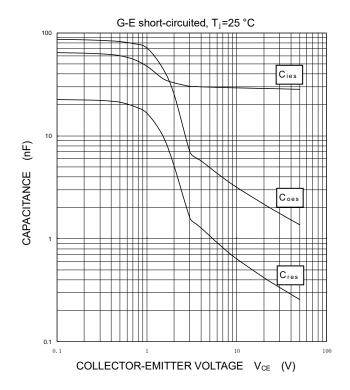
HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)



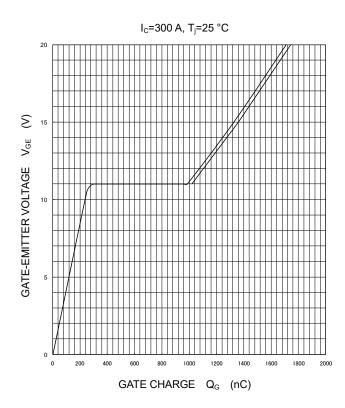
HIGH POWER SWITCHING USE INSULATED TYPE

PERFORMANCE CURVES

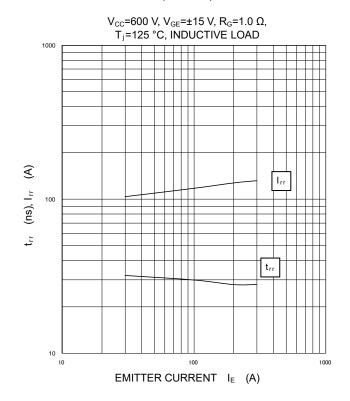
CAPACITANCE CHARACTERISTICS (TYPICAL)



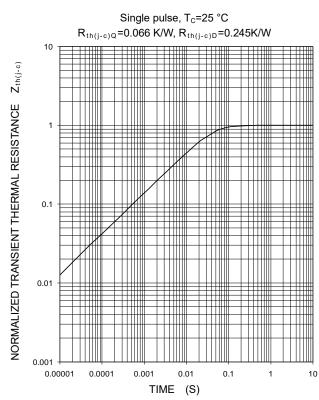
GATE CHARGE CHARACTERISTICS (TYPICAL)



FREE WHEELING DIODE REVERSE RECOVERY CHARACTERISTICS (TYPICAL)



TRANSIENT THERMAL IMPEDANCE CHARACTERISTIC S (MAXIMUM)



Note: The characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

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